

M72/M78 "HOP" AUDIO

M72 EVT

CHANGE LIST (CHANGES FROM M51)

- 23OCT06
 1. CHANGED CODEC TO QFN, ADDED 4 AND 5 PIN FLAT SPEAKER CONNECTORS.
 2. ADDED 3 PIN MICROPHONE CONNECTOR
 3. DELETED OPTIONAL STUFF PATH FOR LINE INPUT TO CDIN OF CODEC, ADDED VARIOUS I/O LABELS
- 24OCT06
 4. DELETED C6517, C6501, C6617, C6601 (TOO LARGE CAPS ON SPEAKER AMPLIFIERS)
 5. CHANGED R6822/R6890 TO 220UF FROM 470UF (NO NEED FOR THAT MUCH BULK)
 6. DELETED R6822/R6890 AS THEY ARE UNNEEDED IN SINGLE CHANNEL MIC CKT
 7. CHANGED CODEC AVDD CAPS TO 22UF (NOT DRIVING A HIGH CURRENT LOAD)
 8. ADDED TP72501 VOLTAGE REGULATOR TO GENERATE 1.5V AVDD
 9. REMOVED R6670/R6670 0.005 OHM RESISTORS THAT WERE EXPENSIVE AND NO LONGER NEEDED
 10. REPLACED C6502, C6602 WITH MORE APPROPRIATE CAPACITORS (35380608) AS THE OTHERS WERE RARE
- 15NOV06
 11. DELETED ALL FERRITE BEADS ON OUTPUT SIDE OF T FILTER
 12. DELETED 4 PIN STATIC ZAP DIODES
 13. REPLACED ALL OUTPUT T FILTER CAPACITORS WITH VARISTERS (100PF CAP PLUS ZAP PROTECTION)
 14. CHANGED LEFT TRANSISTORS TO SOT-163 FROM SOT-163 FOR SMALLER PACKAGE
 15. CHANGED TOP AND BOTTOM RESISTORS FOR LI DETECT SO THAT T WILL OPERATE PROPERLY
- 28NOV06
 16. CHANGED BOARD TO BOARD CONNECTOR TO APN 516S0564 (22 PIN)
 17. ADDED SYSTEM LED
 18. ADDED DECOUPLING AROUND BRD TO BRD CONNECTOR
 19. CHANGED BOARD TO BOARD CONNECTOR TO PADS WHICH IS APN 998-1569
- 29NOV06 AUDIOHW/SW/EFT DESIGN REVIEW HELD (1PM SONNY ROLLINS)
 20. CHANGED L6201/L6202 TO CHEAPER 220 OHM FERRITE BEADS
 21. ADDED NET NAMES TO U6200 PINS 1, U6201 PINS 1, 5, 8, ADDED AUD_ TO BEEP, JDRF, VREF ON PAGE 62
 22. CHANGED R6308 TO 28K ON PAGE 63
 23. CHANGED R6410 TO 10 OHMS. CHANGED NOTES FROM LINE OUTPUT TO HEADPHONES ON PAGE 64
 24. CHANGED L6506, L6507, L6508, L6605, L6606, L6607, L6608 TO 0402 PACKAGES PAGES 65 AND 66
 25. CHANGED ALL CHASSIS NET NAMES TO THE SAME (GND CHASSIS AUDIO EXTERNAL) PAGE 67
 26. CHANGED NET NAME ON J6702 PINS 3 TO NC. ADDED CHASSIS GND'S TO PINS 3 & J6701, PINS 6, 7, J6702
 27. NOSTUFFED DZ6702 AND DZ6705 AS THEY SHOULD NEVER BE NEEDED DUE TO LOCATION OF PIN DEEP INSIDE CONNECTOR
 28. CHANGED REFERENCES TO LINE OUTPUT ON PAGE 68 TO BE REFERENCES TO HEADPHONES
- 6DEC06
 29. FIXED ONE SINGLE PIN NET ON CHASSIS GND NEAR XM6700
 30. ADDED MIN LINE WIDTH PROPERTY TO AUD LO GND'S
 31. DELETED ALL EXCESSIVE (+) SIGNS TO ELIMINATE AMBIGUITY
 32. FIXED THE MIN NECK/MIN LINE WIDTH PROPERTIES ON GND
 33. COMBINED 12V POWER AND GROUND FOR BOTH SPEAKER AMPLIFIERS TO AID IN PLANE POURING AND ROUTING
- 7DEC06
 34. FIXED A COUPLE OF LINE WIDTH MINIMUMS FOR ROUTING
- 8DEC06
 35. ADDED M72/M78 AUDIO IN/OUT CONNECTORS
 36. CHANGED R6303/08 TO 100K FROM 28K AS WE DO NOT NEED THE PULL DOWN FUNCTION FOR DETECT
 37. ADDED R6720/L6781 TO LI TYPE DETECT LINE AS WE ARE NOW GOING TO USE IT
 38. CHANGED NET NAME FOR LI DET. TO END WITH L
 39. DELETED DETECT TRANSISTORS AS THE CONNECTORS WILL NOW TAKE THAT FUNCTIONALITY
- 11DEC06
 40. ADDED STANDOFFS, FIXED NET PROPERTIES IN HEADPHONES CIRCUIT
- 12DEC06
 41. MODIFIED PORT CONNECTIONS TO MATCH M75/M76 WHERE POSSIBLE
 42. MODIFIED SOME MIN LINE/MIN NECK WIDTHS ON GROUNDS FOR I/O
- 13DEC06
 43. ADDED GPIO1 FUNCTIONALITY FOR M72/M78 SELECTION
 44. VREF_A NOW MUTES LINE OUTPUT
- 14DEC06
 45. CHANGED J9900 TO 998-1580 (20 PIN VERSION)
- 15DEC06
 46. ADDED RESISTOR STUFF OPTIONS TO PLATED HOLES TO CONNECT TO RELEVANT GROUNDS
 47. CHANGED MICROPHONE SHIELD SHORT TO 0 OHM RESISTOR IN CASE WE NEED TO REMOVE A GROUND LOOP
 48. ADDED PROPER HOLES FOR AUDIO BOARD (850-0893)
 49. CHANGED PINOUT OF 20 PIN CONNECTOR TO REDUCE CROSSTALK BETWEEN SIGNALS
- 18DEC06
 50. ADDED PROPER SIZED HOLES
 51. CHANGED 220UF CAPS FROM 126S0087 TO HIGHER TEMP, 126S0110 (ALSO ADDS A SUPPLIER FOR GSM)
 52. CHANGED C6300, C6301, C6304, C6305 TO COMPLIANT 127S0050 CAPS
 53. ADDED 128S0106 AS ALTERNATE FOR C6202, C6203
 54. CHANGED L6505/06/07/08, L6605/06/07/08 TO APN 155S0137 TO MATCH PARTS ON OTHER PAGES (SAME PART WITH DIFFERENT PART NUMBER)
- 10JAN07
 55. FOUND SERIOUS ERRORS IN DETECT CIRCUITRY, REDESIGNED DETECTS
- 11JAN07
 56. CHANGED J6701/J6704 TO VERTICAL CONNECTORS PER PD
- 23JAN07
 57. CHANGED U6200 TO LOPP
 58. ADDED 20 PIN ZIP CONNECTOR J9901, HOOKED UP
 59. DELETED ZT9901
- 24JAN07
 60. PLACED QFN CODEC BACK ONTO SCHEMATIC (U6200)
 61. MODIFIED PIN OUT FOR J9900 TO PLACE DGND BETWEEN TWO POWERS
 62. CHANGED PULL DOWN RESISTORS ON OUTPUT OF MAX922 TO 2.4K FROM 1K TO AID IN THD+N
- 29JAN07
 63. CHANGED O6500 TO MATCH O6800-O6802 (BOM COMSOLIDATION)
 64. DELETED R6205 AND NOW SPDIF NET (NOT REQ'D PER DEREK DICARLO AND LAURA METZ)
 65. DELETED R6217 AND NO CONNECTED GPIO_1 (NOT REQ'D PER RADAR 4880571)
- 29JAN07
 65. DELETED R6217 AND NO CONNECTED GPIO_1 (NOT REQ'D PER RADAR 4880571)
- 9 FEB 07
 66. UPDATED PORT LABEL TABLE TO INCLUDE VREF_A AS MUTE FOR HEADPHONES AMPLIFIER
 67. CHANGED L6280 TO 220 OHM FERRITE (SAME AS M70/75/76) AND CHANGED C6202/C6203 TO 150 UF FROM 33UF TO IMPROVE THD+N ON HEADPHONES OUTPUT.
- 13 FEB 07
 68. ADDED DUAL ANALOG MIC SUPPORT
- 13 MAR 07
 69. CHANGED VREF CAP ON CODEC (C6205) TO 3.3UF FROM 10UF TO ENSURE THAT THE VREF IS FULLY CHARGED WHEN SPKR AND HP AMPS ARE UNMUTED
 70. CHANGED HP COUPLING CAPS (C6400, C6401) TO 3.3UF FROM 10UF TO REDUCE POP
 71. ADDED NO-STUFFED R6854 TO CONNECT L AND R CHANNELS TOGETHER FOR MONO MIC IMPLEMENTATION
- 14 MAR 07
 71. BACKED OUT CHANGES FOR MULTIPLE MICROPHONE SUPPORT ON THIS SCHEMATIC (MOVED CHANGES TO NEW 951-0619 TEST SCHEMATIC.


AUDIO: CODEC

SYNC_MASTER=AUDIO SYNC_DATE=08/04/2006

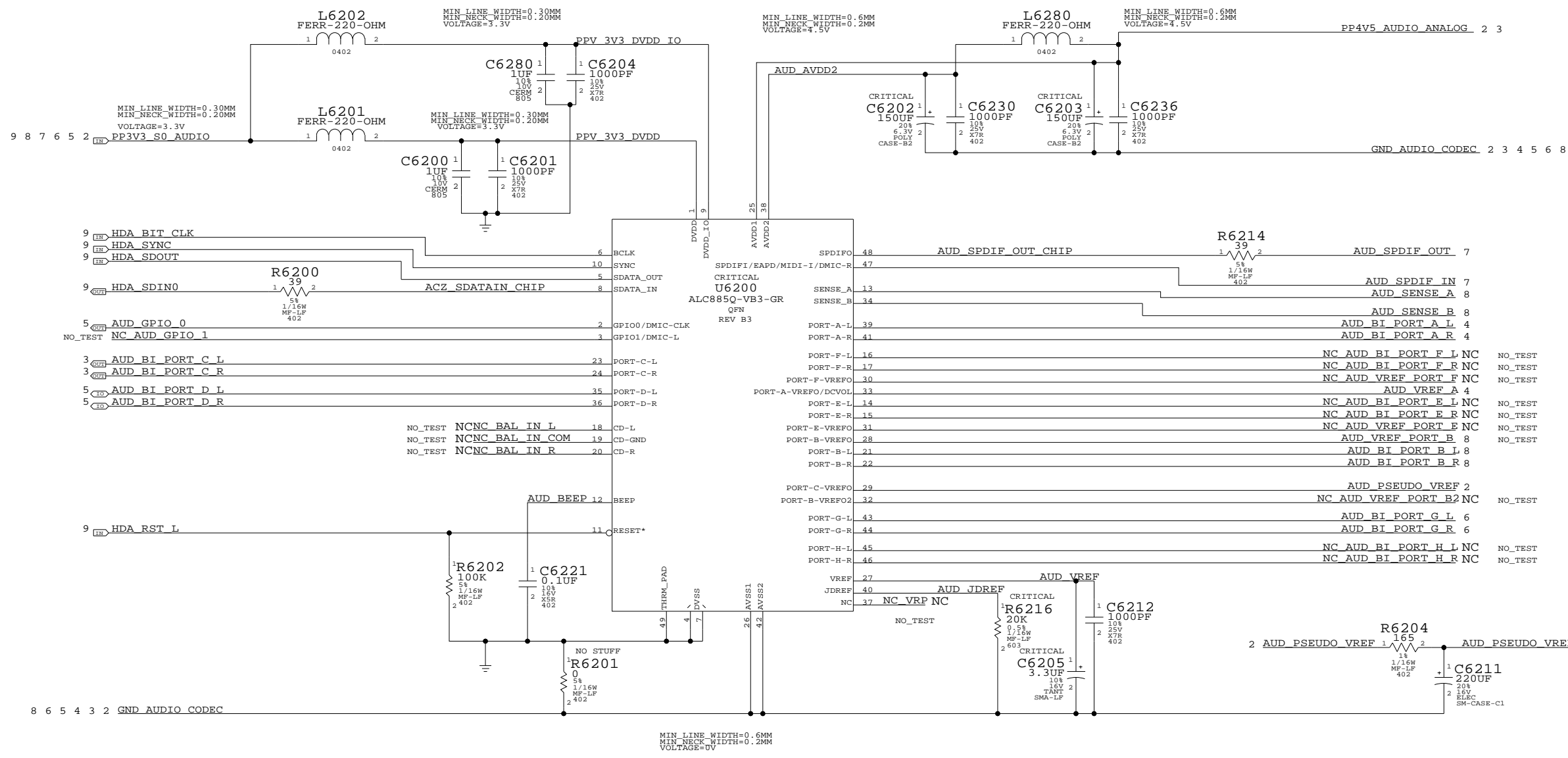
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NONE	1	109	

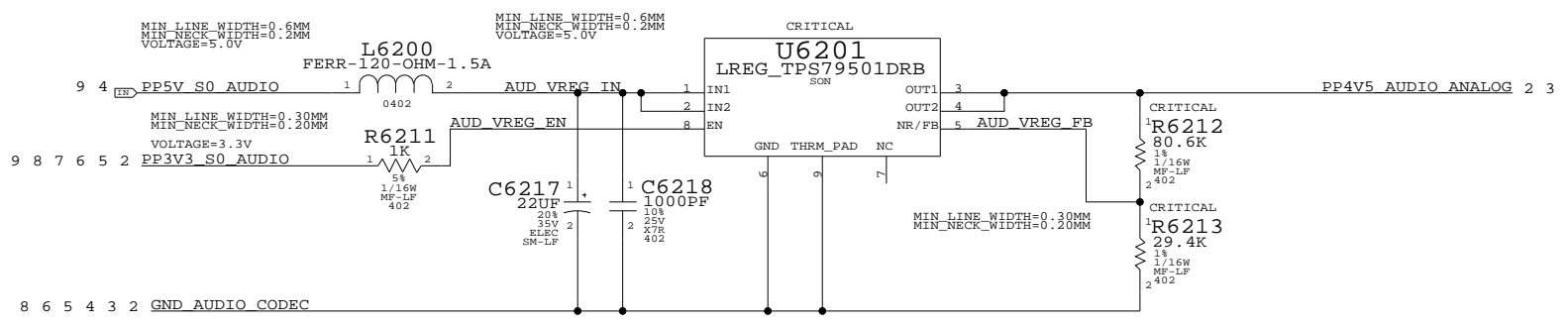
AUDIO CODEC APPLE P/N 353S1538



4.5V POWER SUPPLY FOR CODEC AND LINE IN AMP
 $V_{OUT} = 1.2246 \times (1 + (80.6K/29.4K)) = 4.58V$

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS
12880106	12880079		C6202,C6203	GSM REQUEST

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
820-2136	1	PCB, FAB, AUDIO, M72/M78	MLB1		



AUDIO: CODEC
 SYNC_MASTER=AUDIO SYNC_DATE=08/04/2006
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	SCALE NONE	SHEET 62 OF 109	

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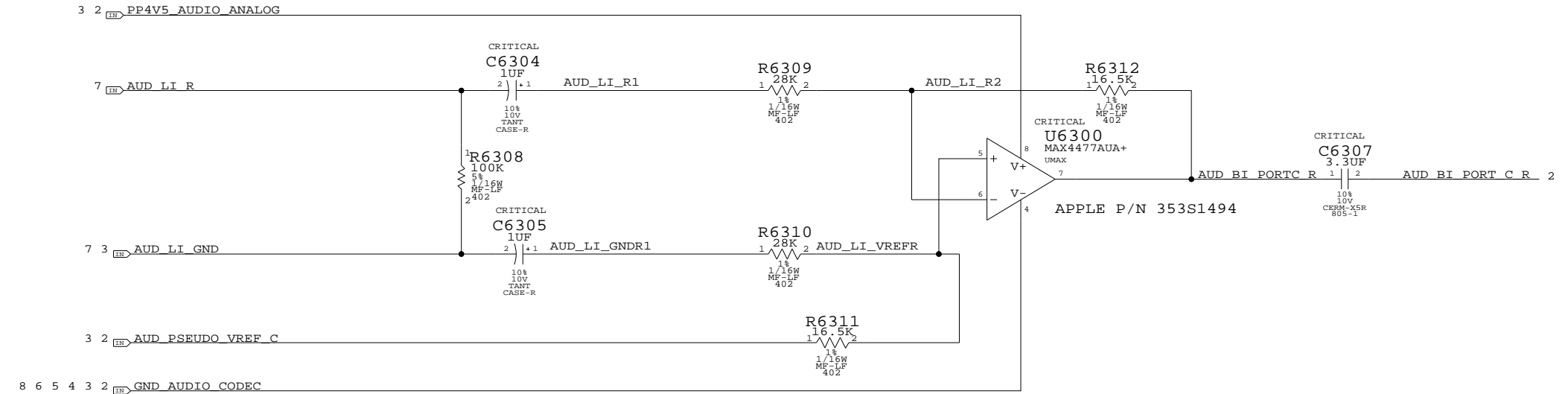
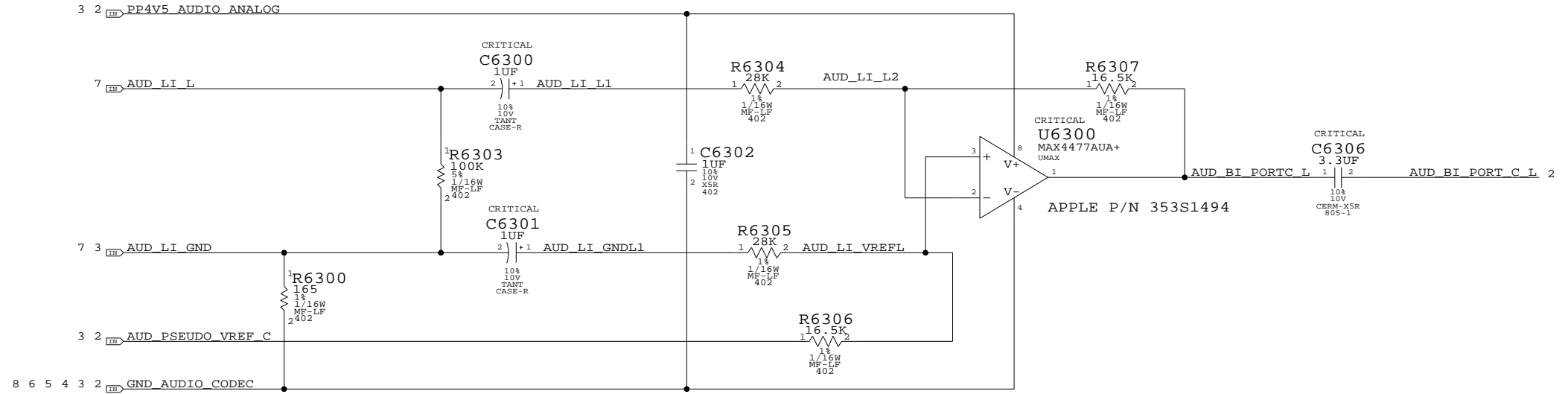
3

2

1

LINE IN PSEUDO-DIFFERENTIAL AMP

AV = 0.59
FC = 5.7 HZ



AUDIO: LINE INPUT AMP

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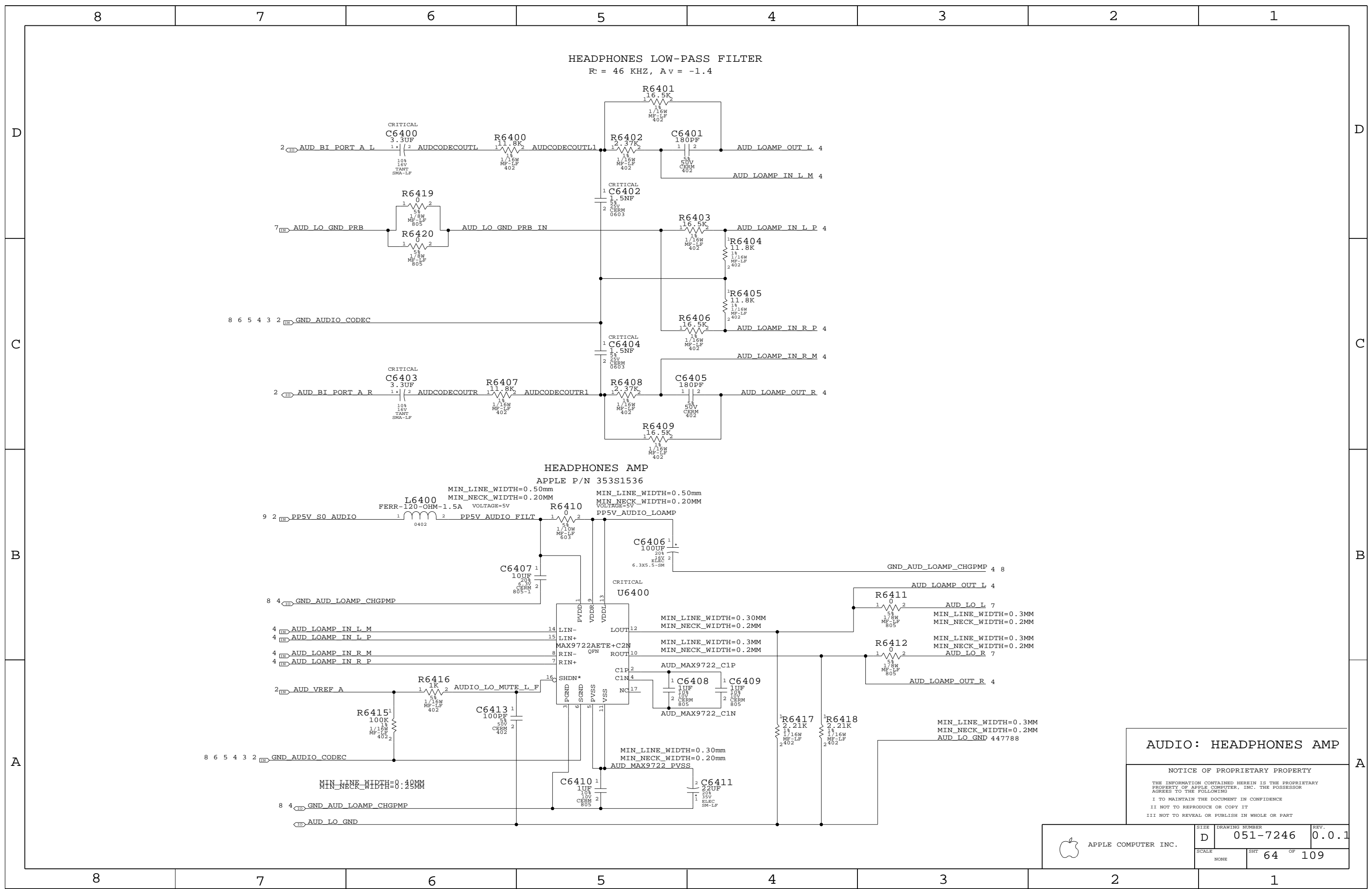
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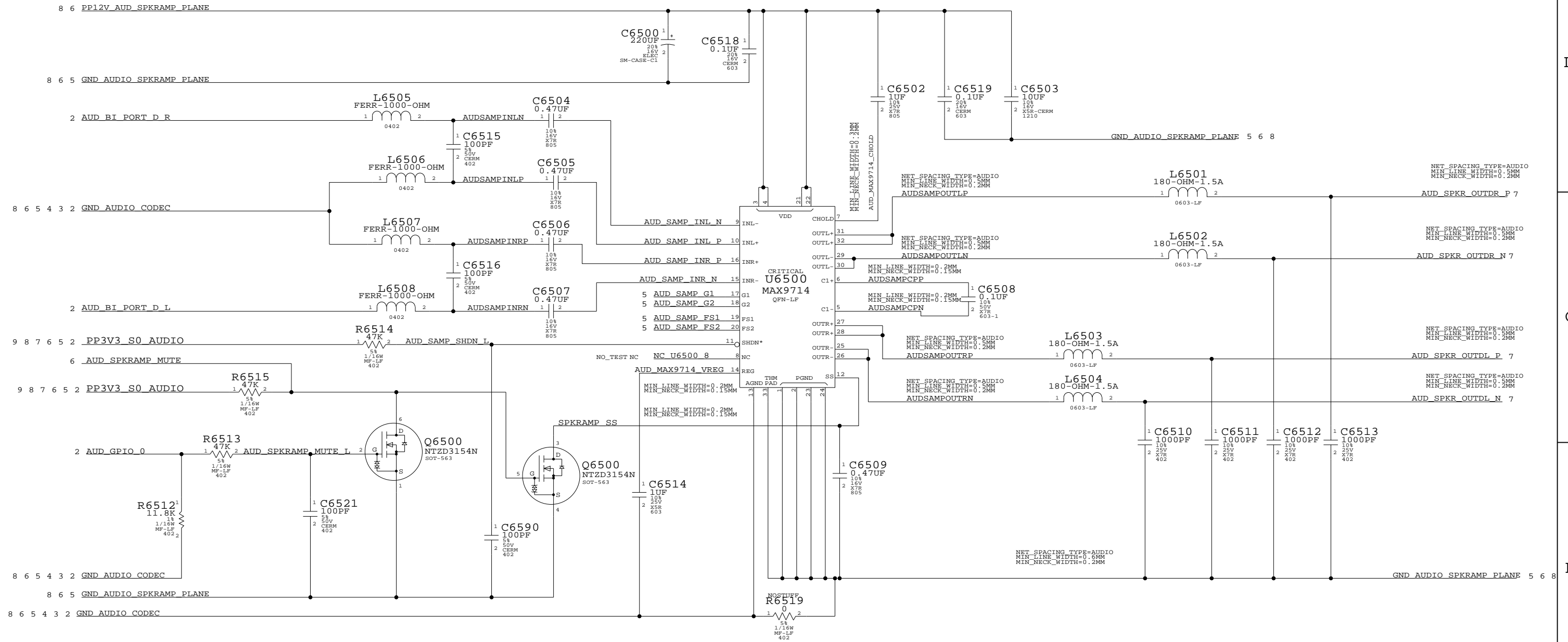
AUDIO: HEADPHONES AMP

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SCALE	NONE	SHT	64 OF 109

SPEAKER AMP
APPLE P/N 353S1156

NET_SPACING_TYPE=AUDIO
MIN_LINE_WIDTH=0.6MM
MIN_NECK_WIDTH=0.2MM
VOLTAGE=12V



GAIN SETTINGS: +16DB
MODULATION SETTING: LOW EMI
GAIN AND SWITCHING FREQUENCY STUFF OPTIONS

9 8 7 6 5 2 PP3V3_S0_AUDIO

5 AUD_SAMP_FS2
5 AUD_SAMP_FS1
5 AUD_SAMP_G2
5 AUD_SAMP_G1

8 6 5 GND_AUDIO_SPKRAMP_PLANE

AUDIO: SPEAKER AMP_1

SYNC_MASTER=AUDIO SYNC_DATE=08/04/2006

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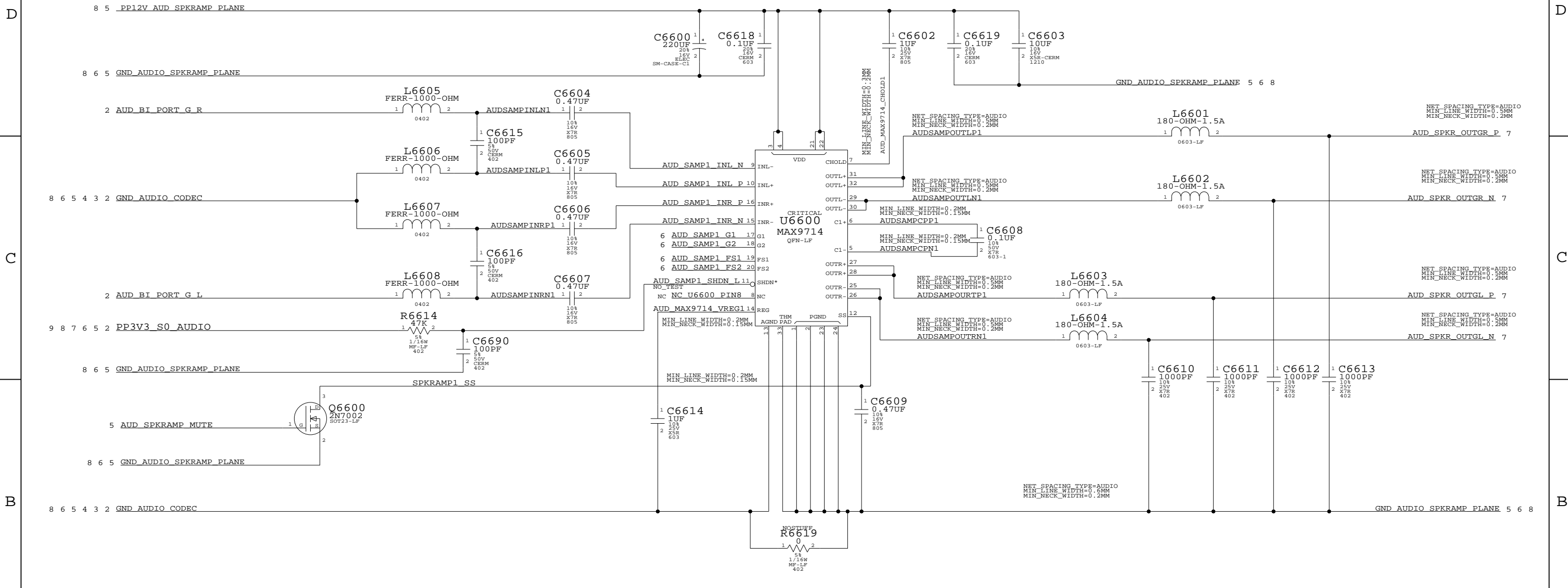


APPLE COMPUTER INC.

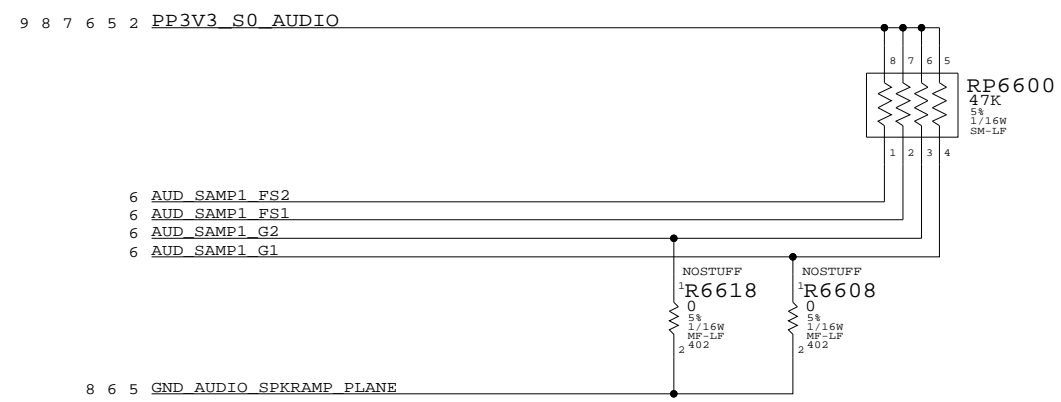
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D	051-7246	0.0.1
SCALE	SHT	OF
NONE	65	109

SPEAKER AMP
APPLE P/N 353S1156

NET SPACING TYPE=AUDIO
MIN_LINE_WIDTH=0.6MM
MIN_NECK_WIDTH=0.2MM
VOLTAGE=1.2V



GAIN SETTINGS: +16DB
MODULATION SETTING: LOW EMI
GAIN AND SWITCHING FREQUENCY STUFF OPTIONS

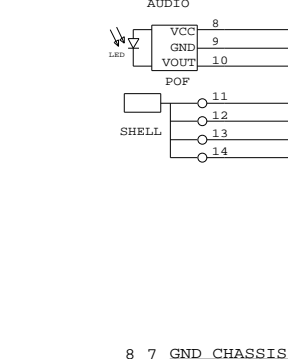
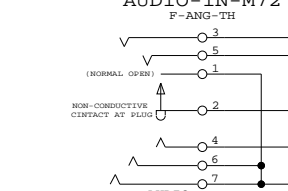


AUDIO: SPEAKER AMP
SYNC_MASTER=AUDIO SYNC_DATE=08/04/2006
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SCALE	SHT 66 OF 109		
NONE			

COMBO IN JACK
APPLE P/N 514-0402

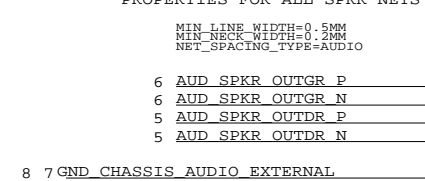
CRITICAL



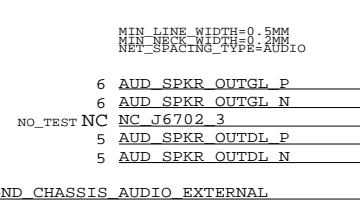
8 7 GND_CHASSIS AUDIO IO

SPEAKER CABLE CONNECTORS
APPLE P/N 518S0191
APPLE P/N 518S0326

PROPERTIES FOR ALL SPKR NETS

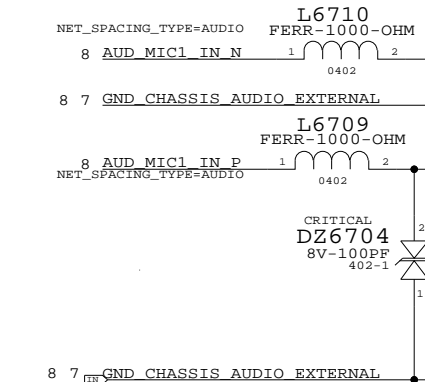


PROPERTIES FOR ALL SPKR NETS



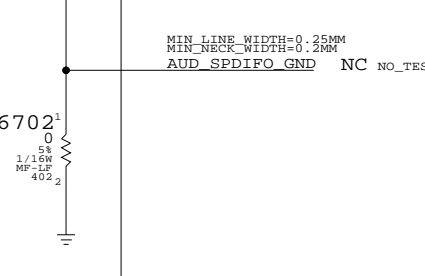
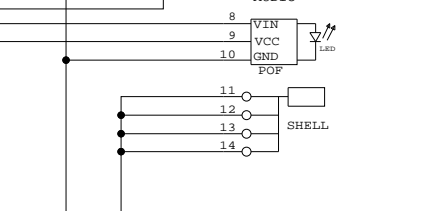
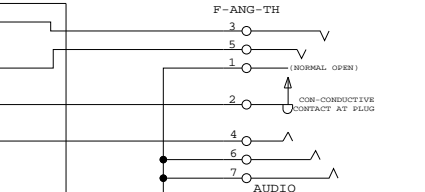
INTERNAL MIC CON
APPLE P/N 518S0314

PROPERTIES FOR ALL SPKR NETS



LINE OUT JACK
APPLE P/N 514-0403

CRITICAL



8 7 GND_CHASSIS AUDIO IO

AUDIO: CONNECTORS

SYNC_MASTER=AUDIO SYNC_DATE=08/04/2006
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NONE	67	109	

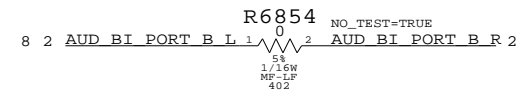
CODEC OUTPUT SIGNAL PATHS

FUNCTION	VOLUME	DAC	PIN COMPLEX	MUTE CONTROL
HEADPHONES	0X0D	0X03	0X15 (A)	VREF A
SPKR AMP (M72/M78)	0X0C	0X02	0X14 (D)	GPIO 0
SPKR AMP (M72/M78)	0X0E	0X04	0X16 (G)	GPIO 0
SPDIFOUT		CONVERTER=0X06	PIN=0X1E	
		DETECT DELEGATE PIN	0X1BH (E)	

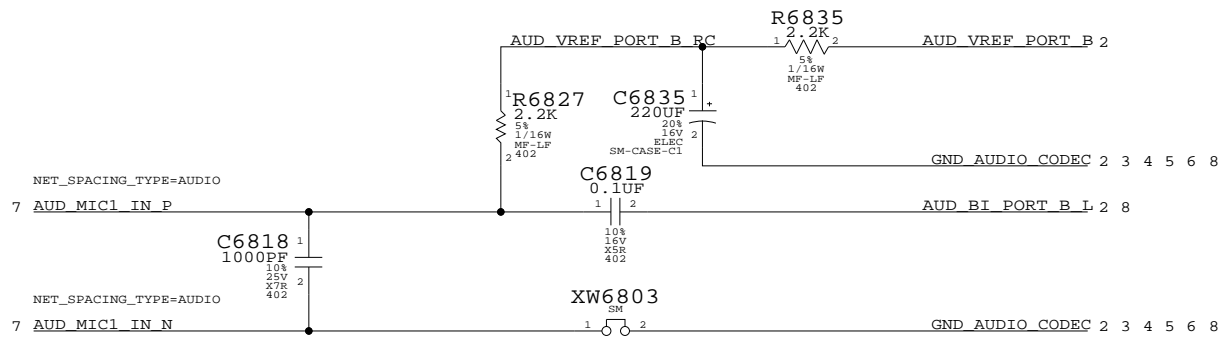
CODEC INPUT SIGNAL PATHS

FUNCTION	ADC	MIXER	PORT	VREF
MIC INPUT	0X07	0X24	0X18 (B)	80%
LINE INPUT	0X08	0X23	0X1A (C)	50%
SPDIFIN	CONVERTER=0X0A		PIN=0X1F	

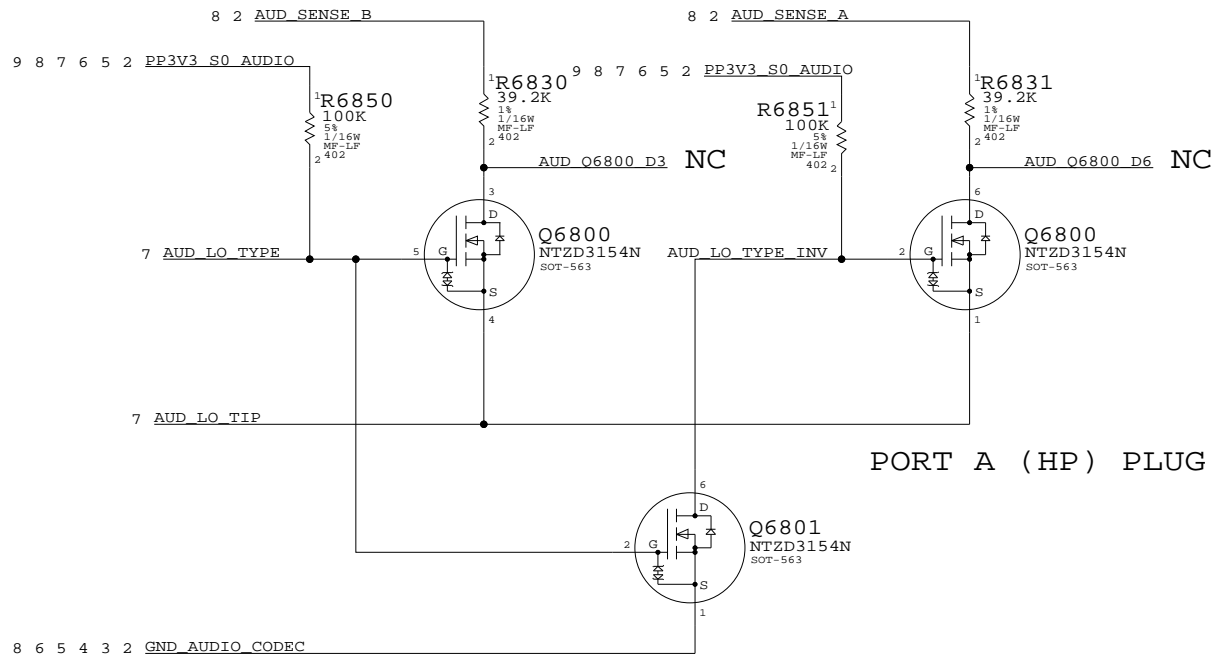
OPTIONAL RESISTOR TO COMBINE L/R SIGNALS FOR MONO MIC



MICROPHONE IMPEDANCE MATCHING CIRCUIT

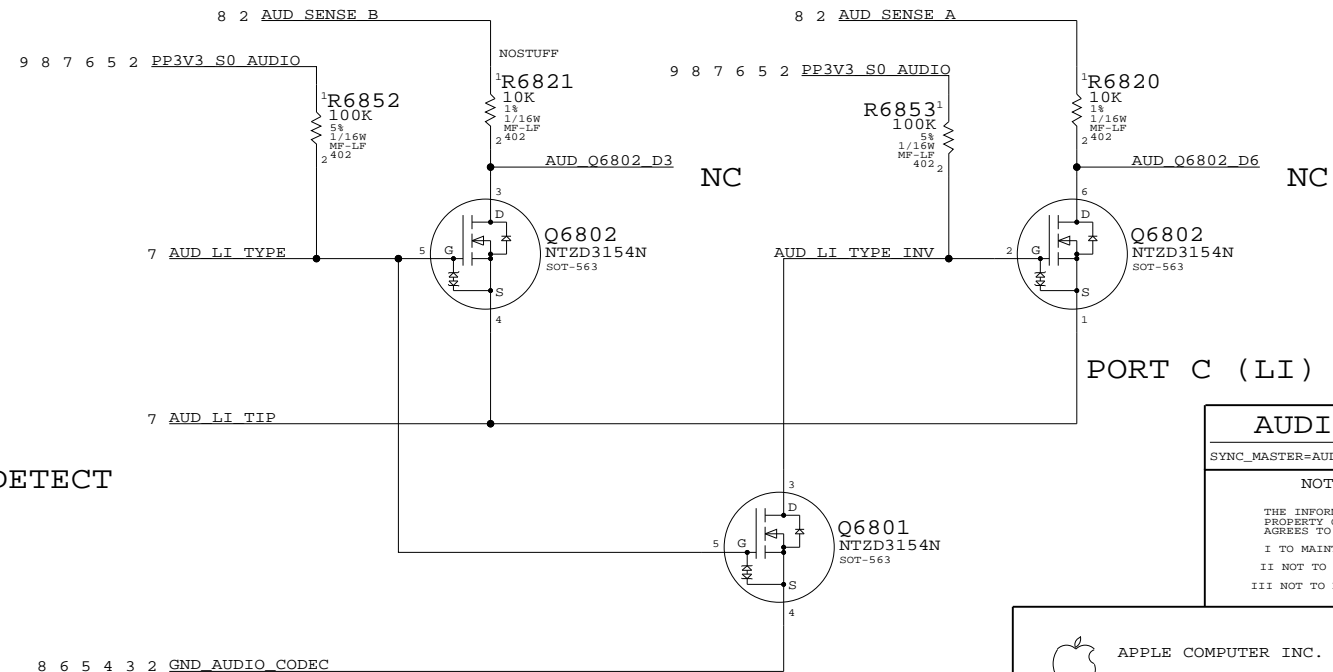


PORT A DIGITAL OUT DETECT DELEGATE

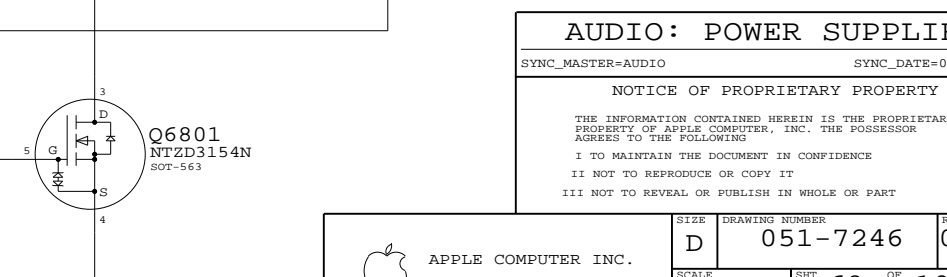


PORT A (HP) PLUG DETECT

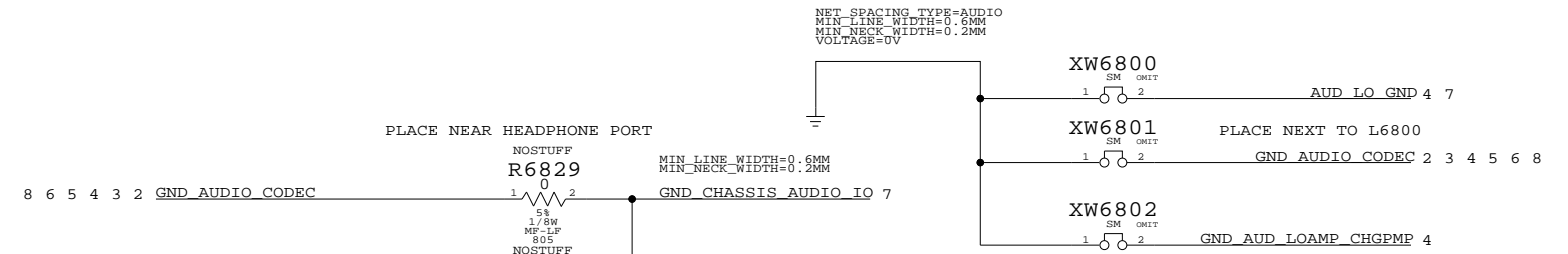
PORT C (LI) DIG DET DELEGATE



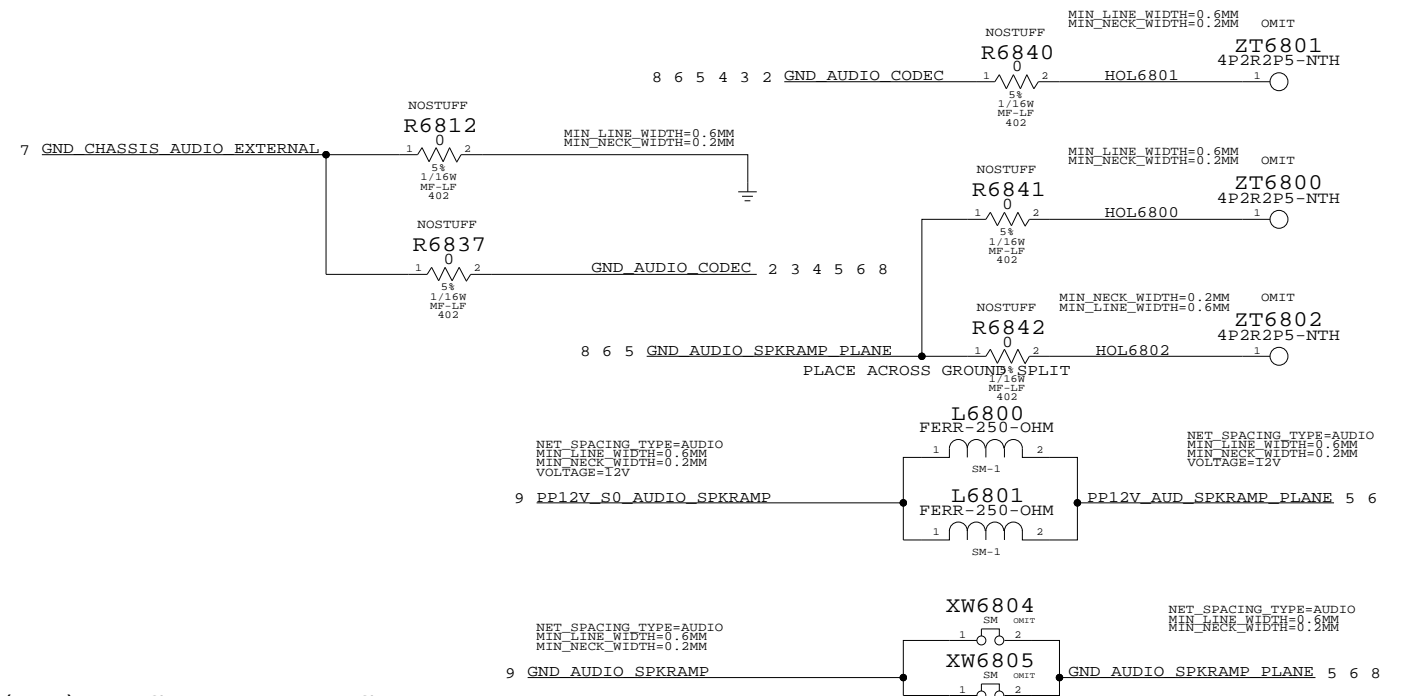
PORT C (LI) INSERT DETECT



AUDIO GROUND RETURNS



AUDIO MTG HOLES



AUDIO: POWER SUPPLIES

SYNC_MASTER=AUDIO SYNC_DATE=08/04/2006

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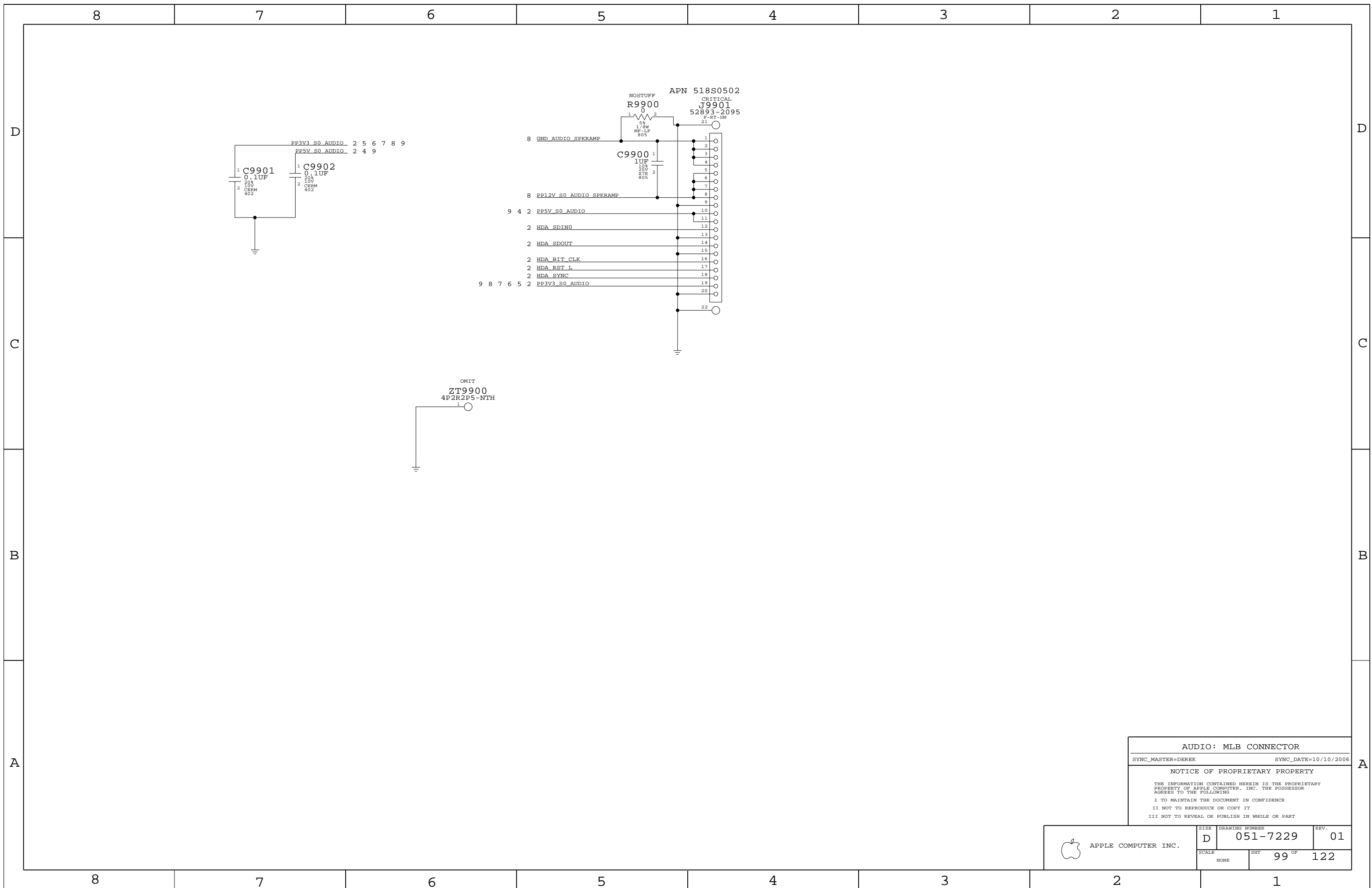
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SCALE	SHT	OF	
NONE	68	109	



AUDIO: MLB CONNECTOR

SYNC_MASTER=DEREK SYNC_DATE=10/10/2006

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	SCALE NONE	SHEET 99 OF	TOTAL SHEETS 122

	8	7	6	5	4	3	2	1
D	Title: Basenet Report Design: hop Date: Oct 23 12:30:13 2006 Base nets and synonyms for hop_lib.HOP(@hop_lib.hop(sch_1)) Base Signal Synonyms Location((Zone) dir))		AUD_PSEUDO_VREF_F AUD_PSEUDO_VREF_F - @hop_lib.HOP 2B2 3A7 3C7 AUD_SAMP1_FS1 AUD_SAMP1_FS1 - @hop_lib.HOP 6A6 6C5 AUD_SAMP1_FS2 AUD_SAMP1_FS2 - @hop_lib.HOP 6A6 6C5 AUD_SAMP1_G1 AUD_SAMP1_G1 - @hop_lib.HOP 6A6 6C5 AUD_SAMP1_G2 AUD_SAMP1_G2 - @hop_lib.HOP 6A6 6C5 AUD_SAMP1_INL_N AUD_SAMP1_INL_N - @hop_lib.HOP 6C5 AUD_SAMP1_INL_P AUD_SAMP1_INL_P - @hop_lib.HOP 6C5 AUD_SAMP1_INR_N AUD_SAMP1_INR_N - @hop_lib.HOP 6C5 AUD_SAMP1_INR_P AUD_SAMP1_INR_P - @hop_lib.HOP 6C5 AUD_SAMP1_SHDN_L AUD_SAMP1_SHDN_L - @hop_lib.HOP 6C5 AUD_SAMP_FS1 AUD_SAMP_FS1 - @hop_lib.HOP 5A6 5C5 AUD_SAMP_FS2 AUD_SAMP_FS2 - @hop_lib.HOP 5A6 5C5 AUD_SAMP_G1 AUD_SAMP_G1 - @hop_lib.HOP 5A6 5C5 AUD_SAMP_G2 AUD_SAMP_G2 - @hop_lib.HOP 5A6 5C5 AUD_SAMP_INL_N AUD_SAMP_INL_N - @hop_lib.HOP 5C5 AUD_SAMP_INL_P AUD_SAMP_INL_P - @hop_lib.HOP 5C5 AUD_SAMP_INR_N AUD_SAMP_INR_N - @hop_lib.HOP 5C5 AUD_SAMP_INR_P AUD_SAMP_INR_P - @hop_lib.HOP 5C5 AUD_SAMP_SHDN_L AUD_SAMP_SHDN_L - @hop_lib.HOP 5C6 AUD_SENSE_A AUD_SENSE_A - @hop_lib.HOP 2C2 8B4 8B7 AUD_SENSE_B AUD_SENSE_B - @hop_lib.HOP 2C2 8B4 AUD_SPDIFIN_JACK AUD_SPDIFIN_JACK - @hop_lib.HOP 7D7 AUD_SPDIF_GND AUD_SPDIF_GND - @hop_lib.HOP 7A3 7D8 AUD_SPDIF_IN AUD_SPDIF_IN - @hop_lib.HOP 2C2 7D4 AUD_SPDIF_OUT AUD_SPDIF_OUT - @hop_lib.HOP 2D2 7B8 AUD_SPDIF_OUT_CHIP AUD_SPDIF_OUT_CHIP - @hop_lib.HOP 2D4 AUD_SPDIF_OUT_JACK AUD_SPDIF_OUT_JACK - @hop_lib.HOP 7B5 AUD_SPKRAMP_MUTE AUD_SPKRAMP_MUTE - @hop_lib.HOP 5C8 6B8 AUD_SPKRAMP_MUTE_L AUD_SPKRAMP_MUTE_L - @hop_lib.HOP 5B7 AUD_SPKR_OUTBL_N AUD_SPKR_OUTBL_N - @hop_lib.HOP 6C1 7D2 AUD_SPKR_OUTBL_P AUD_SPKR_OUTBL_P - @hop_lib.HOP 6C1 7D2 AUD_SPKR_OUTBR_N AUD_SPKR_OUTBR_N - @hop_lib.HOP 5C1 7C2 AUD_SPKR_OUTBR_P AUD_SPKR_OUTBR_P - @hop_lib.HOP 5C1 7C2 AUD_SPKR_OUTCL_N AUD_SPKR_OUTCL_N - @hop_lib.HOP 6C1 7D2 AUD_SPKR_OUTCL_P AUD_SPKR_OUTCL_P - @hop_lib.HOP 6C1 7D2 AUD_SPKR_OUTCR_N AUD_SPKR_OUTCR_N - @hop_lib.HOP 5C1 7C2 AUD_SPKR_OUTCR_P AUD_SPKR_OUTCR_P - @hop_lib.HOP 5C1 7C2 AUD_TYPR_DET_EM AUD_TYPR_DET_EM - @hop_lib.HOP 8A3 AUD_VREF_PORT_F AUD_VREF_PORT_F - @hop_lib.HOP 2C2 8C5 AUD_VREF_PORT_F_RC AUD_VREF_PORT_F_RC - @hop_lib.HOP 8C6 BAL_IN_COM BAL_IN_COM - @hop_lib.HOP 2C7 3B3 BAL_IN_L BAL_IN_L - @hop_lib.HOP 2C7 3D3 BAL_IN_R BAL_IN_R - @hop_lib.HOP 2C7 3C3 BEEP BEEP - @hop_lib.HOP 2C6 GND_AUDIO GND_AUDIO - @hop_lib.HOP 8D4 GND_AUDIO_CODECC GND_AUDIO_CODECC - @hop_lib.HOP 2B7 2D2 3A7 3C7 4A7 4C7 5B8 5C8 6B8 6C8 8A4 8A7 8C4 8C5 8D2 8D4		D			
	12V_S5_SPKRAMP 12V_S5_SPKRAMP - @hop_lib.HOP 5D7 12V_S5_SPKRAMP1 12V_S5_SPKRAMP1 - @hop_lib.HOP 6D7 +PP3V3_S0_AUDIO +PP3V3_S0_AUDIO - @hop_lib.HOP 2D7 5A6 5C8 5C8 6A6 6C8 7B8 8A4 8B4 8B7 9C4 +PP5V_S0_AUDIO +PP5V_S0_AUDIO - @hop_lib.HOP 2B7 4B7 9C4 +PP12V_S5_AUDIO_SPKRAMP +PP12V_S5_AUDIO_SPKRAMP - 5D8 6D8 9D4 AMP @hop_lib.HOP ACZ_BITCLK ACZ_BITCLK - @hop_lib.HOP 2D7 9D6 ACZ_RST_L ACZ_RST_L - @hop_lib.HOP 2C7 9C6 ACZ_SDATIN<0> ACZ_SDATIN<0> - @hop_lib.HOP 2C7 9C6 ACZ_SDATIN_CHIP ACZ_SDATIN_CHIP - @hop_lib.HOP 2C6 ACZ_SDATOUT ACZ_SDATOUT - @hop_lib.HOP 2D7 9C6 ACZ_SYNC ACZ_SYNC - @hop_lib.HOP 2D7 9C6 AUDCODECOUTL AUDCODECOUTL - @hop_lib.HOP 4D6 AUDCODECOUTL1 AUDCODECOUTL1 - @hop_lib.HOP 4D5 AUDCODECOUTR AUDCODECOUTR - @hop_lib.HOP 4C6 AUDCODECOUTR1 AUDCODECOUTR1 - @hop_lib.HOP 4C5 AUDIO_LO_MUTE_L_F AUDIO_LO_MUTE_L_F - @hop_lib.HOP 4A6 AUDLINDETH AUDLINDETH - @hop_lib.HOP 8B7 AUDSAMP_CFN AUDSAMP_CFN - @hop_lib.HOP 5C4 AUDSAMP_CFN1 AUDSAMP_CFN1 - @hop_lib.HOP 6C4 AUDSAMP_CFP AUDSAMP_CFP - @hop_lib.HOP 5C4 AUDSAMP_CFP1 AUDSAMP_CFP1 - @hop_lib.HOP 6C4 AUDSAMP_INLN AUDSAMP_INLN - @hop_lib.HOP 5D6 AUDSAMP_INLN1 AUDSAMP_INLN1 - @hop_lib.HOP 6D6 AUDSAMP_INLP AUDSAMP_INLP - @hop_lib.HOP 5C6 AUDSAMP_INLP1 AUDSAMP_INLP1 - @hop_lib.HOP 6C6 AUDSAMP_INRN AUDSAMP_INRN - @hop_lib.HOP 5C6 AUDSAMP_INRN1 AUDSAMP_INRN1 - @hop_lib.HOP 6C6 AUDSAMP_INRP AUDSAMP_INRP - @hop_lib.HOP 5C6 AUDSAMP_INRP1 AUDSAMP_INRP1 - @hop_lib.HOP 6C6 AUDSAMP_OURTP AUDSAMP_OURTP - @hop_lib.HOP 5C4 AUDSAMP_OURTP1 AUDSAMP_OURTP1 - @hop_lib.HOP 6C4 AUDSAMP_OUTLN AUDSAMP_OUTLN - @hop_lib.HOP 5C4 AUDSAMP_OUTLN1 AUDSAMP_OUTLN1 - @hop_lib.HOP 6C4 AUDSAMP_OUTLP AUDSAMP_OUTLP - @hop_lib.HOP 5C4 AUDSAMP_OUTLP1 AUDSAMP_OUTLP1 - @hop_lib.HOP 6C4 AUDSAMP_OUTRN AUDSAMP_OUTRN - @hop_lib.HOP 5C4 AUDSAMP_OUTRN1 AUDSAMP_OUTRN1 - @hop_lib.HOP 6C4 AUD_BI_PORTA_L AUD_BI_PORTA_L - @hop_lib.HOP 3C3 AUD_BI_PORTA_R AUD_BI_PORTA_R - @hop_lib.HOP 3B3 AUD_BI_PORT_A_L AUD_BI_PORT_A_L - @hop_lib.HOP 2C2 3C2 AUD_BI_PORT_A_R AUD_BI_PORT_A_R - @hop_lib.HOP 2C2 3B2 AUD_BI_PORT_B_L AUD_BI_PORT_B_L - @hop_lib.HOP 2C2 6C8 AUD_BI_PORT_B_R AUD_BI_PORT_B_R - @hop_lib.HOP 2C2 5C8 AUD_BI_PORT_C_L AUD_BI_PORT_C_L - @hop_lib.HOP 2C7 6D8 AUD_BI_PORT_C_R AUD_BI_PORT_C_R - @hop_lib.HOP 2C7 5D8 AUD_BI_PORT_D_L AUD_BI_PORT_D_L - @hop_lib.HOP 2C7 4D7 AUD_BI_PORT_D_R AUD_BI_PORT_D_R - @hop_lib.HOP 2C7 4C7 AUD_BI_PORT_F_L AUD_BI_PORT_F_L - @hop_lib.HOP 2B3 2C2 8C5 AUD_BI_PORT_F_R AUD_BI_PORT_F_R - @hop_lib.HOP 2B2 2C2 AUD_GPIO_0 AUD_GPIO_0 - @hop_lib.HOP 2C7 5B8 AUD_GPIO_1 AUD_GPIO_1 - @hop_lib.HOP 2C7 4A7 AUD_LI_DET_EMI AUD_LI_DET_EMI - @hop_lib.HOP 7D6 AUD_LI_DET_H AUD_LI_DET_H - @hop_lib.HOP 7D4 8B7 AUD_LI_DET_JACK AUD_LI_DET_JACK - @hop_lib.HOP 7D7 AUD_LI_GND AUD_LI_GND - @hop_lib.HOP 3B7 3C7 7D4 AUD_LI_GNDL1 AUD_LI_GNDL1 - @hop_lib.HOP 3C5 AUD_LI_GNDR1 AUD_LI_GNDR1 - @hop_lib.HOP 3B5 AUD_LI_GND_EMI AUD_LI_GND_EMI - @hop_lib.HOP 7D6 AUD_LI_GND_JACK AUD_LI_GND_JACK - @hop_lib.HOP 7D8 AUD_LI_L AUD_LI_L - @hop_lib.HOP 3D7 7D4 AUD_LI_L1 AUD_LI_L1 - @hop_lib.HOP 3D5 AUD_LI_L2 AUD_LI_L2 - @hop_lib.HOP 3D4 AUD_LI_L_EMI AUD_LI_L_EMI - @hop_lib.HOP 7D5 AUD_LI_L_JACK AUD_LI_L_JACK - @hop_lib.HOP 7D7 AUD_LI_R AUD_LI_R - @hop_lib.HOP 3B7 7D4 AUD_LI_R1 AUD_LI_R1 - @hop_lib.HOP 3B5 AUD_LI_R2 AUD_LI_R2 - @hop_lib.HOP 3B4 AUD_LI_R_EMI AUD_LI_R_EMI - @hop_lib.HOP 7D6 AUD_LI_R_JACK AUD_LI_R_JACK - @hop_lib.HOP 7D7 AUD_LI_VREFL AUD_LI_VREFL - @hop_lib.HOP 3C4 AUD_LI_VREFR AUD_LI_VREFR - @hop_lib.HOP 3B4 AUD_LOAMP_IN_L_M AUD_LOAMP_IN_L_M - @hop_lib.HOP 4B7 4D4 AUD_LOAMP_IN_L_P AUD_LOAMP_IN_L_P - @hop_lib.HOP 4A7 4C4 AUD_LOAMP_IN_R_M AUD_LOAMP_IN_R_M - @hop_lib.HOP 4A7 4C4 AUD_LOAMP_IN_R_P AUD_LOAMP_IN_R_P - @hop_lib.HOP 4A7 4C4 AUD_LOAMP_OUT_L AUD_LOAMP_OUT_L - @hop_lib.HOP 4B3 4D4 AUD_LOAMP_OUT_R AUD_LOAMP_OUT_R - @hop_lib.HOP 4A3 4C4 AUD_LO_DET1 AUD_LO_DET1 - @hop_lib.HOP 7B8 8A4 AUD_LO_DET1_1 AUD_LO_DET1_1 - @hop_lib.HOP 8A2 8A4 AUD_LO_DET1_EMI AUD_LO_DET1_EMI - @hop_lib.HOP 7B7 AUD_LO_DET1_INV AUD_LO_DET1_INV - @hop_lib.HOP 8A3 AUD_LO_DET1_JACK AUD_LO_DET1_JACK - @hop_lib.HOP 7B4 AUD_LO_DET2 AUD_LO_DET2 - @hop_lib.HOP 7B8 8B4 AUD_LO_DET2_1 AUD_LO_DET2_1 - @hop_lib.HOP 8B4 AUD_LO_DET2_EMI AUD_LO_DET2_EMI - @hop_lib.HOP 7B7 AUD_LO_DET2_JACK AUD_LO_DET2_JACK - @hop_lib.HOP 7B4 AUD_LO_GND AUD_LO_GND - @hop_lib.HOP 4A3 4A7 7A8 8D2 AUD_LO_GND_EMI AUD_LO_GND_EMI - @hop_lib.HOP 7A7 AUD_LO_GND_JACK AUD_LO_GND_JACK - @hop_lib.HOP 7B4 AUD_LO_GND_FRB AUD_LO_GND_FRB - @hop_lib.HOP 4C7 7A8 AUD_LO_GND_FRB_IN AUD_LO_GND_FRB_IN - @hop_lib.HOP 4C6 AUD_LO_L AUD_LO_L - @hop_lib.HOP 4B3 7B8 AUD_LO_L_EMI AUD_LO_L_EMI - @hop_lib.HOP 7B7 AUD_LO_L_JACK AUD_LO_L_JACK - @hop_lib.HOP 7B4 AUD_LO_R AUD_LO_R - @hop_lib.HOP 4A3 7A8 AUD_LO_R_EMI AUD_LO_R_EMI - @hop_lib.HOP 7A7 AUD_LO_R_JACK AUD_LO_R_JACK - @hop_lib.HOP 7B4 AUD_MAX9714_CHOLD AUD_MAX9714_CHOLD - @hop_lib.HOP 5C4 AUD_MAX9714_CHOLD1 AUD_MAX9714_CHOLD1 - @hop_lib.HOP 6C4 AUD_MAX9714_VREG AUD_MAX9714_VREG - @hop_lib.HOP 5C5 AUD_MAX9714_VREG1 AUD_MAX9714_VREG1 - @hop_lib.HOP 6C5 AUD_MAX9722_C1N AUD_MAX9722_C1N - @hop_lib.HOP 4A5 AUD_MAX9722_C1P AUD_MAX9722_C1P - @hop_lib.HOP 4A5 AUD_MAX9722_FVSS AUD_MAX9722_FVSS - @hop_lib.HOP 4A5 AUD_MIC_IN_N AUD_MIC_IN_N - @hop_lib.HOP 7C6 8C8 AUD_MIC_IN_N_CONN AUD_MIC_IN_N_CONN - @hop_lib.HOP 7C4 AUD_MIC_IN_N_EMI AUD_MIC_IN_N_EMI - @hop_lib.HOP 7C5 AUD_MIC_IN_P AUD_MIC_IN_P - @hop_lib.HOP 7C6 8C8 AUD_MIC_IN_P_CONN AUD_MIC_IN_P_CONN - @hop_lib.HOP 7C4 AUD_MIC_IN_P_EMI AUD_MIC_IN_P_EMI - @hop_lib.HOP 7C5 AUD_MIC_P1 AUD_MIC_P1 - @hop_lib.HOP 8C7 AUD_PORT_A_DET_L AUD_PORT_A_DET_L - @hop_lib.HOP 8B6 AUD_PORT_D_DET_L AUD_PORT_D_DET_L - @hop_lib.HOP 8B2 AUD_PORT_G_DET_L AUD_PORT_G_DET_L - @hop_lib.HOP 8B1 AUD_PSEUDO_VREF AUD_PSEUDO_VREF - @hop_lib.HOP 2B3 2C2							
C								
B								
A								

Title: Cref Part Report
Design: hop
Date: Oct 23 12:30:13 2006

D

C

B

A

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A

Table with 4 columns: Part ID, Name, SM, Location. Rows include CAP_805, CAP_402, CAP_P_SM-CASE-C1, etc.

Table with 4 columns: Part ID, Name, SM, Location. Rows include L6200, L6201, L6202, etc.

Table with 4 columns: Part ID, Name, SM, Location. Rows include R6827, R6829, R6830, etc.