

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD	ENG APPD
01		279015	ENGINEERING RELEASED		
				DATE	DATE
				06/06/03	?

Q59 MLB DVT

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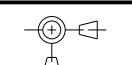

POWER RAIL DEFINITIONS

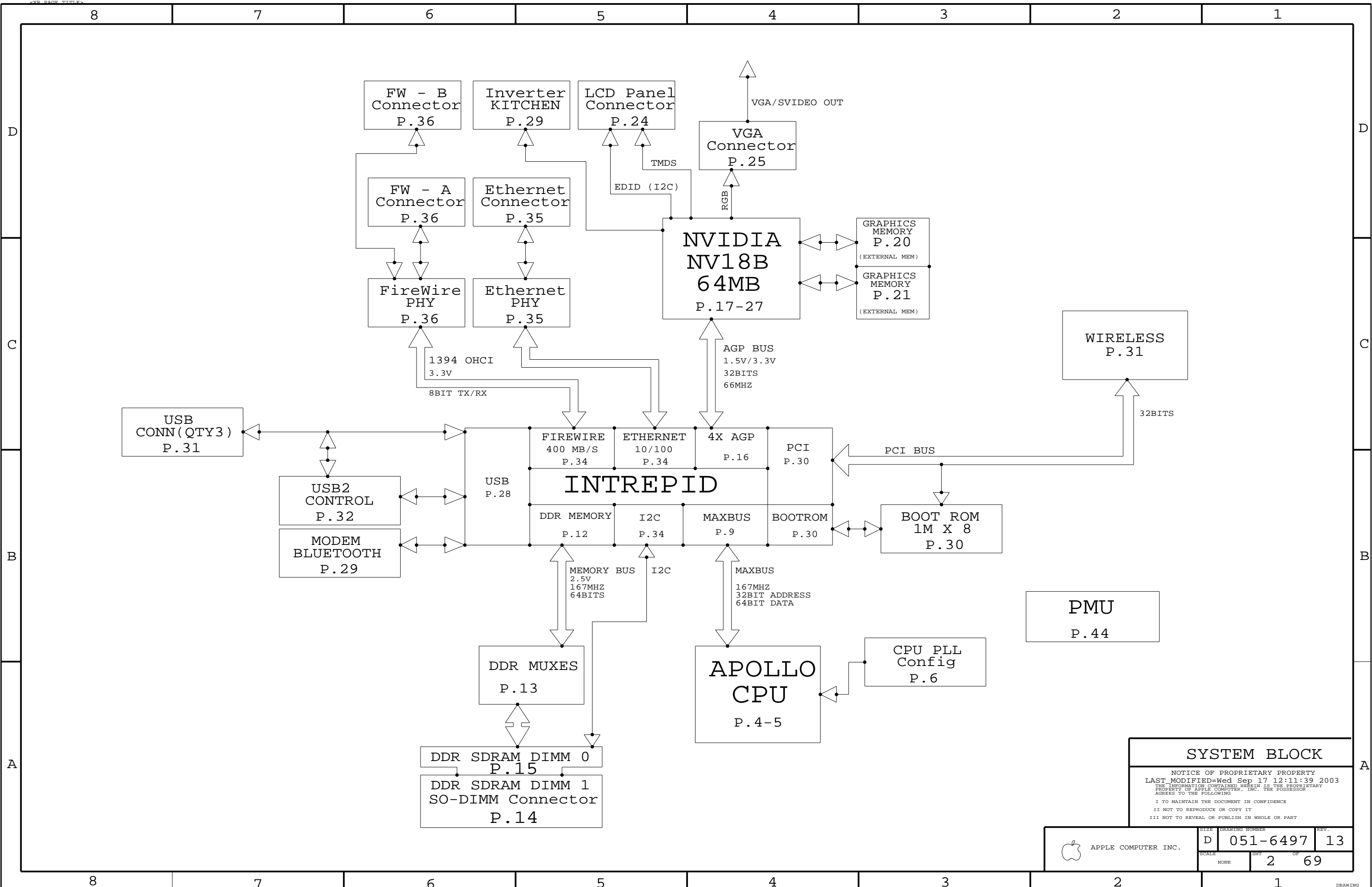
	RUN	SLEEP	SHUTDOWN
+2_5V_MAIN	ON	ON	OFF
+3V_MAIN	ON	ON	OFF
+5V_MAIN	ON	ON	OFF
+5V_SLEEP	ON	OFF	OFF
+12V_MAIN	ON	ON	ON
+12V_SLEEP	ON	OFF	OFF
FW_PWR	ON	ON	OFF
+1.8V_SLEEP	ON	OFF	OFF
+MAXBUS_SLEEP	ON	OFF	OFF

SCHEMATIC AND PCB SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
051-6497	1	SCHEM,MLB,Q59	SCH1	CRITICAL	
820-1550	1	PCB,MLB,IMACG4	PCB1	CRITICAL	
825-2029	1	LBL,SER #,BARCODE	PCB1		
056-1158	1	DESIGN GUIDE,MCO,IMACG4	PCB1	CRITICAL	
057-0085	1	DFM,PNLZN DWG,MLB,Q59	PCB1	CRITICAL	
630-XXXX	1	630-XXXX,PCBA,H,Q59,EEE XXX	HYNIX		OMIT
630-XXXX	1	630-XXXX,PCBA,S,Q59,EEE XXX	SAMSUNG		OMIT

PCB,UL RECOGNIZED, MIN.130 DEG. C TEMP. RATING AND V-0 FLAME RATING PER UL 796 & UL 94. PCB TO BE SILK-SCREENED WITH UL/CUL RECOGNITION MARK, MANUFACTURER'S UL FILE NUMBER, UL PCB MATERIAL DESIGNATION, TEMPERATURE RATING AND FLAME RATING.

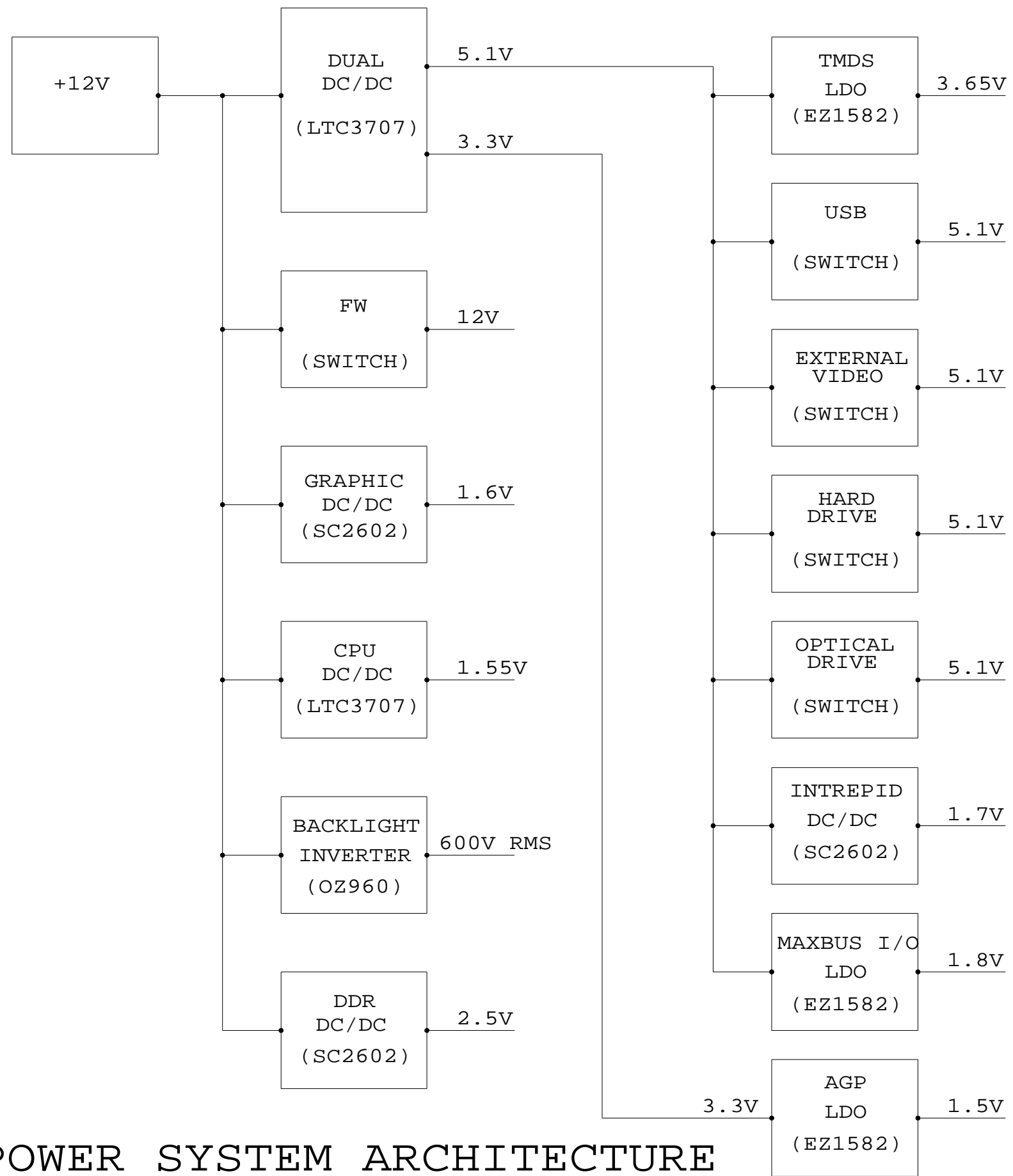
DIMENSIONS ARE IN MILLIMETERS XX : _____ X.XX : _____ X.XXX : _____ ANGLES : _____ DO NOT SCALE DRAWING  THIRD ANGLE PROJECTION	METRIC		 Apple Computer Inc.		
	DRAFTER <input type="checkbox"/>	DESIGN CR <input type="checkbox"/>	NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART		
	ENG APPD <input type="checkbox"/>	MFG APPD <input type="checkbox"/>	TITLE SCHEM, MLB, Q59		
	QA APPD <input type="checkbox"/>	DESIGNER <input type="checkbox"/>	DRAWING NUMBER 051-6497 REV. 13		
RELEASE <input type="checkbox"/>	SCALE <input type="checkbox"/> NONE	MATERIAL/FINISH NOTED AS APPLICABLE D			
		SIZE D		SHEET 1 OF 69	



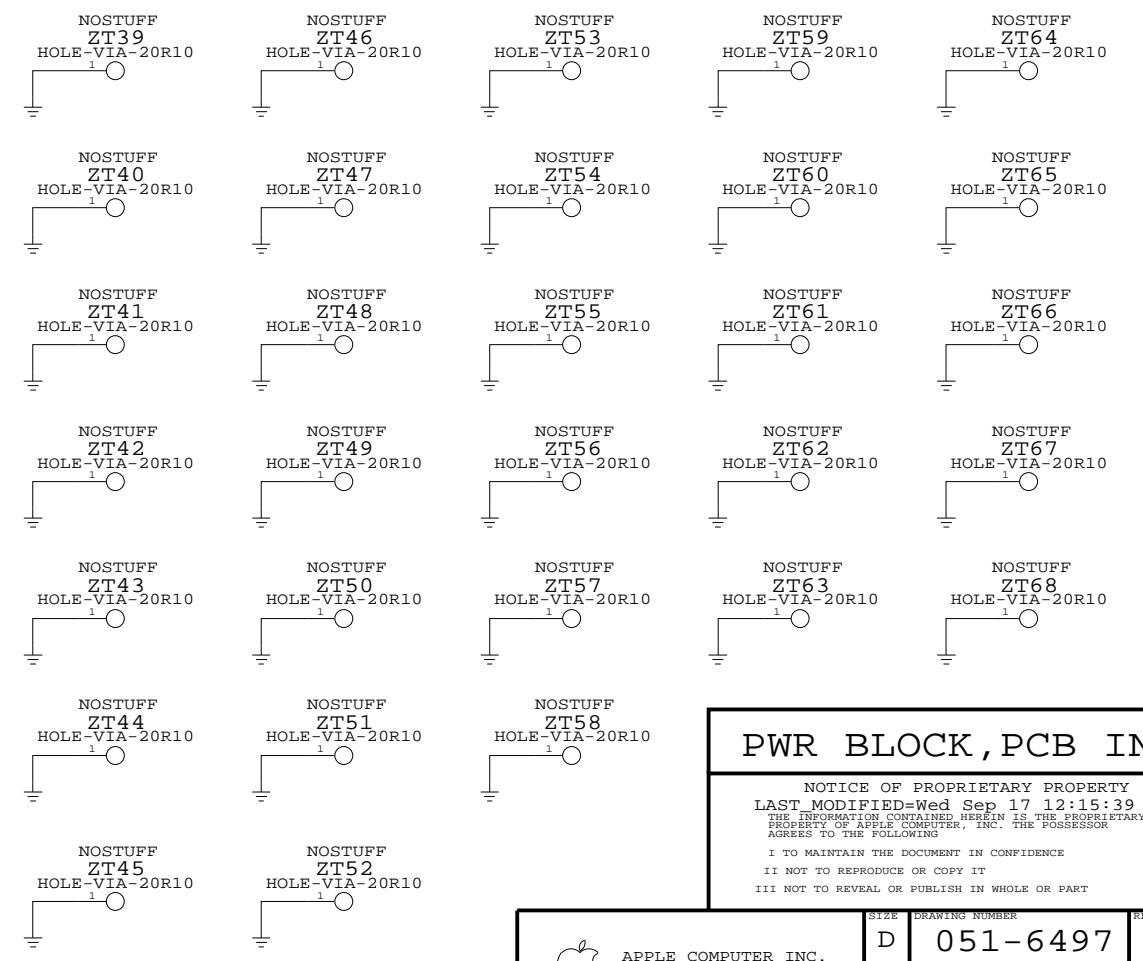
SYSTEM BLOCK

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	NONE	051-6497	13
		SHT	OF
		2	69



LAYER	THICKNESS (MILS)	COPPER (OZ)	TRACE WIDTH (MILS)
1 - SIGNAL-TOP PREPREG	0.7	0.5	4
2 - GROUND1 PREPREG	3	---	---
3 - SIGNAL FILLER	1.4	1	---
4 - POWER PREPREG	3	---	---
5 - POWER FILLER	0.7	0.5	4
6 - SIGNAL PREPREG	17.4	---	---
7 - GROUND2 PREPREG	2.8	2	---
8 - SIGNAL-BOTTOM	4	---	---
	2.8	2	---
	17.4	---	---
	0.7	0.5	4
	3	---	---
	1.4	1	---
	3	---	---
	0.7	0.5	4
=====	=====	=====	=====
TOTAL	62.0	---	---



PWR BLOCK, PCB INFO

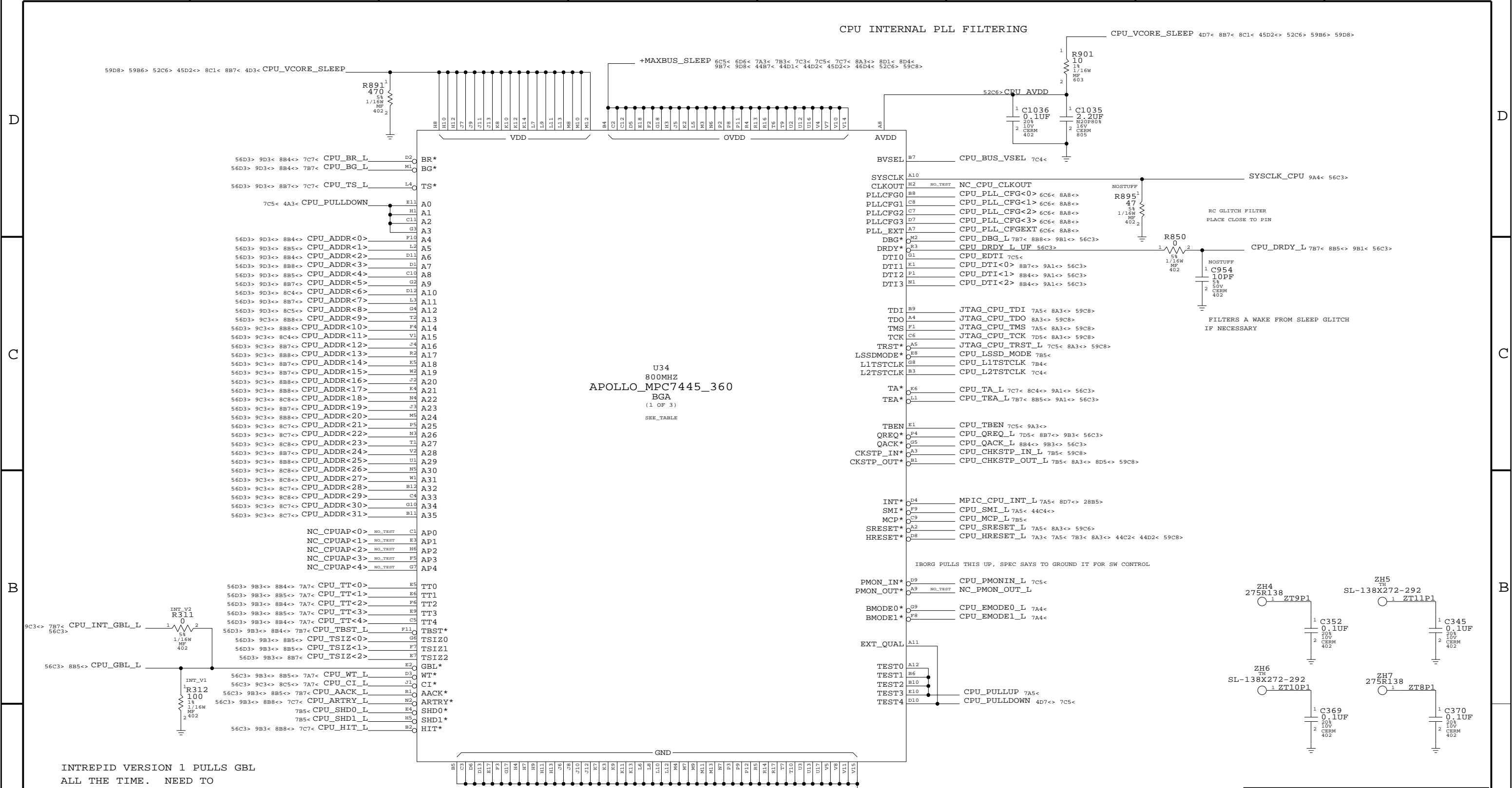
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POWER SYSTEM ARCHITECTURE

APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6497	13
SCALE	SHT	OF	
NONE	3	69	

CPU INTERNAL PLL FILTERING



U34
800MHZ
APOLLO MPC7445_360
BGA
(1 OF 3)
SEE_TABLE

INTREPID VERSION 1 PULLS GBL
ALL THE TIME. NEED TO
CUT THE TRACE AND YANK
DOWN HARD FOR SNOOPING.
FIXED IN INTREPID VERSION 2.

CPU MECHANICAL PARTS SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
875-1475	1	PAD, THERMAL, CPU, U34	U341	?	
870-1113	1	HEAT SINK, CPU, Q26, U34	U342	?	DEV
870-1114	1	CLIP, HEAT SINK, CPU, Q26, U34	U343	?	DEV
412-0042	1	SCREW, MACH, 3MM W, 8MM L, U34	U344	?	DEV
835-0251	1	NUT, 3MM, U34	U345	?	DEV

MPC7450 MAXBUS

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APPLE COMPUTER INC.

SIZE: D DRAWING NUMBER: 051-6497 REV: 13

SCALE: NONE SHEET: 4 OF 69

APOLLO_MPC7445_360

NC_CPUCRUD<0>	NO_TEST	F18	NC_F18
NC_CPUCRUD<1>	NO_TEST	F17	NC_F17
NC_CPUCRUD<2>	NO_TEST	F19	NC_F19
NC_CPUCRUD<3>	NO_TEST	H19	NC_H19
NC_CPUCRUD<4>	NO_TEST	H18	NC_H18
NC_CPUCRUD<5>	NO_TEST	H17	NC_H17
NC_CPUCRUD<6>	NO_TEST	H16	NC_H16
NC_CPUCRUD<7>	NO_TEST	E19	NC_E19
NC_CPUCRUD<8>	NO_TEST	D18	NC_D18
NC_CPUCRUD<9>	NO_TEST	F16	NC_F16
NC_CPUCRUD<10>	NO_TEST	G16	NC_G16
NC_CPUCRUD<11>	NO_TEST	D19	NC_D19
NC_CPUCRUD<12>	NO_TEST	F15	NC_F15
NC_CPUCRUD<13>	NO_TEST	G19	NC_G19
NC_CPUCRUD<14>	NO_TEST	E16	NC_E16
NC_CPUCRUD<15>	NO_TEST	D17	NC_D17
NC_CPUCRUD<16>	NO_TEST	D16	NC_D16

U34
800MHZ
BGA
(3 OF 3)

NC_CPUCRUD<17>	NO_TEST	P15	NC_P15
NC_CPUCRUD<18>	NO_TEST	L15	NC_L15
NC_CPUCRUD<19>	NO_TEST	N15	NC_N15
NC_CPUCRUD<20>	NO_TEST	P18	NC_P18
NC_CPUCRUD<21>	NO_TEST	N14	NC_N14
NC_CPUCRUD<22>	NO_TEST	M14	NC_M14
NC_CPUCRUD<23>	NO_TEST	M17	NC_M17
NC_CPUCRUD<24>	NO_TEST	N13	NC_N13
NC_CPUCRUD<25>	NO_TEST	N16	NC_N16
NC_CPUCRUD<26>	NO_TEST	M19	NC_M19
NC_CPUCRUD<27>	NO_TEST	M16	NC_M16
NC_CPUCRUD<28>	NO_TEST	P19	NC_P19
NC_CPUCRUD<29>	NO_TEST	N17	NC_N17
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NC_CPUCRUD<31>	NO_TEST	L17	NC_L17
NC_CPUCRUD<32>	NO_TEST	L14	NC_L14
NC_CPUCRUD<33>	NO_TEST	K15	NC_K15
NC_CPUCRUD<34>	NO_TEST	J14	NC_J14
NC_CPUCRUD<35>	NO_TEST	J18	NC_J18
NC_CPUCRUD<36>	NO_TEST	J19	NC_J19
NC_CPUCRUD<37>	NO_TEST	J15	NC_J15
NC_CPUCRUD<38>	NO_TEST	K19	NC_K19
NC_CPUCRUD<39>	NO_TEST	J16	NC_J16
NC_CPUCRUD<40>	NO_TEST	H15	NC_H15
NC_CPUCRUD<41>	NO_TEST	L16	NC_L16
NC_CPUCRUD<42>	NO_TEST	P16	NC_P16
NC_CPUCRUD<43>	NO_TEST	M18	NC_M18
NC_CPUCRUD<44>	NO_TEST	L19	NC_L19
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NC_CPUCRUD<46>	NO_TEST	K18	NC_K18
NC_CPUCRUD<47>	NO_TEST	J17	NC_J17
NC_CPUCRUD<48>	NO_TEST	K16	NC_K16
NC_CPUCRUD<49>	NO_TEST	C19	NC_C19
NC_CPUCRUD<50>	NO_TEST	D15	NC_D15
NC_CPUCRUD<51>	NO_TEST	G15	NC_G15
NC_CPUCRUD<52>	NO_TEST	C18	NC_C18
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NC_CPUCRUD<54>	NO_TEST	B19	NC_B19
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NC_CPUCRUD<56>	NO_TEST	D14	NC_D14
NC_CPUCRUD<57>	NO_TEST	E15	NC_E15
NC_CPUCRUD<58>	NO_TEST	B15	NC_B15
NC_CPUCRUD<59>	NO_TEST	B17	NC_B17
NC_CPUCRUD<60>	NO_TEST	C17	NC_C17
NC_CPUCRUD<61>	NO_TEST	C16	NC_C16
NC_CPUCRUD<62>	NO_TEST	G13	NC_G13
NC_CPUCRUD<63>	NO_TEST	E14	NC_E14
NC_CPUCRUD<64>	NO_TEST	H14	NC_H14
NC_CPUCRUD<65>	NO_TEST	G14	NC_G14
NC_CPUCRUD<66>	NO_TEST	C15	NC_C15
NC_CPUCRUD<67>	NO_TEST	A17	NC_A17
NC_CPUCRUD<68>	NO_TEST	G12	NC_G12
NC_CPUCRUD<69>	NO_TEST	F14	NC_F14
NC_CPUCRUD<70>	NO_TEST	F13	NC_F13
NC_CPUCRUD<71>	NO_TEST	E13	NC_E13
NC_CPUCRUD<72>	NO_TEST	B16	NC_B16
NC_CPUCRUD<73>	NO_TEST	A15	NC_A15
NC_CPUCRUD<74>	NO_TEST	C14	NC_C14
NC_CPUCRUD<75>	NO_TEST	A18	NC_A18
NC_CPUCRUD<76>	NO_TEST	A13	NC_A13
NC_CPUCRUD<77>	NO_TEST	F12	NC_F12
NC_CPUCRUD<78>	NO_TEST	A14	NC_A14
NC_CPUCRUD<79>	NO_TEST	G11	NC_G11
NC_CPUCRUD<80>	NO_TEST	C13	NC_C13

NC_CPUCRUD<81>	NO_TEST	N12	NC_N12
NC_CPUCRUD<82>	NO_TEST	N18	NC_N18
NC_CPUCRUD<83>	NO_TEST	K17	NC_K17
NC_CPUCRUD<84>	NO_TEST	N19	NC_N19
NC_CPUCRUD<85>	NO_TEST	B18	NC_B18
NC_CPUCRUD<86>	NO_TEST	E12	NC_E12
NC_CPUCRUD<87>	NO_TEST	B13	NC_B13
NC_CPUCRUD<88>	NO_TEST	B14	NC_B14
NC_CPUCRUD<89>	NO_TEST	A6	NC_A6

APOLLO_MPC7445_360

56D3> 9D1<> 8C4<>	CPU_DATA<0>	R15	D0
56D3> 9D1<> 8C7<>	CPU_DATA<1>	M15	D1
56D3> 9D1<> 8C8<>	CPU_DATA<2>	T14	D2
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56D3> 9D1<> 8C7<>	CPU_DATA<4>	M16	D4
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56D3> 9D1<> 8C8<>	CPU_DATA<7>	F14	D7
56D3> 9D1<> 8C5<>	CPU_DATA<8>	V13	D8
56D3> 9D1<> 8C4<>	CPU_DATA<9>	M13	D9
56D3> 9D1<> 8C7<>	CPU_DATA<10>	T13	D10
56D3> 9D1<> 8C5<>	CPU_DATA<11>	F13	D11
56D3> 9D1<> 8C5<>	CPU_DATA<12>	U14	D12
56D3> 9D1<> 8C7<>	CPU_DATA<13>	M14	D13
56D3> 9D1<> 8C8<>	CPU_DATA<14>	R12	D14
56D3> 9D1<> 8C5<>	CPU_DATA<15>	T12	D15
56D3> 9C1<> 8C4<>	CPU_DATA<16>	M12	D16
56D3> 9C1<> 8C7<>	CPU_DATA<17>	V12	D17
56D3> 9C1<> 8C4<>	CPU_DATA<18>	N11	D18
56D3> 9C1<> 8C4<>	CPU_DATA<19>	M10	D19
56D3> 9C1<> 8C4<>	CPU_DATA<20>	R11	D20
56D3> 9C1<> 8C8<>	CPU_DATA<21>	U11	D21
56D3> 9C1<> 8C7<>	CPU_DATA<22>	M11	D22
56D3> 9C1<> 8C8<>	CPU_DATA<23>	T11	D23
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56D3> 9C1<> 8C5<>	CPU_DATA<26>	F10	D26
56D3> 9C1<> 8C7<>	CPU_DATA<27>	U10	D27
56D3> 9C1<> 8D8<>	CPU_DATA<28>	R9	D28
56D3> 9C1<> 8C8<>	CPU_DATA<29>	M10	D29
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56D3> 9C1<> 8D5<>	CPU_DATA<38>	F7	D38
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56D3> 9B1<> 8C5<>	CPU_DATA<42>	V18	D42
56D3> 9B1<> 8C4<>	CPU_DATA<43>	R18	D43
56D3> 9B1<> 8C8<>	CPU_DATA<44>	V19	D44
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56D3> 9B1<> 8C4<>	CPU_DATA<46>	U19	D46
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56D3> 9D8< 9B1<>	8C5<>	U18	D48
56D3> 9D8< 9B1<>	8C4<>	M17	D49
56D3> 9D8< 9B1<>	8C8<>	M18	D50
56D3> 9D8< 9B1<>	8C8<>	T16	D51
56D3> 9C8< 9B1<>	8C5<>	T18	D52
56D3> 9C8< 9B1<>	8C7<>	T17	D53
56D3> 9C8< 9B1<>	8D7<>	M3	D54
56D3> 9C8< 9B1<>	8C5<>	V17	D55
56D3> 9B1<>	8D8<>	U4	D56
56D3> 9D5< 9B1<>	8D5<>	U8	D57
56D3> 9D5< 9B1<>	8D4<>	U7	D58
56D3> 9D5< 9B1<>	8D8<>	R7	D59
56D3> 9D5< 9B1<>	8D8<>	P6	D60
56D3> 9C5< 9B1<>	8D4<>	R8	D61
56D3> 9C5< 9B1<>	8D4<>	M8	D62
56D3> 9C5< 9B1<>	8D5<>	T8	D63

U34
800MHZ
BGA
(2 OF 3)

NC_CPUDP<0>	NO_TEST	T3	DP0
NC_CPUDP<1>	NO_TEST	M4	DP1
NC_CPUDP<2>	NO_TEST	T4	DP2
NC_CPUDP<3>	NO_TEST	M9	DP3
NC_CPUDP<4>	NO_TEST	M6	DP4
NC_CPUDP<5>	NO_TEST	V3	DP5
NC_CPUDP<6>	NO_TEST	N8	DP6
NC_CPUDP<7>	NO_TEST	M6	DP7

MPC7450 - 2

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	NONE	D 051-6497	13
		SHEET	OF
		5	69

BOMOPTIONS FOR UPPER-SET OF RESISTORS

1200@133&1500@167&1333@133&1667@167&1467@133&1833@167&1600@133&2000@167&1733@133&2167@167&1867@133&2333@167&2000@133&2500@167&2133@133&2667@167

667@133&833@167&733@133&917@167&800@133&1000@167&1067@133&1333@167&1333@133&1667@167&1467@133&1833@167&1600@133&2000@167&1867@133&2333@167&2133@133&2667@167

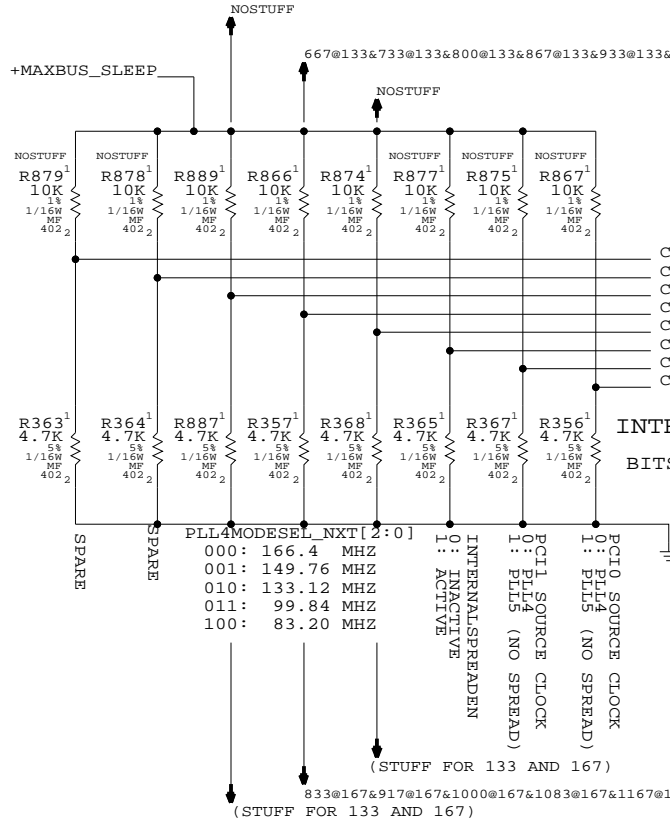
800@133&1000@167&867@133&1083@167&1067@133&1333@167&1200@133&1500@167&1733@133&2167@167&1867@133&2333@167&2133@133&2667@167

667@133&833@167&933@133&1167@167&1200@133&1500@167&1333@133&1667@167&1600@133&2000@167

667@133&833@167&733@133&917@167&800@133&1000@167&867@133&1083@167&1000@133&1250@167&1200@133&1500@167&1467@133&1833@167&1600@133&2000@167&1733@133&2167@167&2000@133&2500@167&2133@133&2667@167

+MAXBUS_SLEEP 4D5< 6C5< 7A3< 7B3< 7C3< 7C5< 7C7< 8A3<> 8D1< 8D4< 9B7< 9D8<
44B7< 44D1< 44D2< 45D2<> 46D4< 52C6> 59C8>

8D1< 8A3<> 7C7< 7C5< 7C3< 7B3< 7A3< 6D8< 4D5<
46D4< 45D2<> 44D2< 44D1< 44B7< 9D8< 9B7< 8D4<



CPU FREQUENCY CONFIGURATION
(SUPPORTED CPU & BUS SPEEDS)

MULTIPLIER (BUS-TO-CORE)	CORE FREQUENCY (AT BUS FREQUENCY)		CPU_PLL_CFG
	167MHZ	133MHZ	E 0123 HEX
5.0X	833	667	0 1011 0B
5.5X	917	733	0 1001 09
6.0X	1000	800	0 1101 0D
6.5X	1083	867	0 0101 05
7.0X	1167	933	0 0010 02
7.5X	1250	1000	0 0001 01
8.0X	1333	1067	0 1100 0C
9.0X	1500	1200	1 0111 17
10.0X	1667	1333	1 1010 1A
11.0X	1833	1467	1 1001 19
12.0X	2000	1600	1 1011 1B
13.0X	2167	1733	1 0101 15
14.0X	2333	1867	1 1100 1C
15.0X	2500	2000	1 0001 11
16.0X	2667	2133	1 1101 1D

CPU SPEED & BUS RATIO SUPPORT
THE CONFIGURATION RESISTORS BELOW ARE SELF CONFIGURING
WHEN THE ENGINEER SELECTS THE APPROPRIATE CPU AND
BUS SPEED BOM OPTION, THE APPROPRIATE RESISTORS ARE
ARE AUTOMATICALLY SELECTED

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
337S2799	1	IC, APOLLO6, S1COH, 1.0GHZ, 1.5V+30/-130MV, 28W, 85C	U34	CRITICAL	1000@167
337S2801	1	IC, APOLLO6, S1COH, 1.25GHZ, 1.57V+70/-70MV, 35W, 85C	U34	CRITICAL	1250@167

CPU BUS RATIO BITS

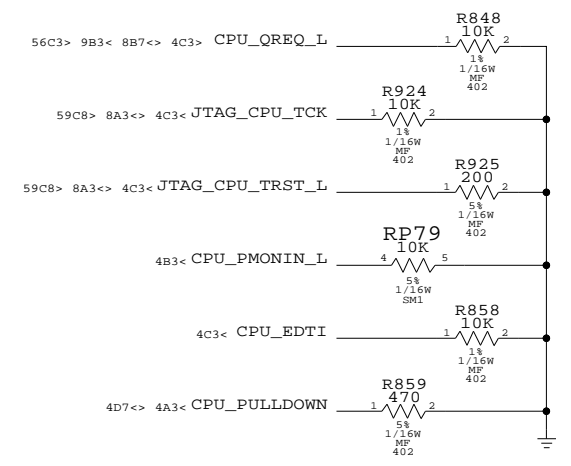
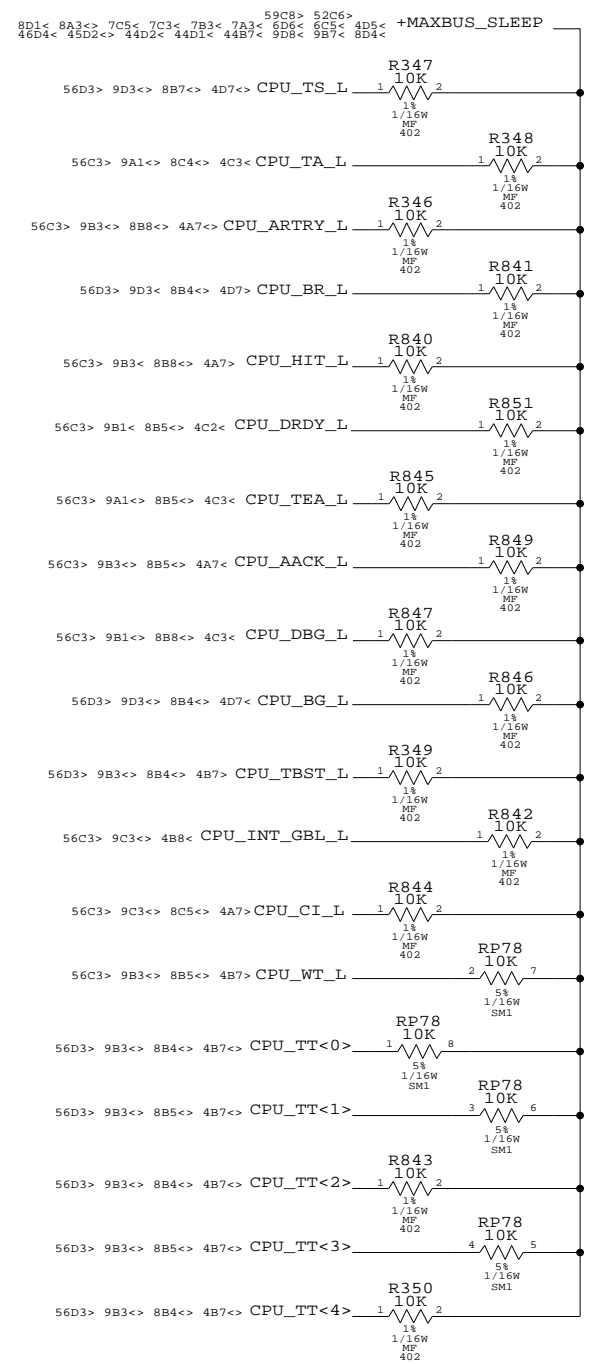
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NONE	6		69

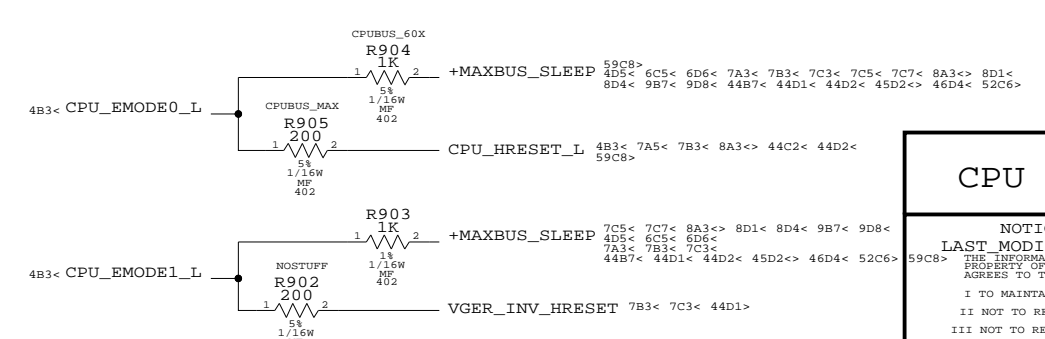
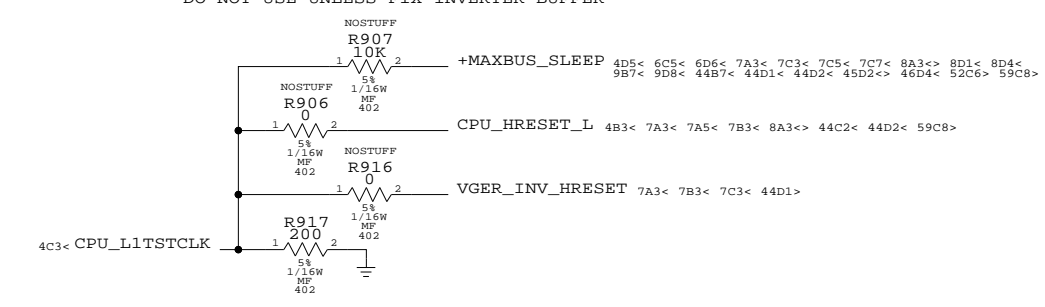
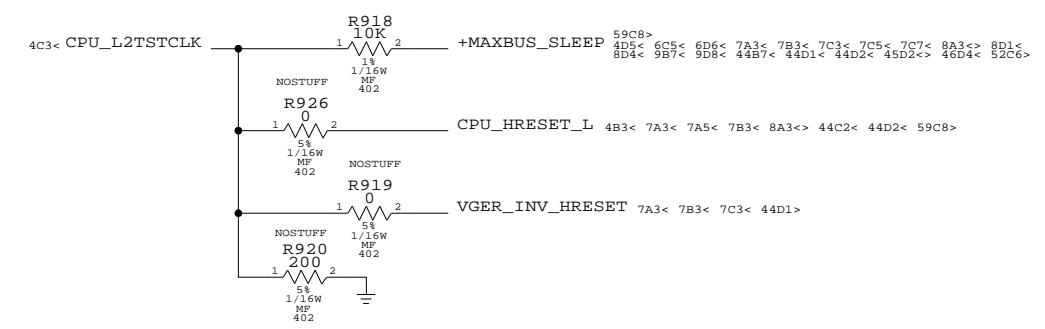
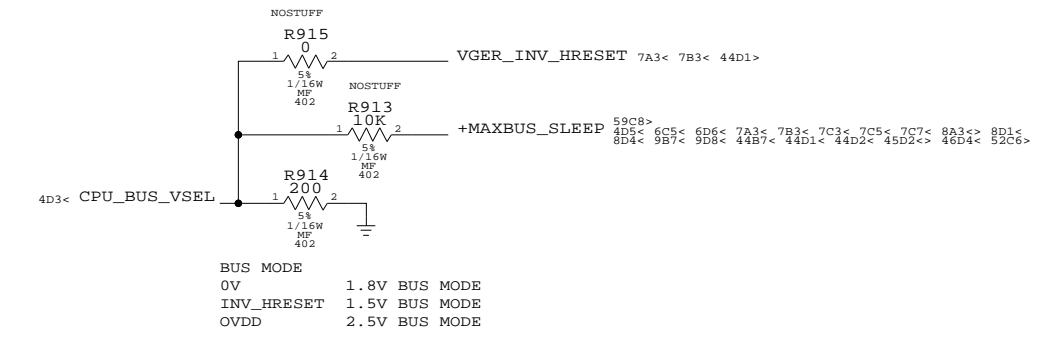
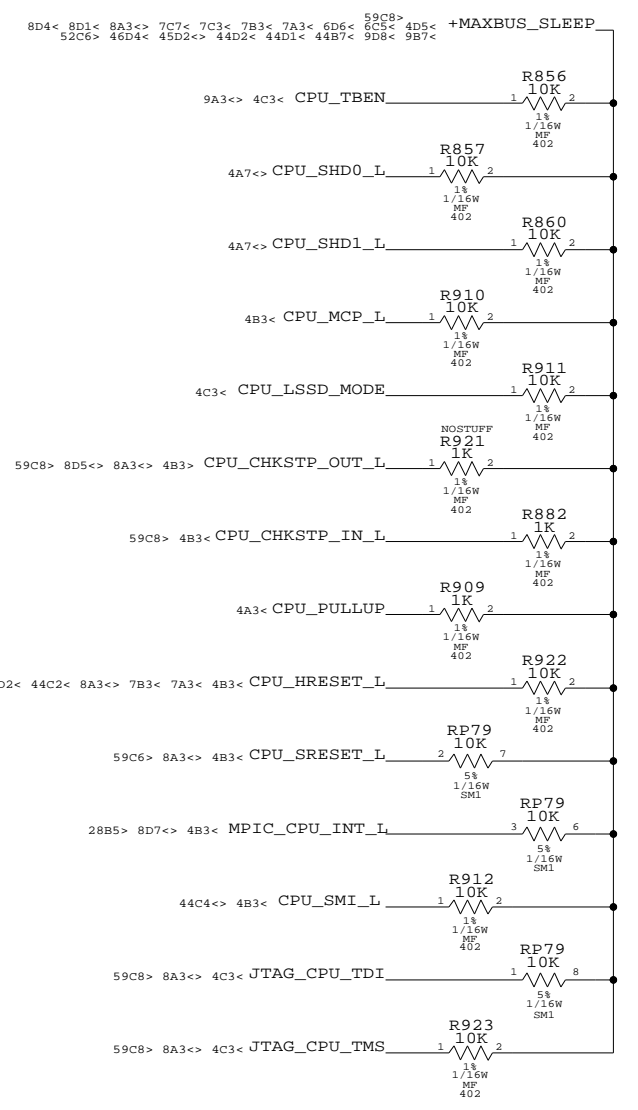
BMODE <0> <1>	MSSCR0 <16:17>	Sys Bus	Vger ID	Addr Drve
L L	1 1	???	01	yes unavail
L !hr	1 0	Max	01	yes unavail
L hr	1 1	???	00	yes unavail
L H	1 0	Max	00	yes unavail
!hr L	0 1	MB+	01	yes unavail
!hr !hr	0 0	60x	01	yes unavail
!hr hr	0 1	MB+	00	yes unavail
!hr H	0 0	60x	00	yes unavail
hr L	1 1	???	01	norm unavail
hr !hr	1 0	Max	01	norm
hr hr	1 1	???	00	norm unavail
HR H	1 0	MAX	00	NORM <- DEFAULT
H L	0 1	MB+	01	norm unavail
H !hr	0 0	60x	01	norm
H hr	0 1	MB+	00	norm unavail
H H	0 0	60x	00	norm

SIGNAL	TIED	APPLICATION
CPU_EMODE0_L	HIGH	60X BUS MODE
CPU_BUS_VSEL	CPU_HRESET_L	MAX BUS MODE
	CPU_HRESET_L	2.5V INTERFACE
	LOW	1.8V INTERFACE
CPU_L3_VSEL	CPU_HRESET_H	1.5V INTERFACE
	CPU_HRESET_L or L3_OVDD	2.5V INTERFACE
CPU_L3_VSEL	LOW	1.8V INTERFACE
	CPU_HRESET_H	1.5V INTERFACE

MAXBUS PULL-UPS



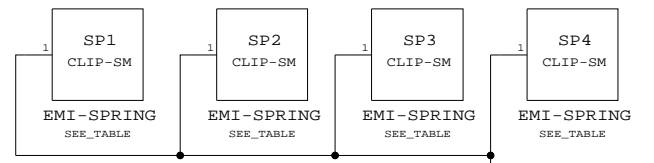
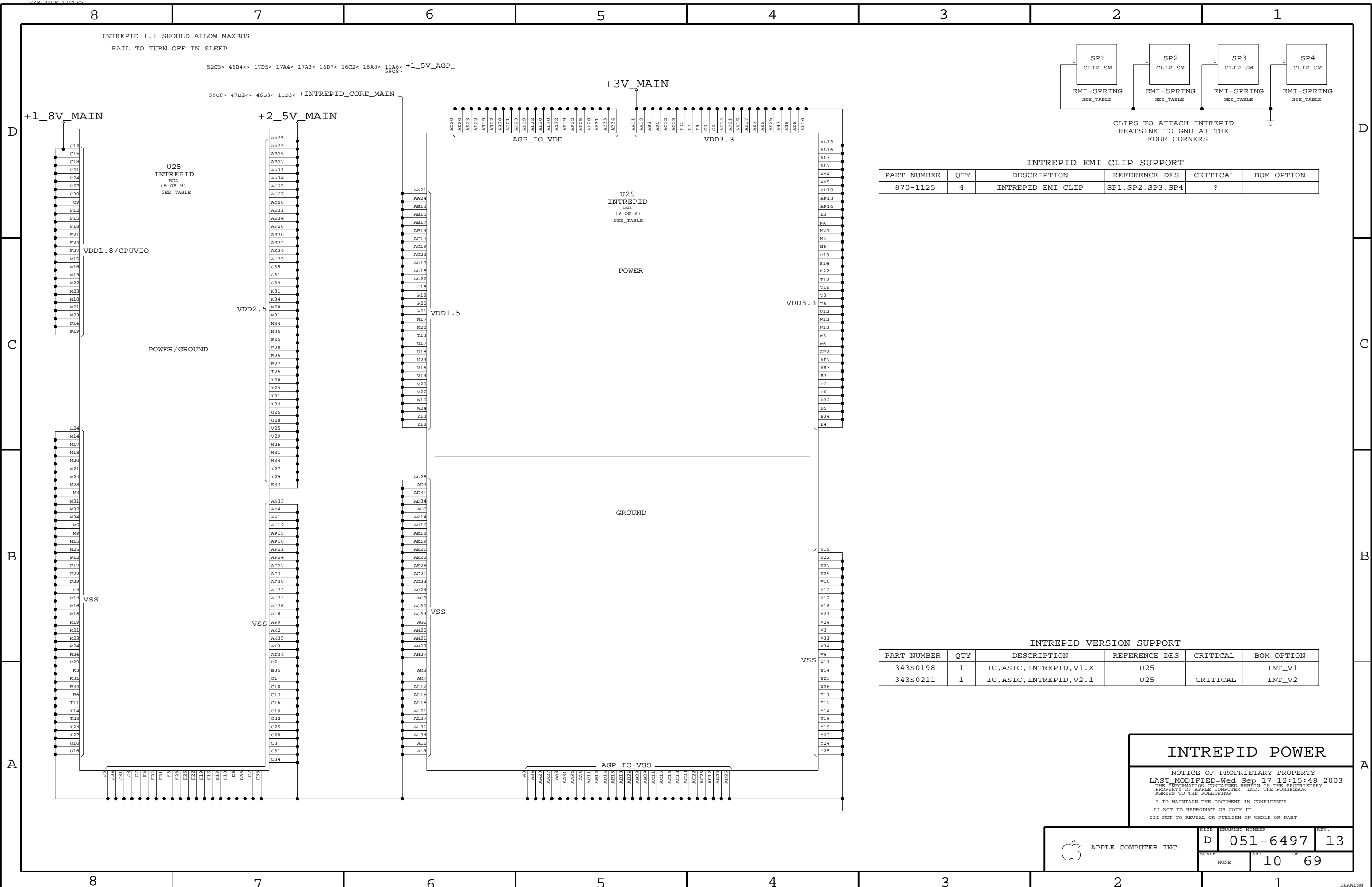
MPC7450 PULL-UPS



CPU CONFIG OPTIONS

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		SHT 7	OF 69



CLIPS TO ATTACH INTREPID HEATSINK TO GND AT THE FOUR CORNERS

INTREPID EMI CLIP SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
870-1125	4	INTREPID EMI CLIP	SP1, SP2, SP3, SP4	?	

INTREPID VERSION SUPPORT

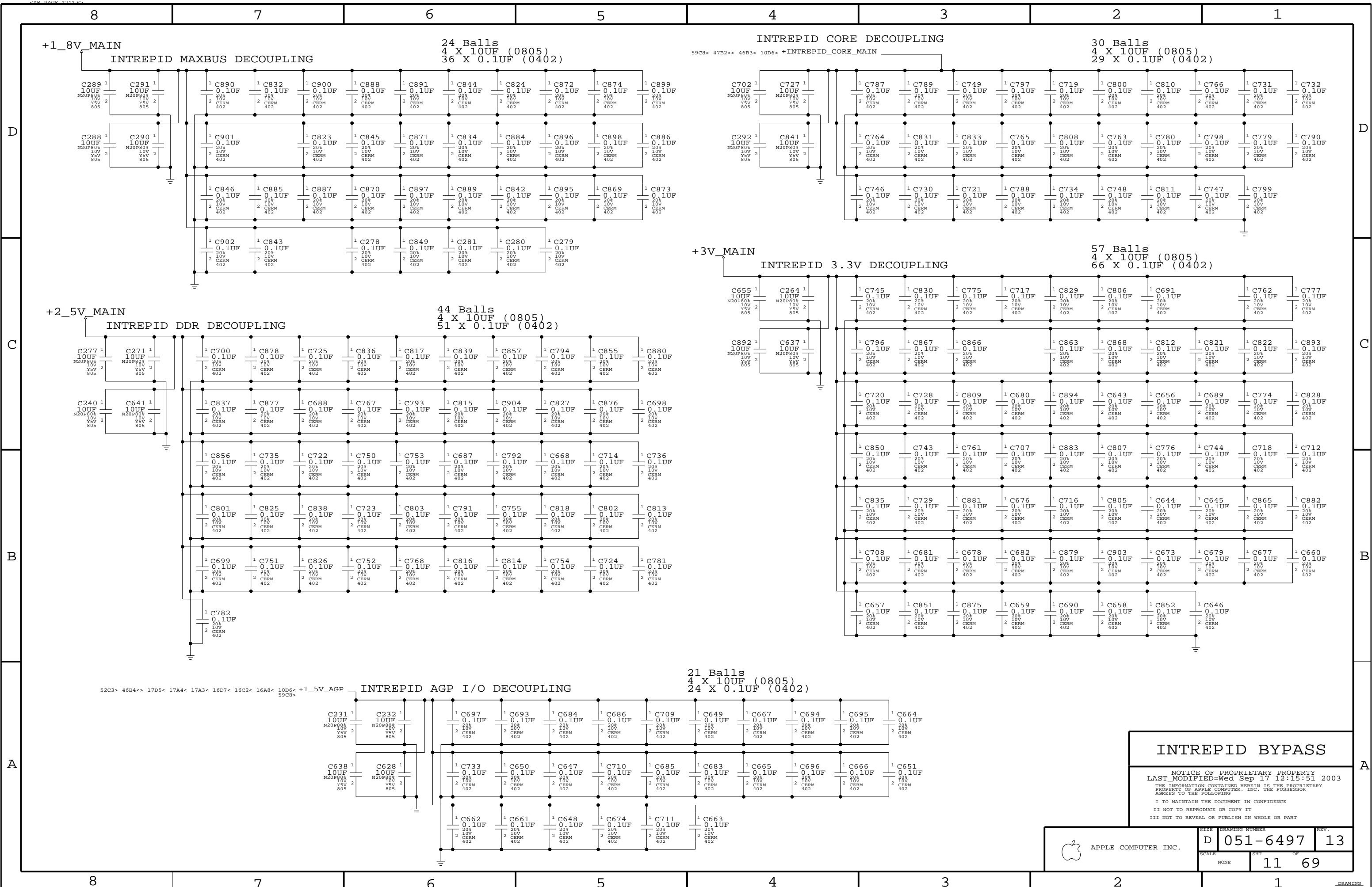
PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
343S0198	1	IC, ASIC, INTREPID, V1.X	U25		INT_V1
343S0211	1	IC, ASIC, INTREPID, V2.1	U25	CRITICAL	INT_V2

INTREPID POWER

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NONE		10	69



INTREPID BYPASS

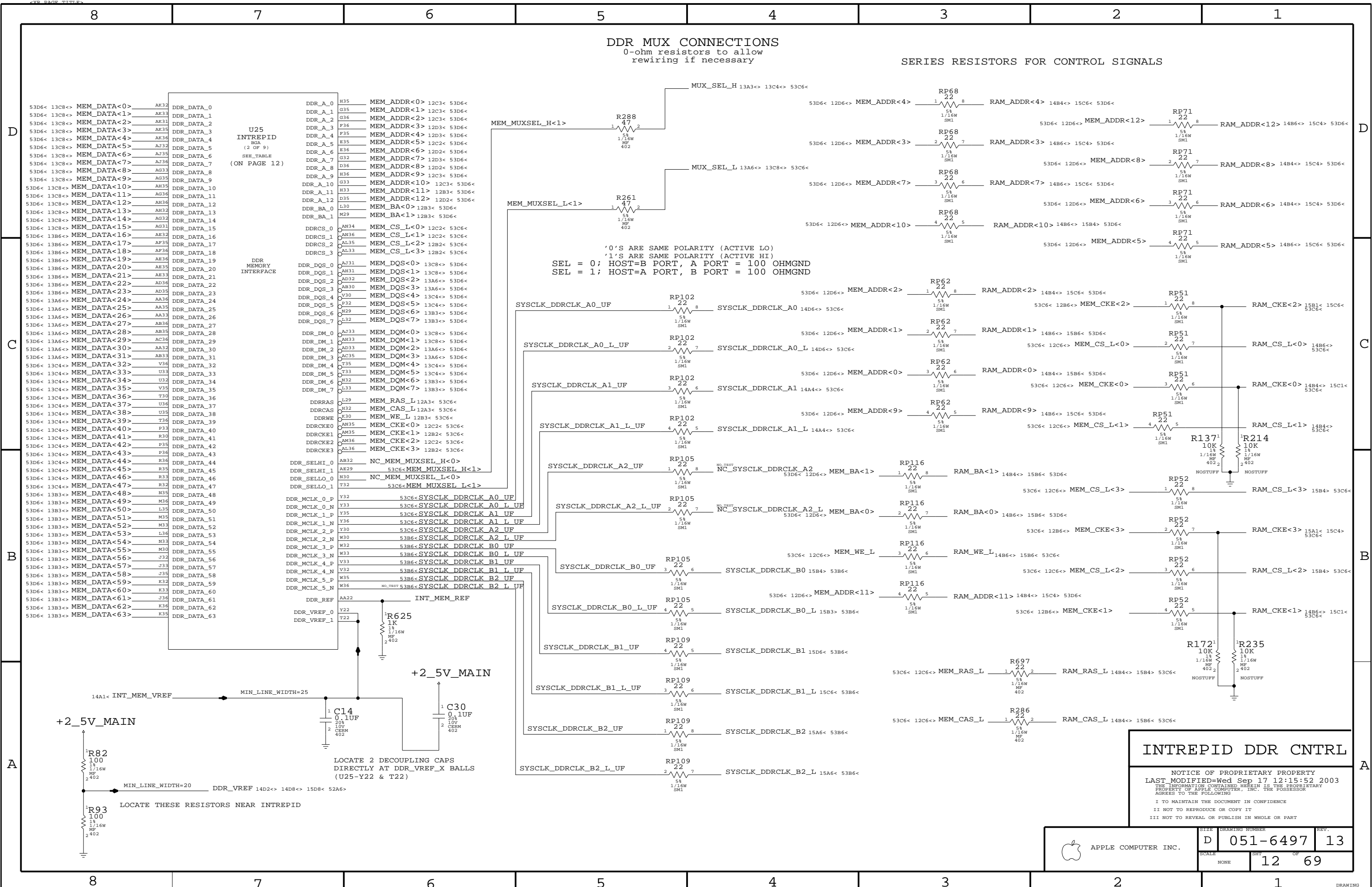
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		SCALE	SHT	OF	
		NONE	11	69	

DDR MUX CONNECTIONS
0-ohm resistors to allow
rewiring if necessary

SERIES RESISTORS FOR CONTROL SIGNALS



'0'S ARE SAME POLARITY (ACTIVE LO)
'1'S ARE SAME POLARITY (ACTIVE HI)
SEL = 0; HOST=B PORT, A PORT = 100 OHMGND
SEL = 1; HOST=A PORT, B PORT = 100 OHMGND

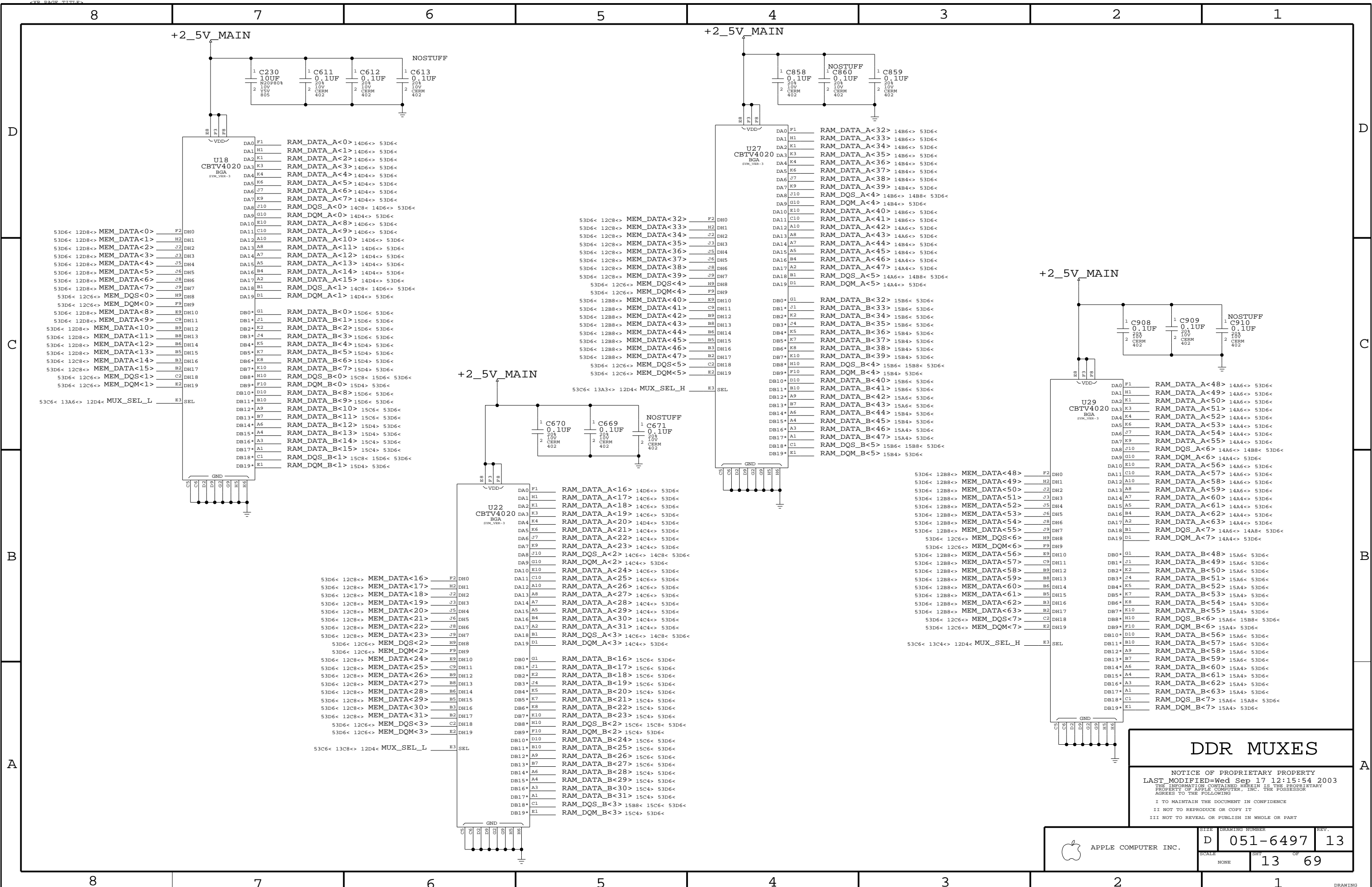
INTREPID DDR CNTRL

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	SCALE	SHT	OF
NONE		12	69



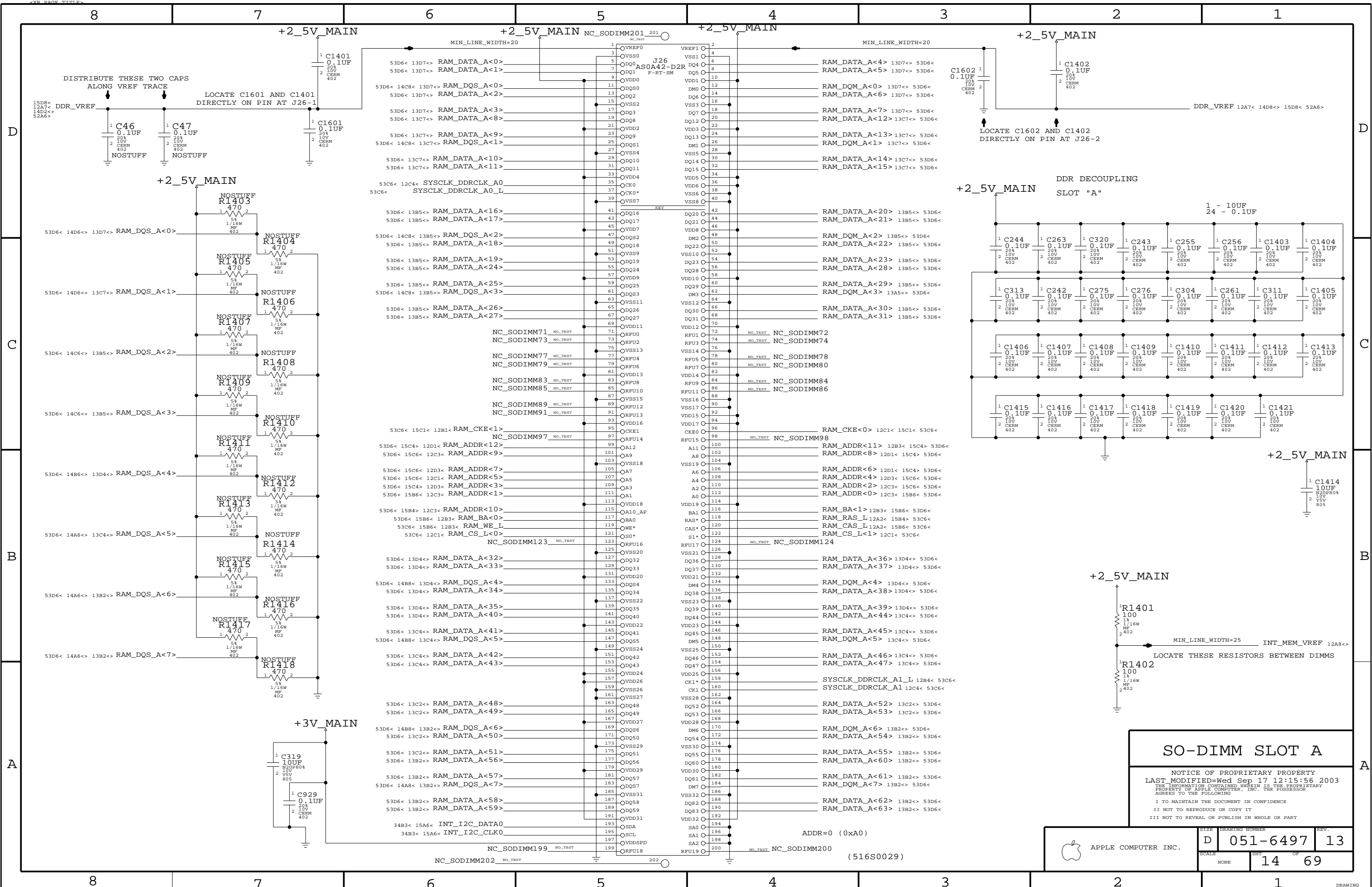
APPLE COMPUTER INC.

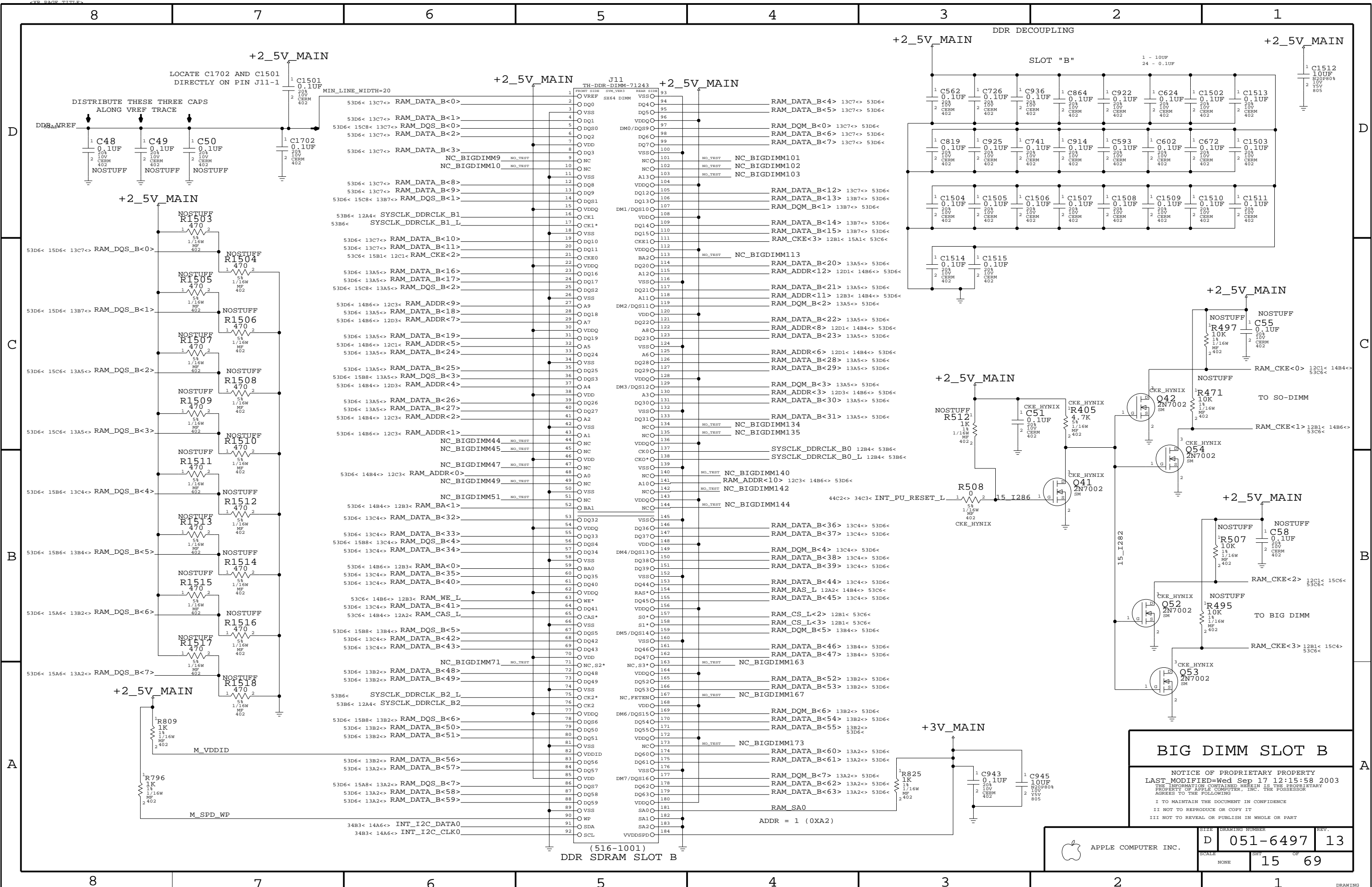


DDR MUXES

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	SCALE: NONE	SHEET: 13	OF: 69





APPLE COMPUTER INC.

D 051-6497 13

SCALE NONE SHEET 15 OF 69

SIZE DRAWING NUMBER REV.

INTREPID AGP CLK IS 1.5V OUT
NEED 3.3V SWING FOR VIDEO CHIPS
VERSION 1 WORKAROUND IS LA CLOCK
VERSION 2 WORKAROUND IS UNUSED PIN

52D3> 30D5< 28D6<> 9D4< +1_5V_INTREPID_PLL

52C3> 46B4<> 17D5< 17A4< 17A3< 16C2< 16A8< 11A6< 10D6< +1_5V_AGP

54A7< 30C5<> 30A7< INT_ROM_OVERLAY_PU

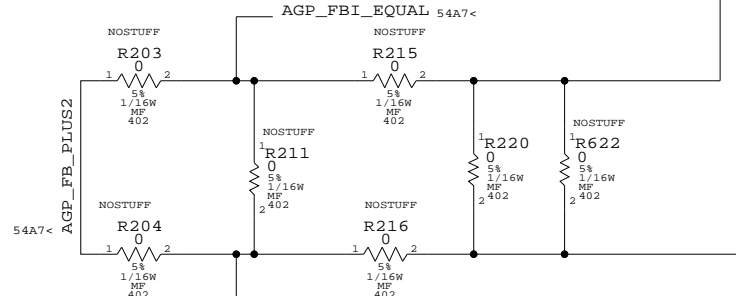
54A7< 17C7< CLK66M_GPU_AGP

THESE RESISTORS
SHARE THE SAME PAD

2" LONGER
(0.5NS SLOWER)

AGP (ZERO DELAY)

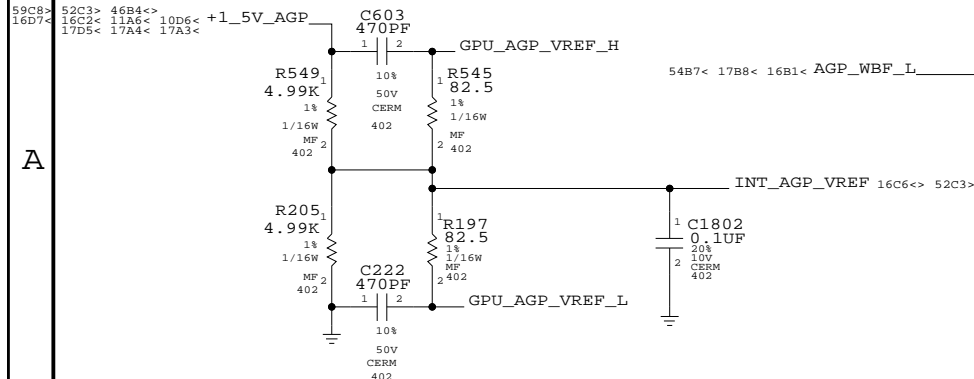
2" SHORTER
(0.5NS FASTER)



PLACE ALL SERPENTINES ON INTERNAL LAYER

GPU AGP I/O REFERENCE

(PLACE CLOSE TO GPU AGP BALLS)



R667 4.7 1/16W MF 402
C760 0.1UF 20% 10V CERM 402
C1801 0.1UF 20% 10V CERM 402

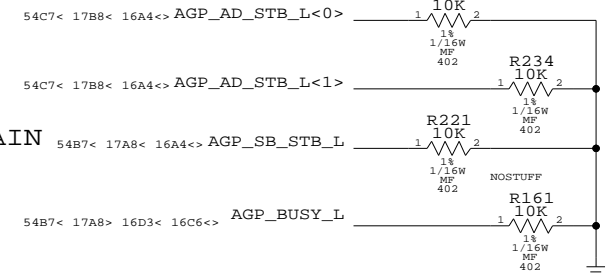
U25
INTREPID
BGA
(3 OF 9)
SEE_TABLE
(ON PAGE 12)

AGP_BUSY
AGP_CLK
AGP_FB_IN
AGP_FB_OUT

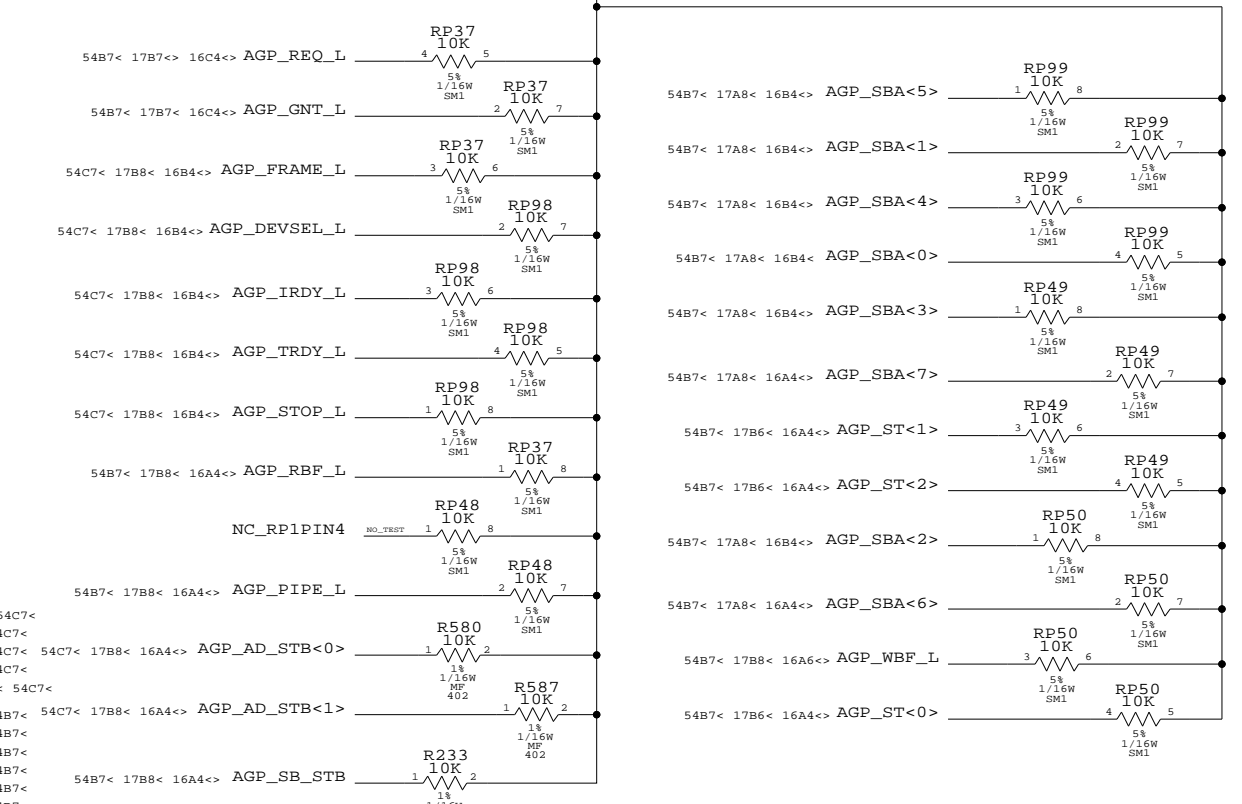
AGP INTERFACES
VOUT = AGPIO (1.5V)
VIN = VCORE (1.5V)

AGPREQ	AT33	AGP_REQ_L	16C3<	17B7<>	54B7<
AGPGNT	AM29	AGP_GNT_L	16C3<	17B7<	54B7<
AGPAD0	AR19	AGP_AD<0>	17D8<	54C7<	
AGPAD1	AM19	AGP_AD<1>	17D8<	54C7<	
AGPAD2	AT20	AGP_AD<2>	17D8<	54C7<	
AGPAD3	AR20	AGP_AD<3>	17D8<	54C7<	
AGPAD4	AT21	AGP_AD<4>	17D8<	54C7<	
AGPAD5	AN20	AGP_AD<5>	17D8<	54C7<	
AGPAD6	AR21	AGP_AD<6>	17D8<	54C7<	
AGPAD7	AN21	AGP_AD<7>	17D8<	54C7<	
AGPAD8	AM21	AGP_AD<8>	17D8<	54C7<	
AGPAD9	AT22	AGP_AD<9>	17D8<	54C7<	
AGPAD10	AR22	AGP_AD<10>	17D8<	54C7<	
AGPAD11	AN22	AGP_AD<11>	17D8<	54C7<	
AGPAD12	AM22	AGP_AD<12>	17D8<	54C7<	
AGPAD13	AT23	AGP_AD<13>	17D8<	54C7<	
AGPAD14	AR23	AGP_AD<14>	17D8<	54C7<	
AGPAD15	AN24	AGP_AD<15>	17D8<	54C7<	
AGPAD16	AM23	AGP_AD<16>	17C8<	54C7<	
AGPAD17	AR24	AGP_AD<17>	17C8<	54C7<	
AGPAD18	AT25	AGP_AD<18>	17C8<	54C7<	
AGPAD19	AM24	AGP_AD<19>	17C8<	54C7<	
AGPAD20	AN25	AGP_AD<20>	17C8<	54C7<	
AGPAD21	AR25	AGP_AD<21>	17C8<	54C7<	
AGPAD22	AL24	AGP_AD<22>	17C8<	54C7<	
AGPAD23	AR26	AGP_AD<23>	17C8<	54C7<	
AGPAD24	AT26	AGP_AD<24>	17C8<	54C7<	
AGPAD25	AM25	AGP_AD<25>	17C8<	54C7<	
AGPAD26	AN26	AGP_AD<26>	17C8<	54C7<	
AGPAD27	AR26	AGP_AD<27>	17C8<	54C7<	
AGPAD28	AT27	AGP_AD<28>	17C8<	54C7<	
AGPAD29	AM28	AGP_AD<29>	17C8<	54C7<	
AGPAD30	AN28	AGP_AD<30>	17C8<	54C7<	
AGPAD31	AN27	AGP_AD<31>	17C8<	54C7<	
AGPCBE_0	AM20	AGP_CBE<0>	17C8<	54C7<	
AGPCBE_1	AT23	AGP_CBE<1>	17C8<	54C7<	
AGPCBE_2	AN24	AGP_CBE<2>	17C8<	54C7<	
AGPCBE_3	AL25	AGP_CBE<3>	17C8<	54C7<	
AGPPAR	AT29	AGP_PAR	17B8<	54B7<	
AGPFRAME	AN28	AGP_FRAME_L	16C3<	17B8<	54C7<
AGPTRDY	AR29	AGP_TRDY_L	16B3<	17B8<	54C7<
AGPIRDY	AT28	AGP_IRDY_L	16C3<	17B8<	54C7<
AGPSTOP	AM28	AGP_STOP_L	16B3<	17B8<	54C7<
AGPDEVSEL	AM27	AGP_DEVSEL_L	16C3<	17B8<	54C7<
AGP_SBA0	AT32	AGP_SBA<0>	16C1<	17A8<	54B7<
AGP_SBA1	AR32	AGP_SBA<1>	16C1<	17A8<	54B7<
AGP_SBA2	AM31	AGP_SBA<2>	16B1<	17A8<	54B7<
AGP_SBA3	AN31	AGP_SBA<3>	16C1<	17A8<	54B7<
AGP_SBA4	AR31	AGP_SBA<4>	16C1<	17A8<	54B7<
AGP_SBA5	AT31	AGP_SBA<5>	16C1<	17A8<	54B7<
AGP_SBA6	AM30	AGP_SBA<6>	16B1<	17A8<	54B7<
AGP_SBA7	AN30	AGP_SBA<7>	16B1<	17A8<	54B7<
AGP_SB_STB_P	AN25	AGP_SB_STB	16B3<	17B8<	54B7<
AGP_SB_STB_N	AG25	AGP_SB_STB_L	16D1<	17A8<	54B7<
AGP_ST0	AN29	AGP_ST<0>	16B1<	17B6<	54B7<
AGP_ST1	AT30	AGP_ST<1>	16B1<	17B6<	54B7<
AGP_ST2	AR30	AGP_ST<2>	16B1<	17B6<	54B7<
AGP_AD_STB0_P	AK20	AGP_AD_STB<0>	16B3<	17B8<	54C7<
AGP_AD_STB0_N	AK19	AGP_AD_STB_L<0>	16D1<	17B8<	54C7<
AGP_AD_STB1_P	AK21	AGP_AD_STB<1>	16B3<	17B8<	54C7<
AGP_AD_STB1_N	AK22	AGP_AD_STB_L<1>	16D1<	17B8<	54C7<
AGPIPE	AL29	AGP_PIPE_L	16B3<	17B8<	54B7<
AGPRBF	AK24	AGP_RBF_L	16B3<	17B8<	54B7<

AGP PULL-UPS/PULL DOWNS



52C3> 46B4<> 17D5< 17A4< 17A3< 16D7< 16A8< 11A6< 10D6< +1_5V_AGP

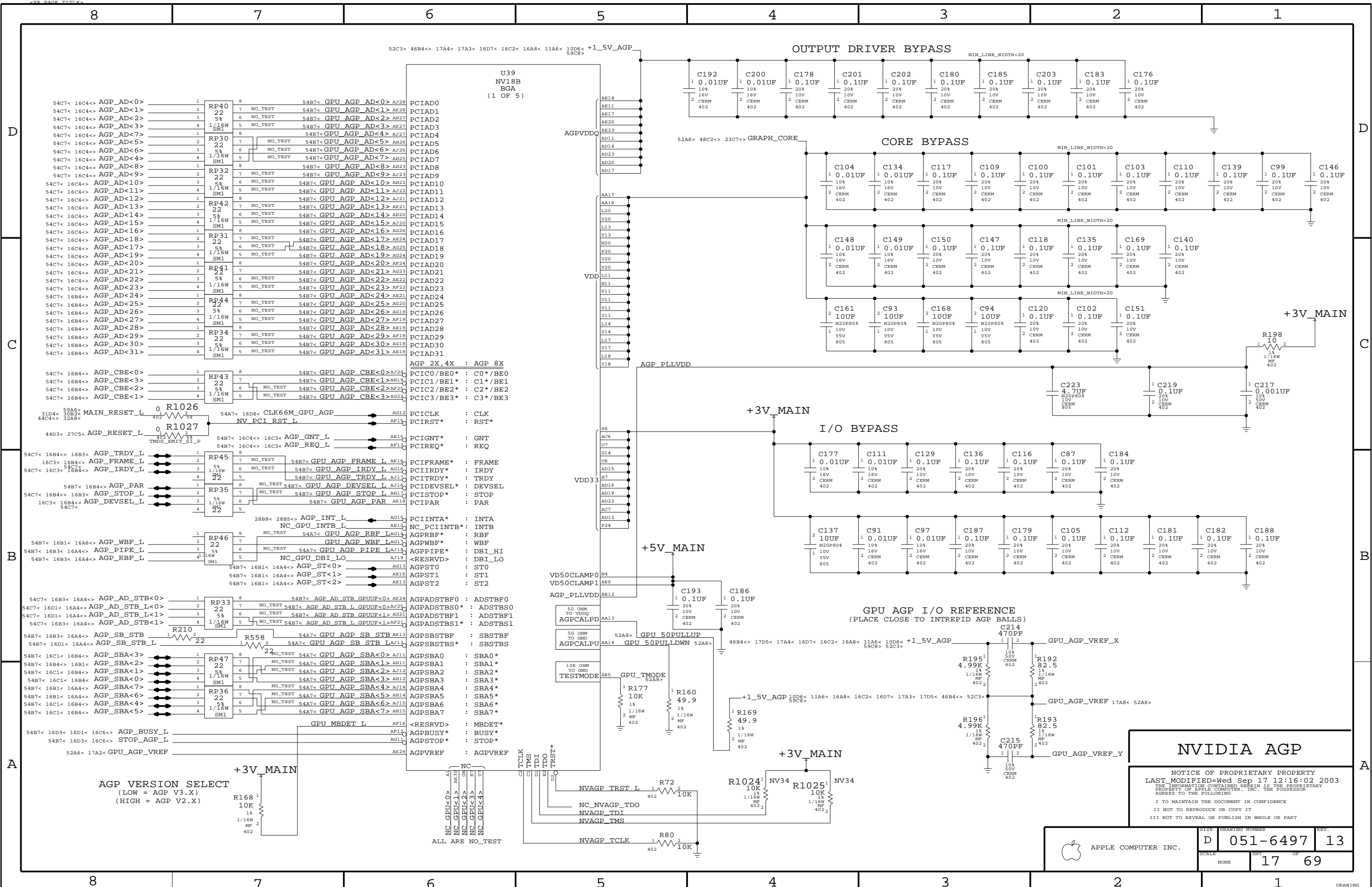


INTREPID AGP

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NONE	16	69	



U39
NV18B
BGA
(1 OF 5)

AGP AD<0>	PCIAD0
AGP AD<1>	PCIAD1
AGP AD<2>	PCIAD2
AGP AD<3>	PCIAD3
AGP AD<4>	PCIAD4
AGP AD<5>	PCIAD5
AGP AD<6>	PCIAD6
AGP AD<7>	PCIAD7
AGP AD<8>	PCIAD8
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AGP AD<11>	PCIAD11
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AGP AD<24>	PCIAD24
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AGP AD<26>	PCIAD26
AGP AD<27>	PCIAD27
AGP AD<28>	PCIAD28
AGP AD<29>	PCIAD29
AGP AD<30>	PCIAD30
AGP AD<31>	PCIAD31

AGP CBE<0>	PCIC0/BE0*	: C0*/BE0
AGP CBE<1>	PCIC1/BE1*	: C1*/BE1
AGP CBE<2>	PCIC2/BE2*	: C2*/BE2
AGP CBE<3>	PCIC3/BE3*	: C3*/BE3

AGP INT*	: INTA
AGP INTB*	: INTB
AGP RBF*	: RBF
AGP WBF*	: WBF
AGP PIPE*	: DBI_HI
AGP DBI_LO	: DBI_LO
AGP ST0	: ST0
AGP ST1	: ST1
AGP ST2	: ST2

AGP AD STB<0>	AGPADSTBF0	: ADSTBF0
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AGP AD STB<30>	AGPADSTBF30	: ADSTBF30
AGP AD STB<31>	AGPADSTBF31	: ADSTBF31

AGP SBA<0>	AGPSBA0	: SBA0*
AGP SBA<1>	AGPSBA1	: SBA1*
AGP SBA<2>	AGPSBA2	: SBA2*
AGP SBA<3>	AGPSBA3	: SBA3*
AGP SBA<4>	AGPSBA4	: SBA4*
AGP SBA<5>	AGPSBA5	: SBA5*
AGP SBA<6>	AGPSBA6	: SBA6*
AGP SBA<7>	AGPSBA7	: SBA7*

AGP MBDET	AGPMBDET*	: MBDET*
AGP BUSY	AGPBUSY*	: BUSY*
AGP STOP	AGPSTOP*	: STOP*
AGP VREF	AGPVREF	: AGPVREF

NVIDIA AGP

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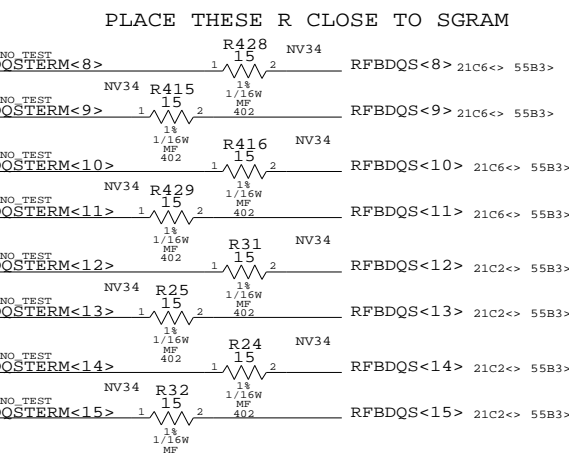
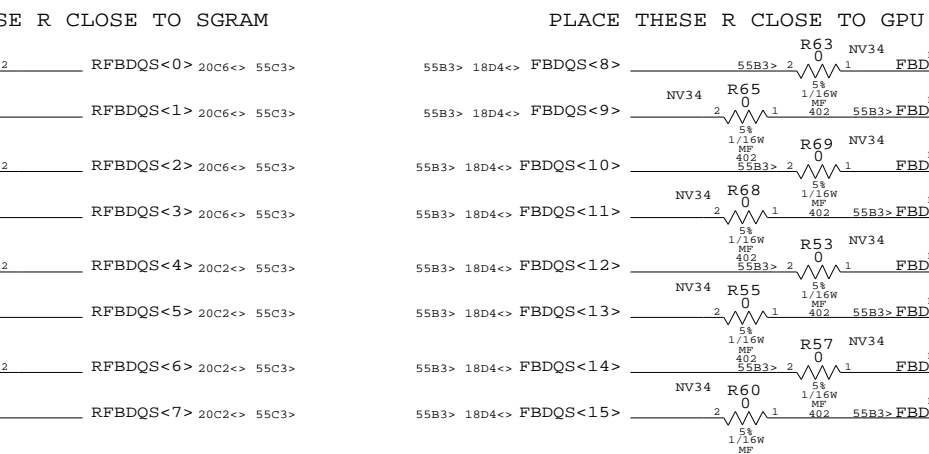
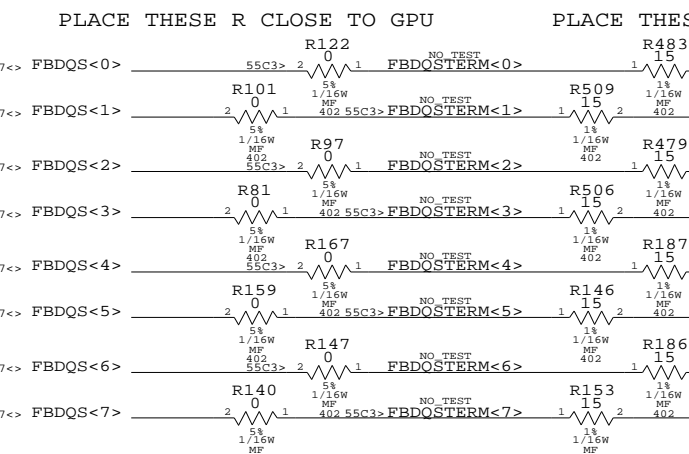
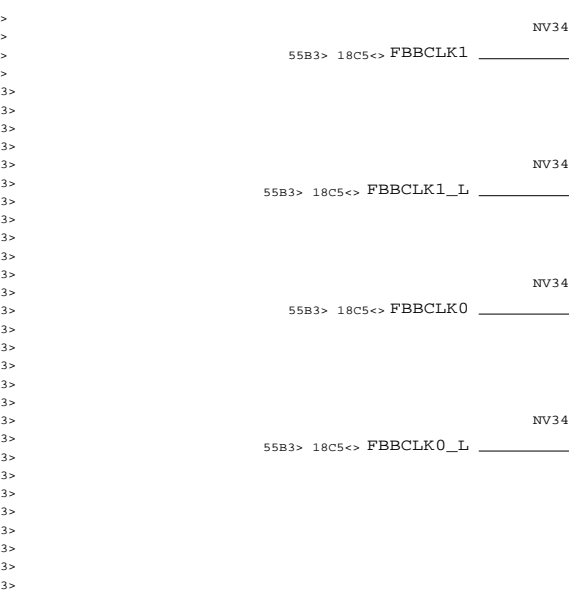
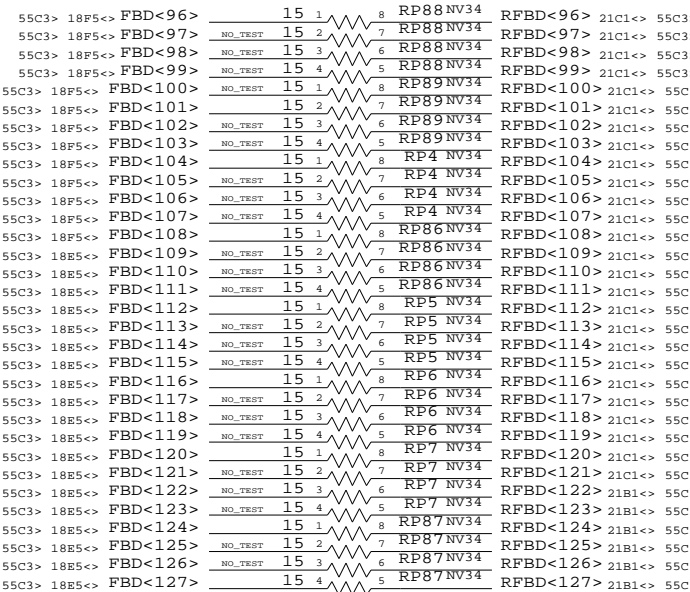
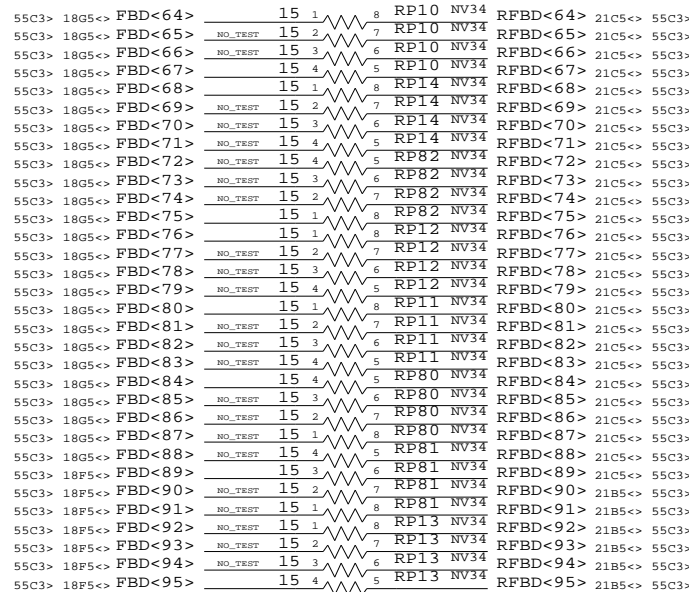
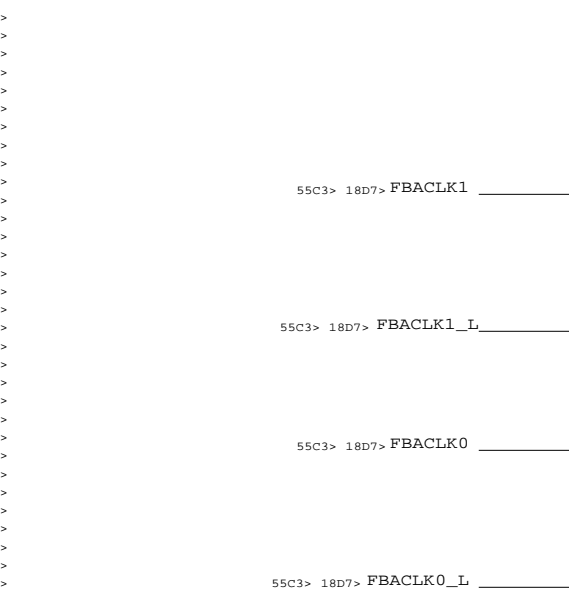
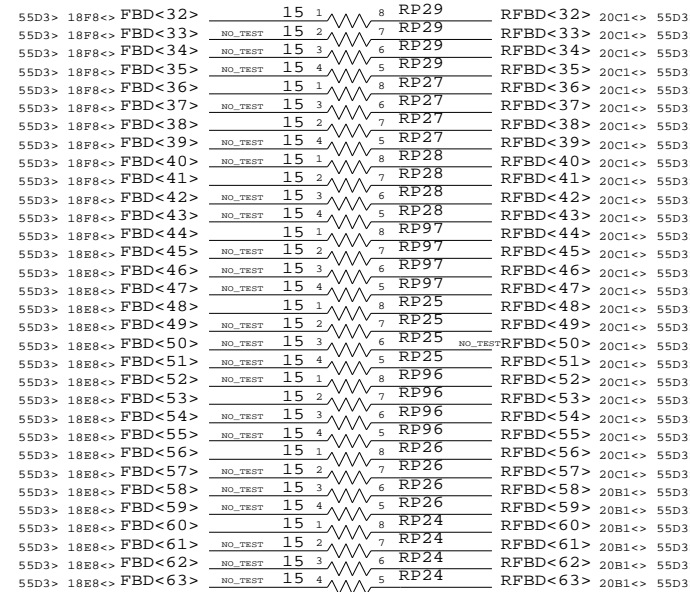
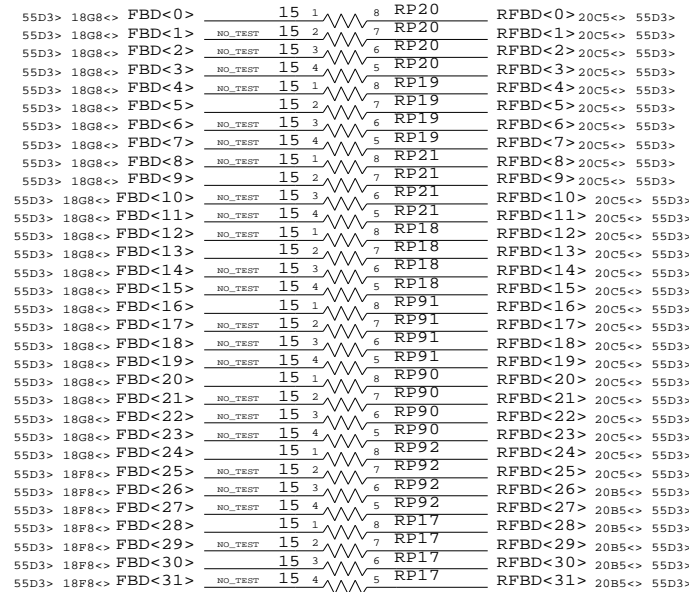
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SCALE	SHEET	DRAWING NUMBER	REV.
			13
NONE	17	OF	69

DRAWING



PLACE R'S BETWEEN GPU & MEMORY



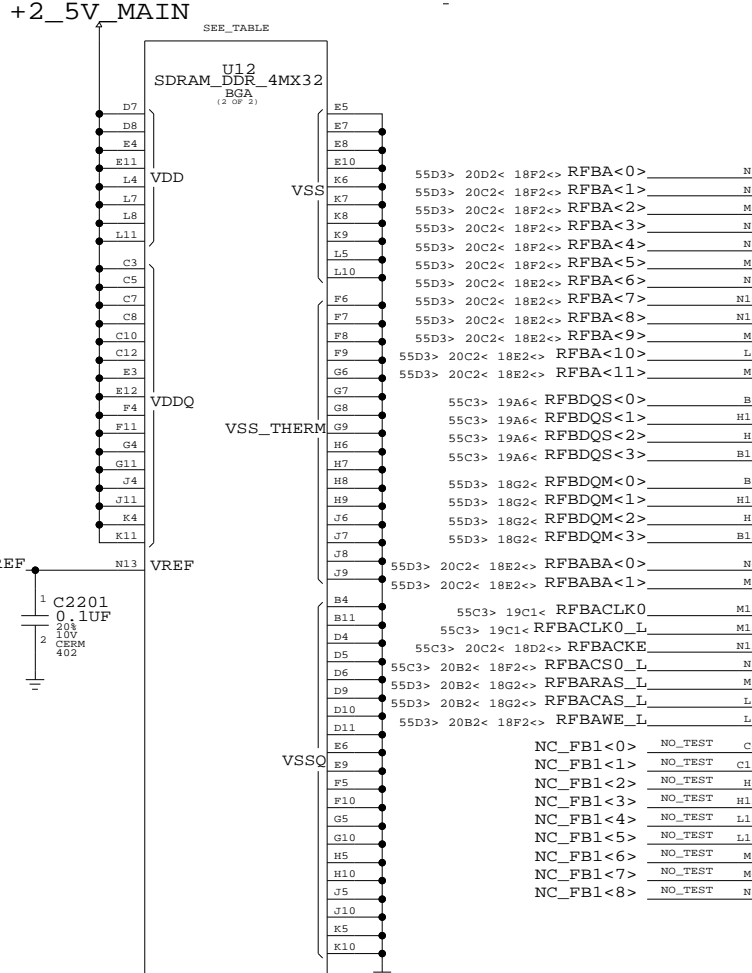
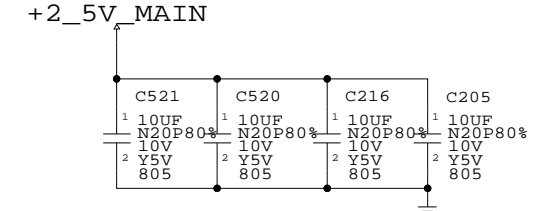
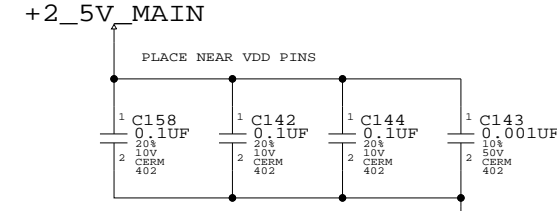
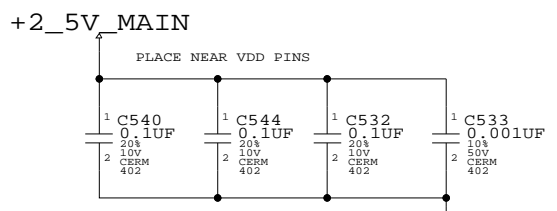
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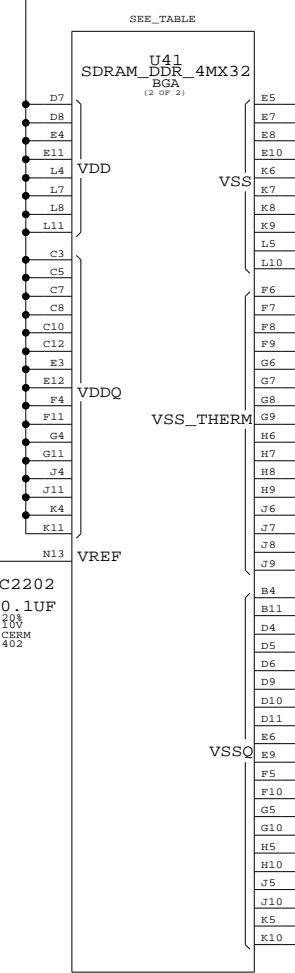
APPLE COMPUTER INC.

SIZE	DRAWING NUMBER	REV.
D	051-6497	13
SCALE	SHT	OF
NONE	19	69



SEE_TABLE

Pin	Signal	Pin	Signal
D00	B7	RFBD<0>	19D7< 55D3>
D01	C6	RFBD<1>	19D7< 55D3>
D02	B6	RFBD<2>	19D7< 55D3>
D03	B5	RFBD<3>	19D7< 55D3>
D04	C2	RFBD<4>	19D7< 55D3>
D05	D3	RFBD<5>	19D7< 55D3>
D06	D2	RFBD<6>	19D7< 55D3>
D07	E2	RFBD<7>	19D7< 55D3>
D08	K13	RFBD<8>	19D7< 55D3>
D09	K12	RFBD<9>	19D7< 55D3>
DQ10	J13	RFBD<10>	19D7< 55D3>
DQ11	J12	RFBD<11>	19D7< 55D3>
DQ12	G13	RFBD<12>	19D7< 55D3>
DQ13	G12	RFBD<13>	19D7< 55D3>
DQ14	F13	RFBD<14>	19D7< 55D3>
DQ15	F12	RFBD<15>	19D7< 55D3>
DQ16	F3	RFBD<16>	19D7< 55D3>
DQ17	F2	RFBD<17>	19D7< 55D3>
DQ18	G3	RFBD<18>	19D7< 55D3>
DQ19	G2	RFBD<19>	19C7< 55D3>
DQ20	J3	RFBD<20>	19C7< 55D3>
DQ21	J2	RFBD<21>	19C7< 55D3>
DQ22	K2	RFBD<22>	19C7< 55D3>
DQ23	K1	RFBD<23>	19C7< 55D3>
DQ24	E13	RFBD<24>	19C7< 55D3>
DQ25	D13	RFBD<25>	19C7< 55D3>
DQ26	D12	RFBD<26>	19C7< 55D3>
DQ27	C13	RFBD<27>	19C7< 55D3>
DQ28	B10	RFBD<28>	19C7< 55D3>
DQ29	B9	RFBD<29>	19C7< 55D3>
DQ30	C9	RFBD<30>	19C7< 55D3>
DQ31	B8	RFBD<31>	19C7< 55D3>

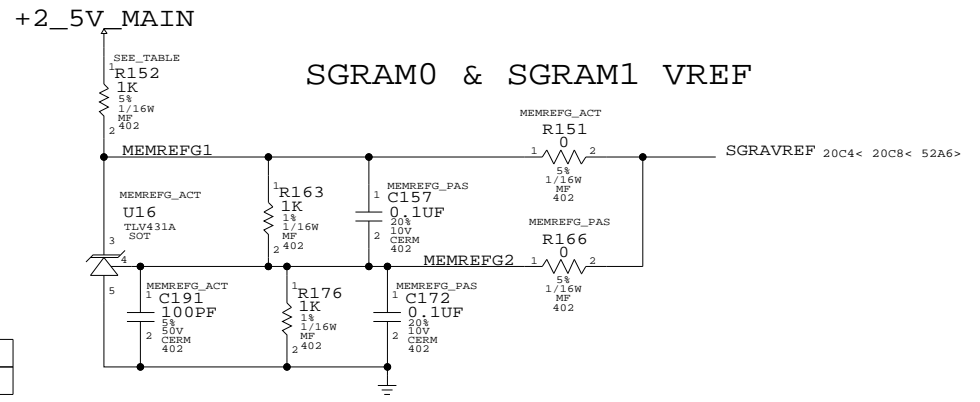
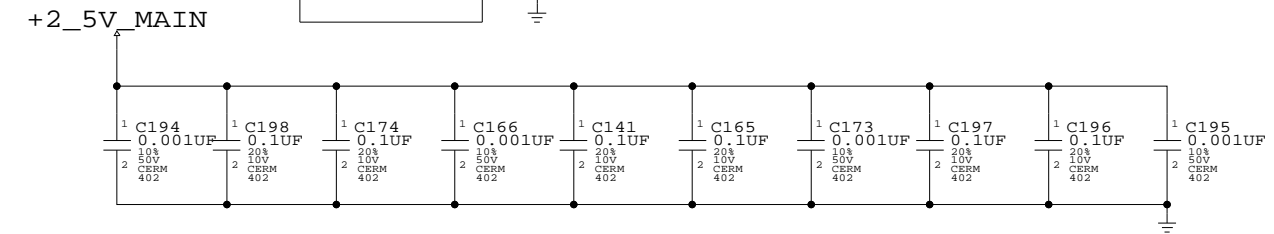
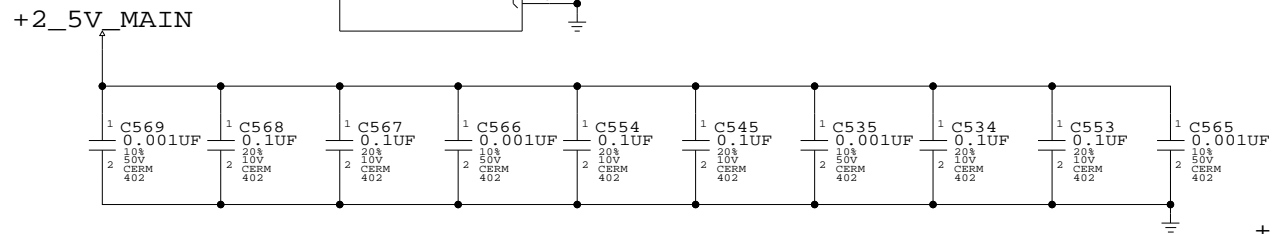


SEE_TABLE

Pin	Signal	Pin	Signal
D00	B7	RFBD<32>	19D4< 55D3>
D01	C6	RFBD<33>	19D4< 55D3>
D02	B6	RFBD<34>	19D4< 55D3>
D03	B5	RFBD<35>	19D4< 55D3>
D04	C2	RFBD<36>	19D4< 55D3>
D05	D3	RFBD<37>	19D4< 55D3>
D06	D2	RFBD<38>	19D4< 55D3>
D07	E2	RFBD<39>	19D4< 55D3>
D08	K13	RFBD<40>	19D4< 55D3>
D09	K12	RFBD<41>	19D4< 55D3>
DQ10	J13	RFBD<42>	19D4< 55D3>
DQ11	J12	RFBD<43>	19D4< 55D3>
DQ12	G13	RFBD<44>	19D4< 55D3>
DQ13	G12	RFBD<45>	19D4< 55D3>
DQ14	F13	RFBD<46>	19D4< 55D3>
DQ15	F12	RFBD<47>	19D4< 55D3>
DQ16	F3	RFBD<48>	19D4< 55D3>
DQ17	F2	RFBD<49>	19D4< 55D3>
DQ18	G3	RFBD<50>	19D4< 55D3>
DQ19	G2	RFBD<51>	19C4< 55D3>
DQ20	J3	RFBD<52>	19C4< 55D3>
DQ21	J2	RFBD<53>	19C4< 55D3>
DQ22	K2	RFBD<54>	19C4< 55D3>
DQ23	K1	RFBD<55>	19C4< 55D3>
DQ24	E13	RFBD<56>	19C4< 55D3>
DQ25	D13	RFBD<57>	19C4< 55D3>
DQ26	D12	RFBD<58>	19C4< 55D3>
DQ27	C13	RFBD<59>	19C4< 55D3>
DQ28	B10	RFBD<60>	19C4< 55D3>
DQ29	B9	RFBD<61>	19C4< 55D3>
DQ30	C9	RFBD<62>	19C4< 55D3>
DQ31	B8	RFBD<63>	19C4< 55D3>

SEE_TABLE

Pin	Signal	Pin	Signal
DQ0	B7	RFBD<32>	19D4< 55D3>
DQ1	C6	RFBD<33>	19D4< 55D3>
DQ2	B6	RFBD<34>	19D4< 55D3>
DQ3	B5	RFBD<35>	19D4< 55D3>
DQ4	C2	RFBD<36>	19D4< 55D3>
DQ5	D3	RFBD<37>	19D4< 55D3>
DQ6	D2	RFBD<38>	19D4< 55D3>
DQ7	E2	RFBD<39>	19D4< 55D3>
DQ8	K13	RFBD<40>	19D4< 55D3>
DQ9	K12	RFBD<41>	19D4< 55D3>
DQ10	J13	RFBD<42>	19D4< 55D3>
DQ11	J12	RFBD<43>	19D4< 55D3>
DQ12	G13	RFBD<44>	19D4< 55D3>
DQ13	G12	RFBD<45>	19D4< 55D3>
DQ14	F13	RFBD<46>	19D4< 55D3>
DQ15	F12	RFBD<47>	19D4< 55D3>
DQ16	F3	RFBD<48>	19D4< 55D3>
DQ17	F2	RFBD<49>	19D4< 55D3>
DQ18	G3	RFBD<50>	19D4< 55D3>
DQ19	G2	RFBD<51>	19C4< 55D3>
DQ20	J3	RFBD<52>	19C4< 55D3>
DQ21	J2	RFBD<53>	19C4< 55D3>
DQ22	K2	RFBD<54>	19C4< 55D3>
DQ23	K1	RFBD<55>	19C4< 55D3>
DQ24	E13	RFBD<56>	19C4< 55D3>
DQ25	D13	RFBD<57>	19C4< 55D3>
DQ26	D12	RFBD<58>	19C4< 55D3>
DQ27	C13	RFBD<59>	19C4< 55D3>
DQ28	B10	RFBD<60>	19C4< 55D3>
DQ29	B9	RFBD<61>	19C4< 55D3>
DQ30	C9	RFBD<62>	19C4< 55D3>
DQ31	B8	RFBD<63>	19C4< 55D3>



SGRAM0 & SGRAM1 MEMORY SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
333S0249	2	SDRAM, 4MX32, DDR, 275MHZ	U12, U41	CRITICAL	SAMSUNG_275_32M
333S0250	2	SDRAM, 4MX32, DDR, 275MHZ	U12, U41	CRITICAL	HYNIX_275_32M
333S0251	2	SDRAM, 4MX32, DDR, 300MHZ	U12, U41	CRITICAL	SAMSUNG_300_32M
333S0252	2	SDRAM, 4MX32, DDR, 300MHZ	U12, U41	CRITICAL	HYNIX_300_32M

SGRAM0 & SGRAM1 DDR MEMORY REFERENCE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S1103	1	RES, 1K-OHM, 5%, 1/16W, 0402	R152	CRITICAL	MEMREFG_ACT
116S1000	1	RES, 0-OHM, 5%, 1/16W, 0402	R152		MEMREFG_PAS

SGRAM0 & SGRAM1

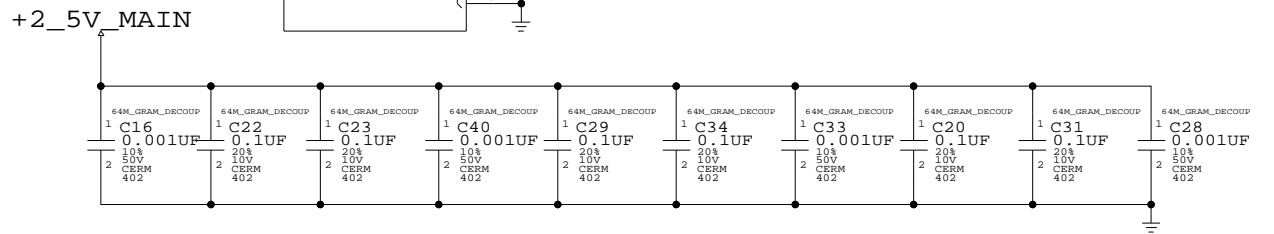
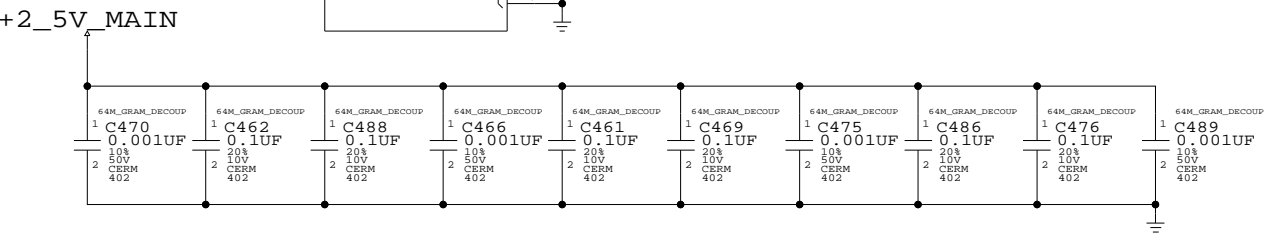
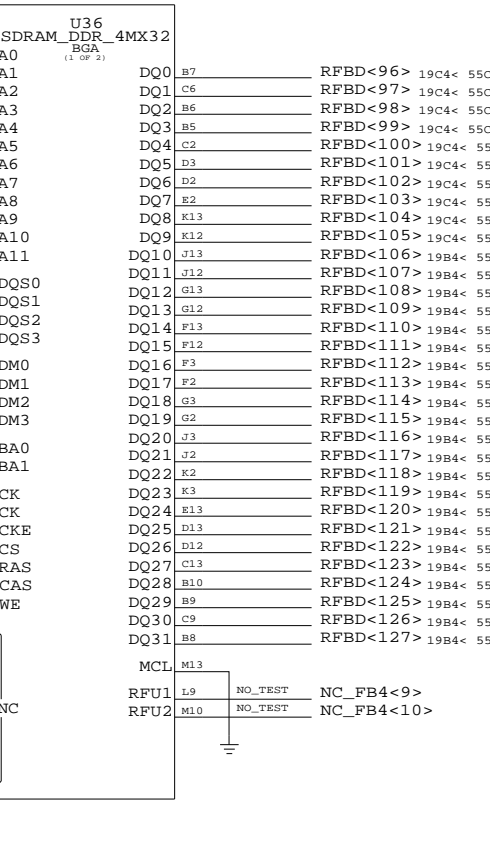
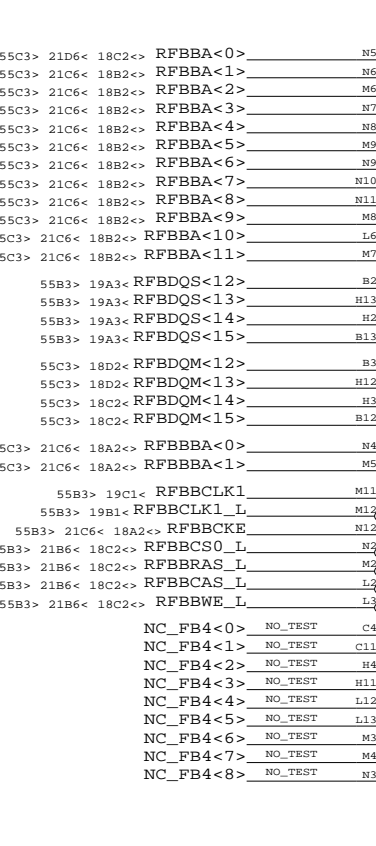
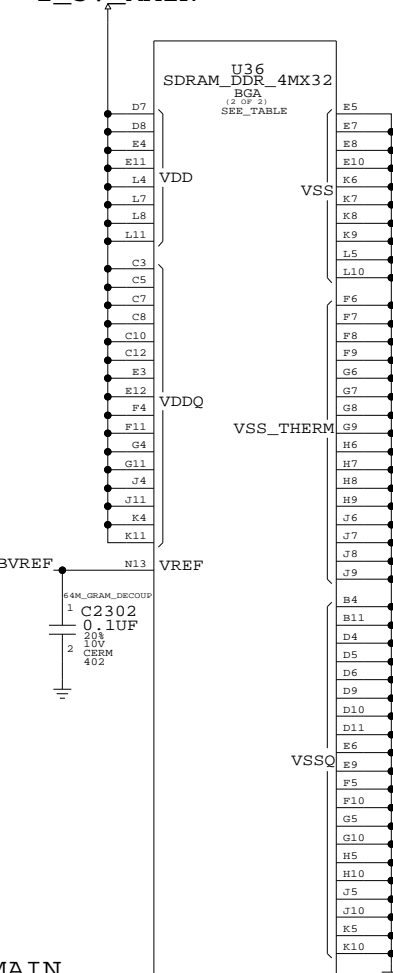
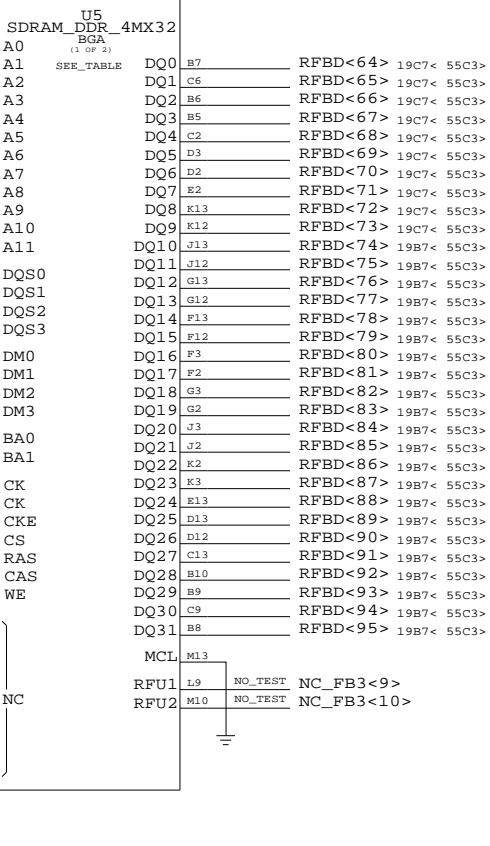
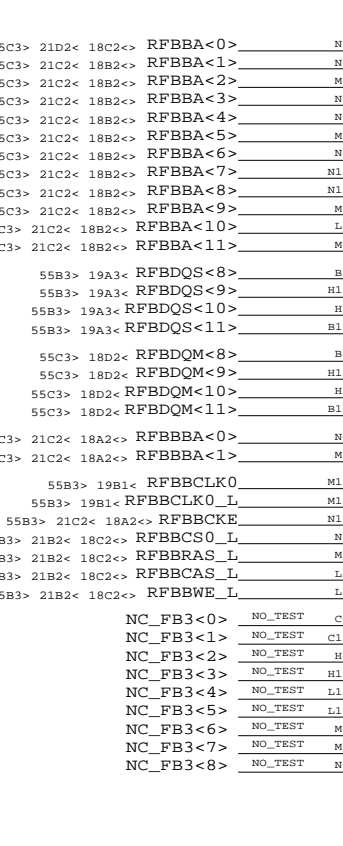
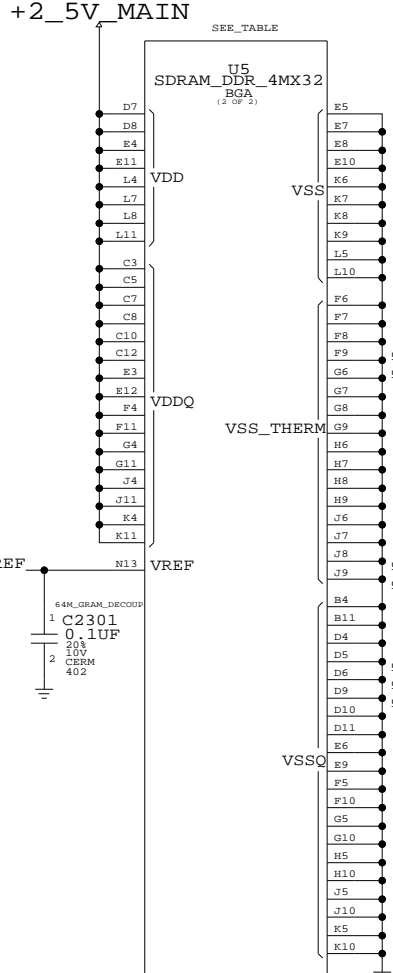
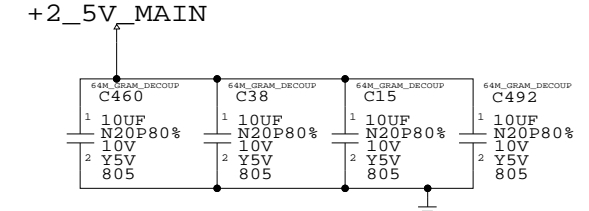
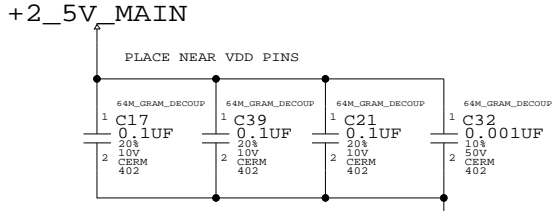
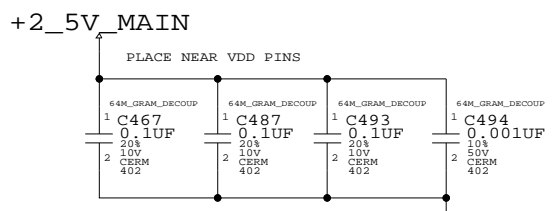
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D 051-6497 13

SCALE NONE SHEET 20 OF 69

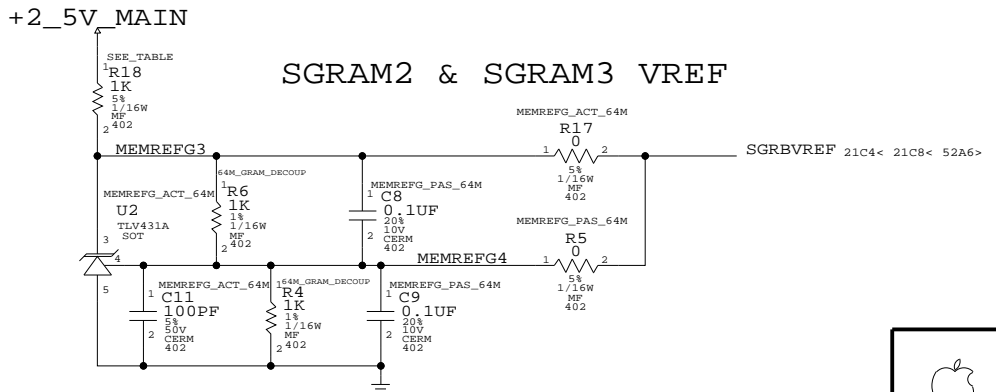


SGRAM0 & SGRAM1 MEMORY SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
333S0249	2	SDRAM, 4MX32, DDR, 275MHZ	U5,U36	CRITICAL	SAMSUNG_275_64M
333S0250	2	SDRAM, 4MX32, DDR, 275MHZ	U5,U36	CRITICAL	HYNIX_275_64M
333S0251	2	SDRAM, 4MX32, DDR, 300MHZ	U5,U36	CRITICAL	SAMSUNG_300_64M
333S0252	2	SDRAM, 4MX32, DDR, 300MHZ	U5,U36	CRITICAL	HYNIX_300_64M

SGRAM2 & SGRAM3 DDR MEMORY REFERENCE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
116S1103	1	RES, 1K-OHM, 5%, 1/16W, 0402	R18	CRITICAL	MEMREFG_ACT_64M
116S1000	1	RES, 0-OHM, 5%, 1/16W, 0402	R18		MEMREFG_PAS_64M



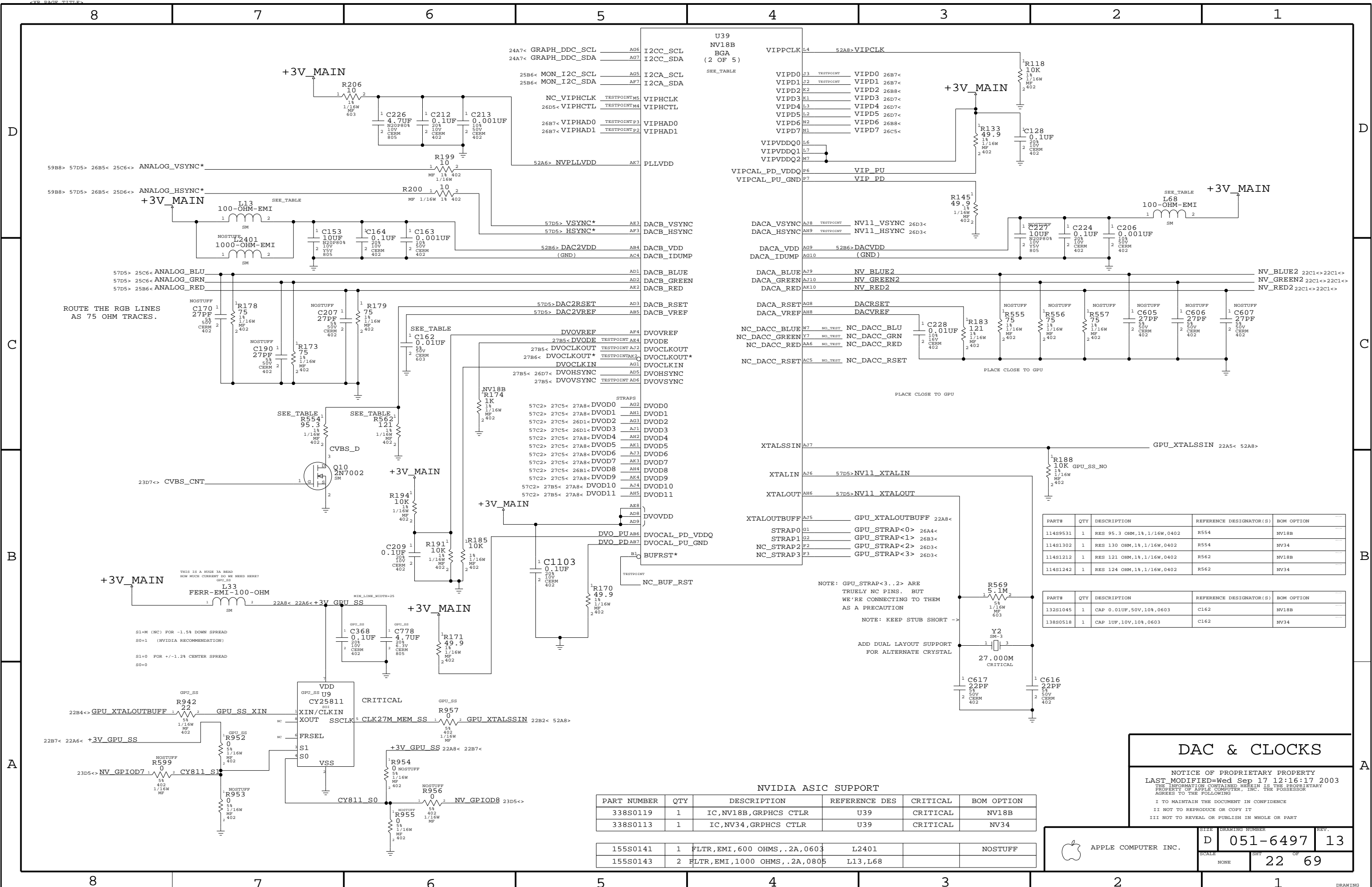
SGRAM2 & SGRAM3

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D	051-6497	13
SCALE	SHT	OF
NONE	21	69



59B8> 57D5> 26B5< 25C6<> ANALOG_VSYNC*

59B8> 57D5> 26B5< 25D6<> ANALOG_HSYNC*

ROUTE THE RGB LINES AS 75 OHM TRACES.

THIS IS A HEAD HOW MUCH CURRENT DO WE NEED HERE? GPU_SS

NOTE: GPU_STRAP<3..2> ARE TRULY NC PINS. BUT WE'RE CONNECTING TO THEM AS A PRECAUTION

NOTE: KEEP STUB SHORT ->

ADD DUAL LAYOUT SUPPORT FOR ALTERNATE CRYSTAL

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
338S0119	1	IC,NV18B,GRPHCS CTLR	U39	CRITICAL	NV18B
338S0113	1	IC,NV34,GRPHCS CTLR	U39	CRITICAL	NV34

155S0141	1	FLTR,EMI,600 OHMS,.2A,0603	L2401		NOSTUFF
155S0143	2	FLTR,EMI,1000 OHMS,.2A,0805	L13,L68		

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
11489531	1	RES 95.3 OHM,1%,1/16W,0402	R554	NV18B
11481302	1	RES 130 OHM,1%,1/16W,0402	R554	NV34
11481212	1	RES 121 OHM,1%,1/16W,0402	R562	NV18B
11481242	1	RES 124 OHM,1%,1/16W,0402	R562	NV34

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
13281045	1	CAP 0.01UF,50V,10%,0603	C162	NV18B
13880518	1	CAP 1UF,10V,10%,0603	C162	NV34

DAC & CLOCKS

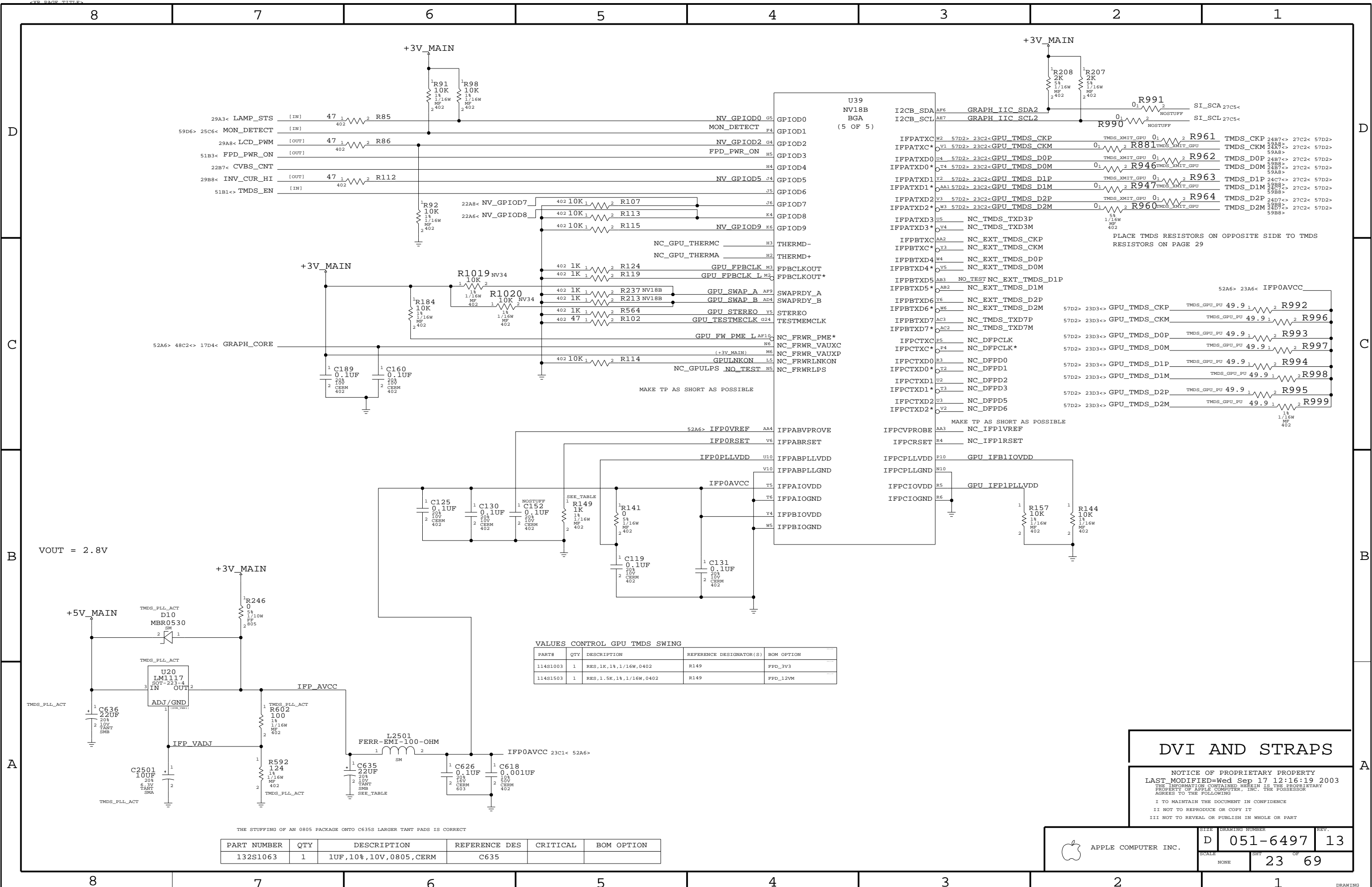
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SCALE: NONE SHEET: 22 OF 69

SIZE: DRAWING NUMBER: REV. D 051-6497 13

APPLE COMPUTER INC.

NVIDIA ASIC SUPPORT



VALUES CONTROL GPU TMS SWING

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
11481003	1	RES,1K,1%,1/16W,0402	R149	FPD_3V3
11481503	1	RES,1.5K,1%,1/16W,0402	R149	FPD_12VM

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
132S1063	1	1UF,10%,10V,0805,CERM	C635		

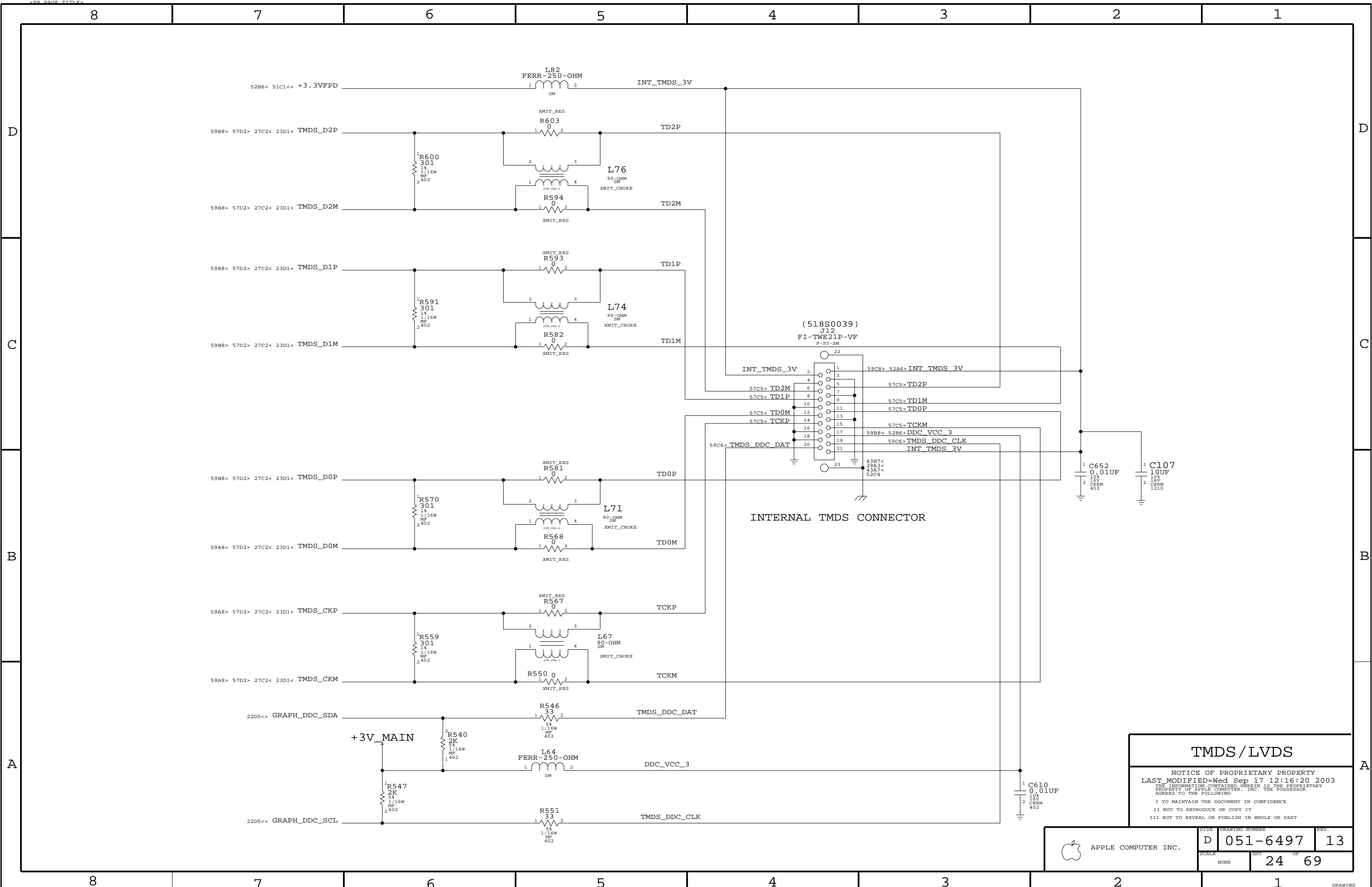
DVI AND STRAPS

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	D	051-6497	13
SCALE	SHEET		OF
NONE	23		69

THE STUFFING OF AN 0805 PACKAGE ONTO C635S LARGER TANT PADS IS CORRECT

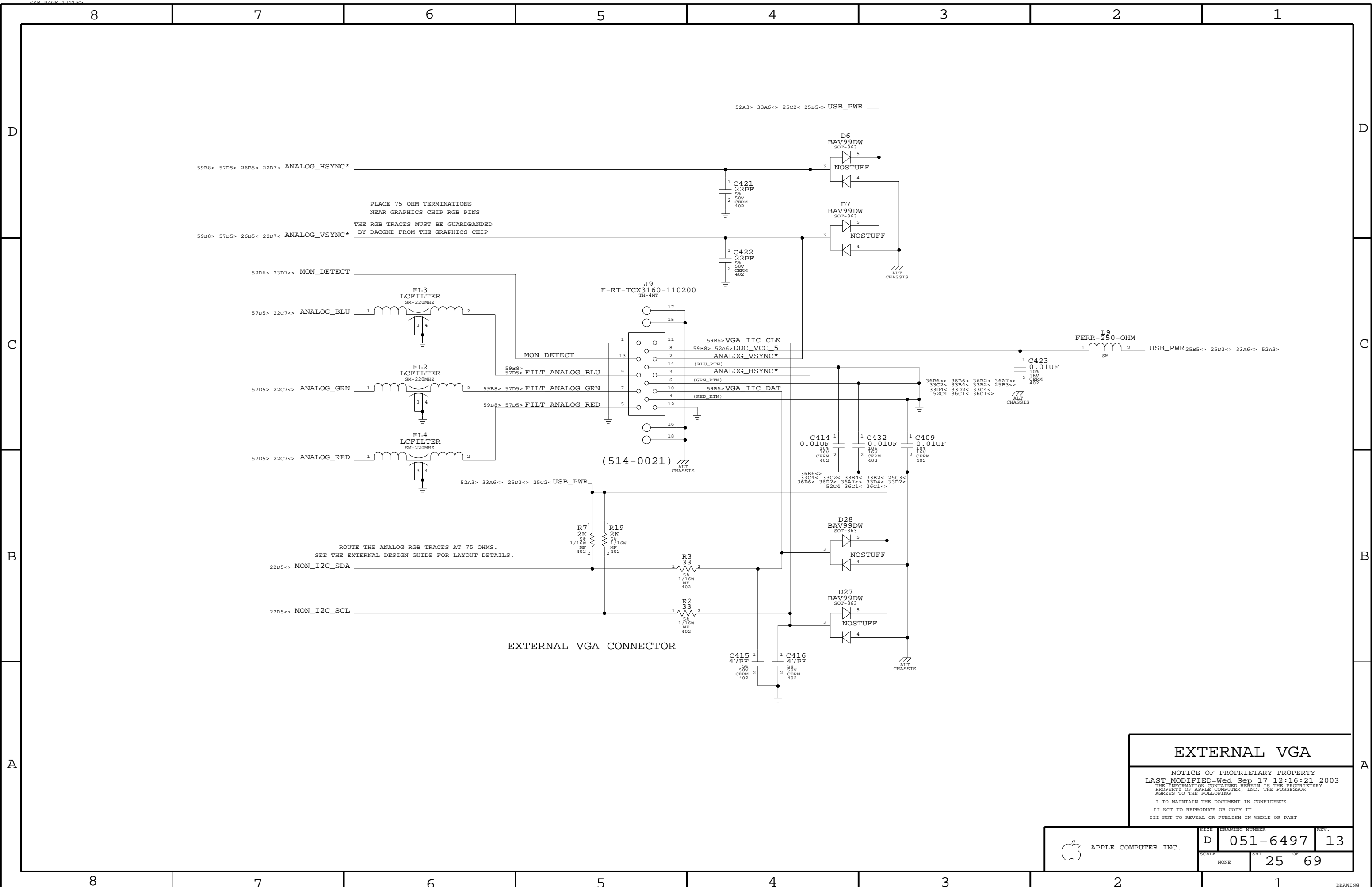
VOUT = 2.8V



TMDS/LVDS

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APPLE COMPUTER INC.	SIZE D	DRAWING NUMBER 051-6497	REV. 13
	SCALE NONE	SHEET 24	OF 69

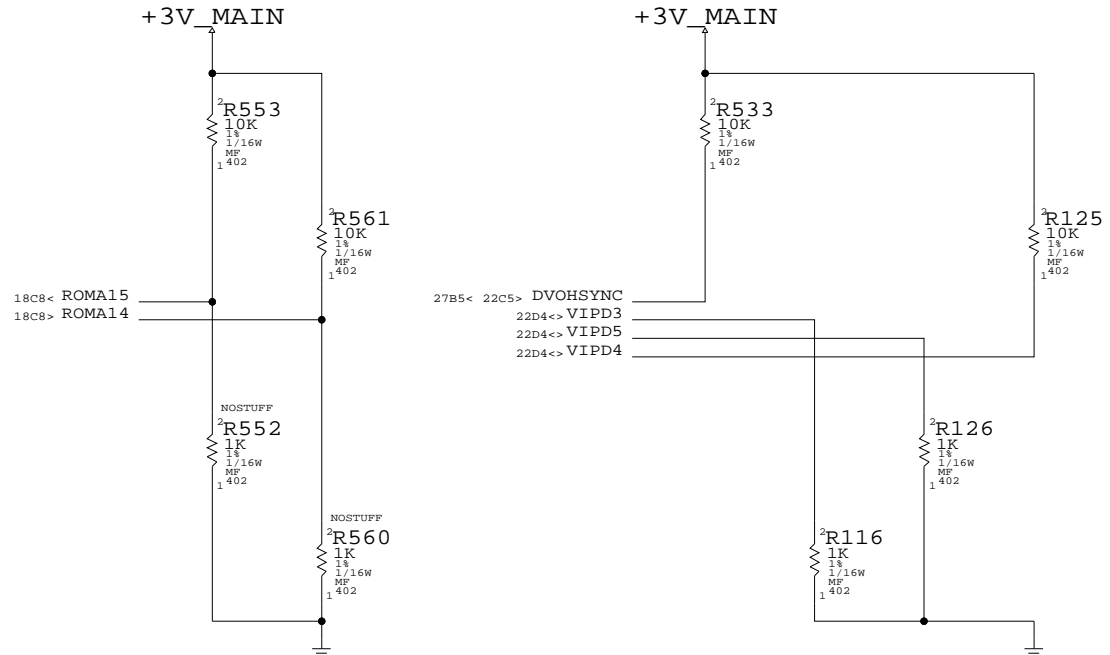


EXTERNAL VGA

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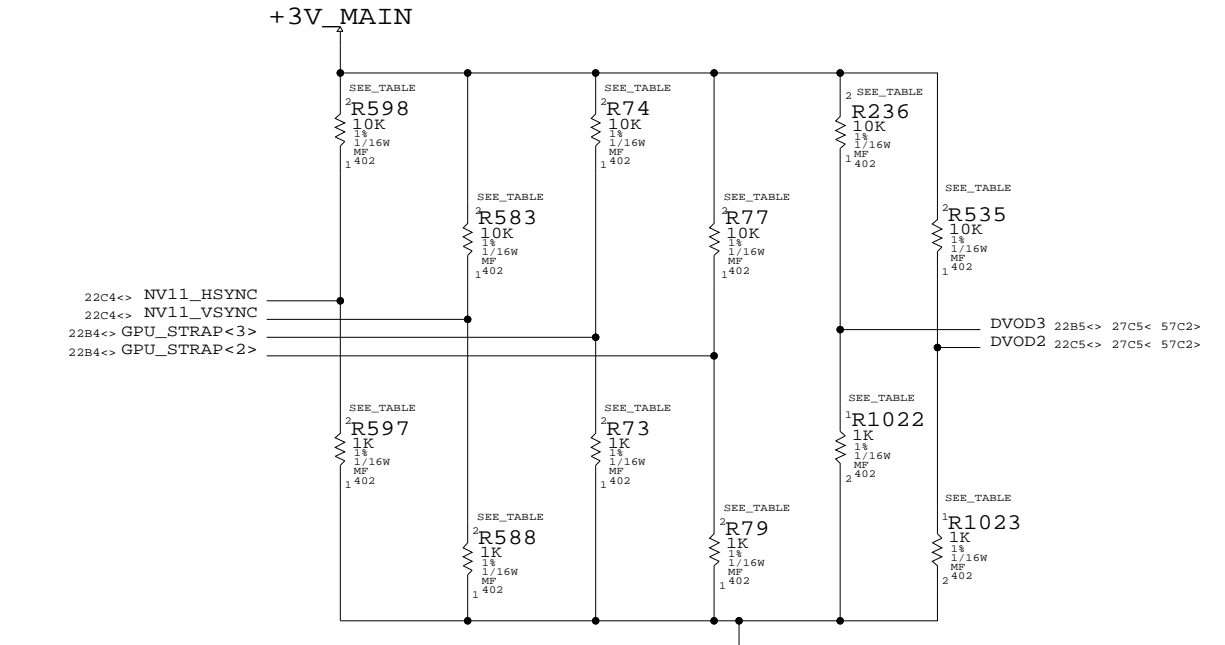
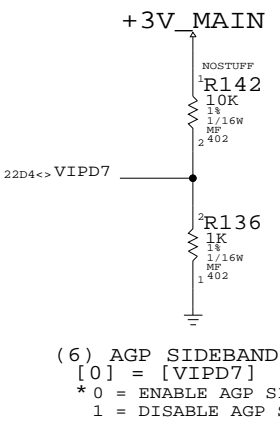
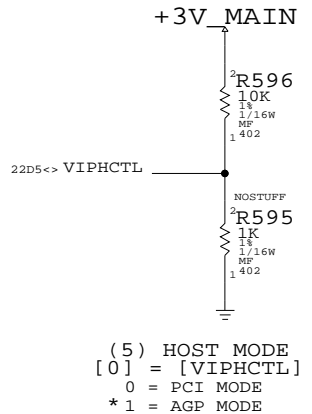
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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6497	13
SCALE	SHT	OF	REV.
NONE	25	69	13



(1) ROM TYPE (OVERRIDDEN IF STRAP1 = 0)
[1..0] = [ROMA15,ROMA14]
00 = PARALLEL
01 = SERIAL AT25F
10 = SERIAL SST45VF
* 11 = SERIAL FUTURE

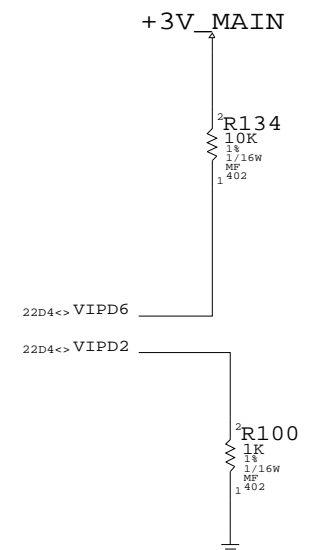
(3) PCI DEVICE ID
[3..0] = [DVOHSYNC, VIPD3, VIPD5, VIPD4]
0010 = 0X112 GEFORCE2 GO
0011 = 0X113 QUADRO2 GO
0100 = 0X114 NV17M
0000 = 0X110 GEFORCE2GO MX (NV11B)
* 1001 = NV18B, NV31, NV34



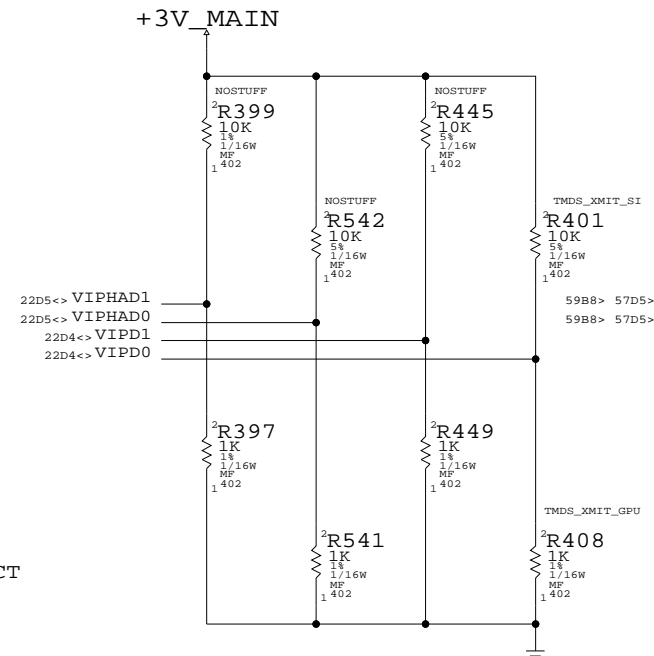
(8) FRAME BUFFER MEMORY TYPE
[3..0] = [NV11_HSYNC, NV11_VSYNC, GPU_STRAP<3>, GPU_STRAP<2>]
1111 = 222MHZ
1101 = 275MHZ SAMSUNG
1100 = 275MHZ HYNIX

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
114S1004	5	RES,10KOHM,1%,0402	R598,R583,R77,R236,R535		SAMSUNG_NV18B_270
114S1003	1	RES,1KOHM,1%,0402	R73		SAMSUNG_NV18B_270
114S1004	4	RES,10KOHM,1%,0402	R598,R583,R236,R535		HYNIX_NV18B_270
114S1003	2	RES,1KOHM,1%,0402	R73,R79		HYNIX_NV18B_270

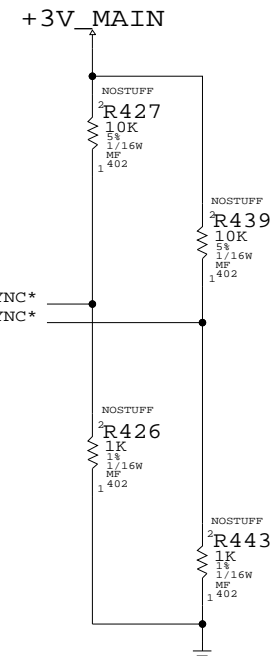
114S1004	5	RES,10KOHM,1%,0402	R598,R583,R535	R74,R77	SAMSUNG_NV34_270
114S1003	1	RES,1KOHM,1%,0402	R1022		SAMSUNG_NV34_270
114S1004	4	RES,10KOHM,1%,0402	R583,R74,R77,R535		HYNIX_NV34_270
114S1003	2	RES,1KOHM,1%,0402	R597,R1022		HYNIX_NV34_270



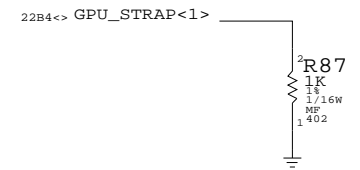
(2) CRYSTAL FREQUENCY SELECT
[1..0] = [VIPD6, VIPD2]
00 = 13.5MHZ
01 = 14.38MHZ
* 10 = 27MHZ
11 = {UNDEFINED}



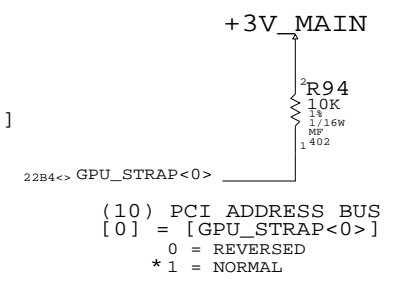
(4) USER DEFINED STRAPS
[3..0] = [VIPHAD1, VIPHAD0, VIPD1, VIPD0]
THESE BITS ARE UNDEFINED BUT THEY MUST BE KEPT LOW DURING RESET



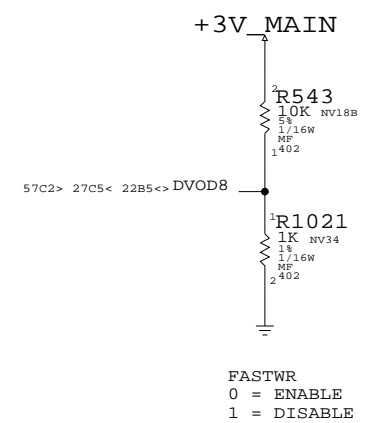
(7) TV MODE
[1..0] = [ANALOG_HSYNC*, ANALOG_VSYNC*]
00 = SECAM
01 = NTSC
10 = PAL
* 11 = DISABLED
(THESE RESISTORS ARE ALL NOSTUFF)



(9) SUB-VENDOR
[0] = [GPU_STRAP<1>]
0 = SYSTEM BIOS (VENDOR & SUBSYSTEM ID=0X0000)
1 = ADAPTER CARD VGA BIOS (VENDOR & SUBSYSTEM ID=0X54-0X57)



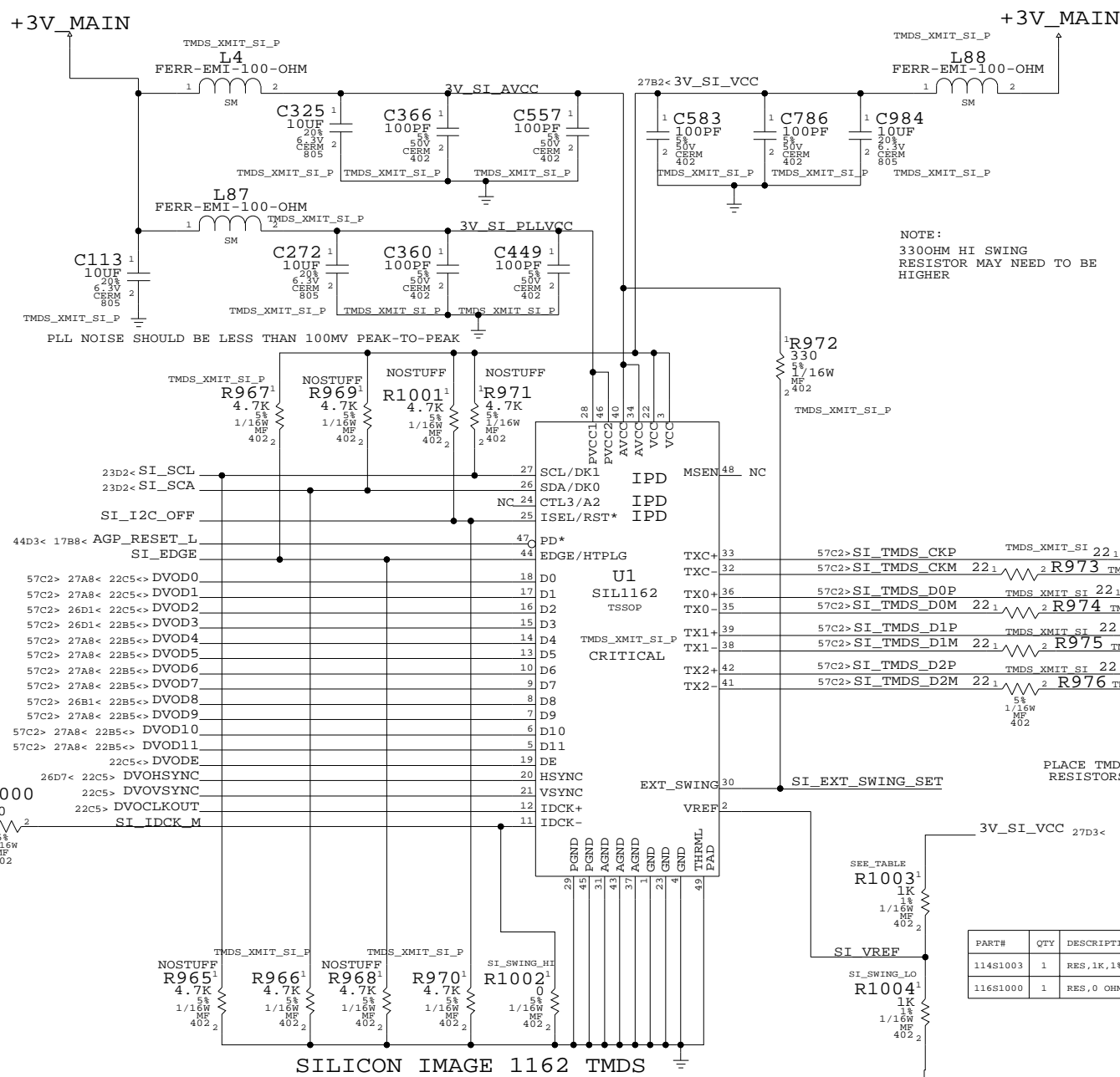
(10) PCI ADDRESS BUS
[0] = [GPU_STRAP<0>]
0 = REVERSED
* 1 = NORMAL



NVIDIA STRAPS 1

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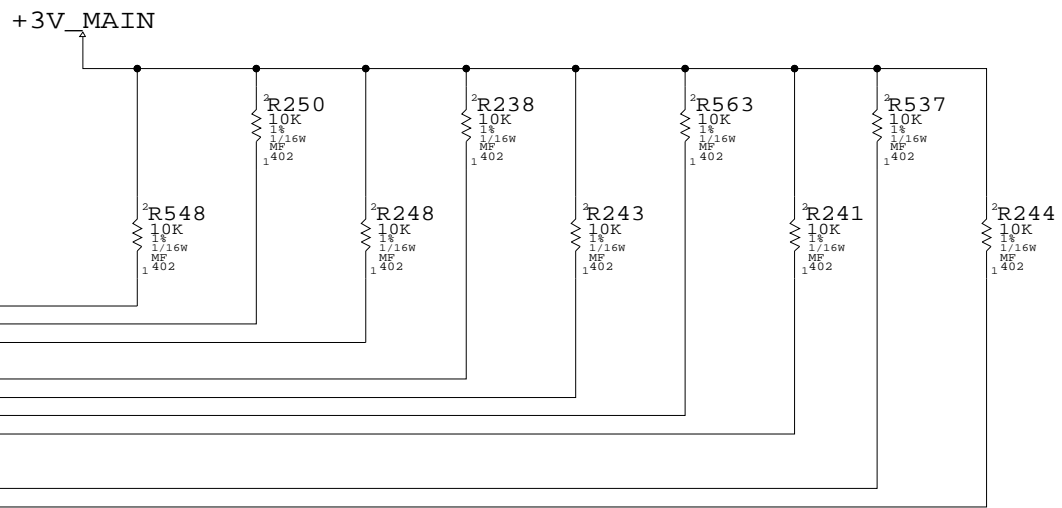
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6497	13
SCALE	SHT	OF	
NONE	26	69	



NOTE:
330OHM HI SWING
RESISTOR MAY NEED TO BE
HIGHER

PLACE TMSD RESISTORS ON OPPOSITE SIDE TO TMSD
RESISTORS ON PAGE 25

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
11481003	1	RES,1K,1%,1/16W,0402	R1003	SI_SWING_LO
11681000	1	RES,0 OHM,1%,1/16W,0402	R1003	SI_SWING_HI



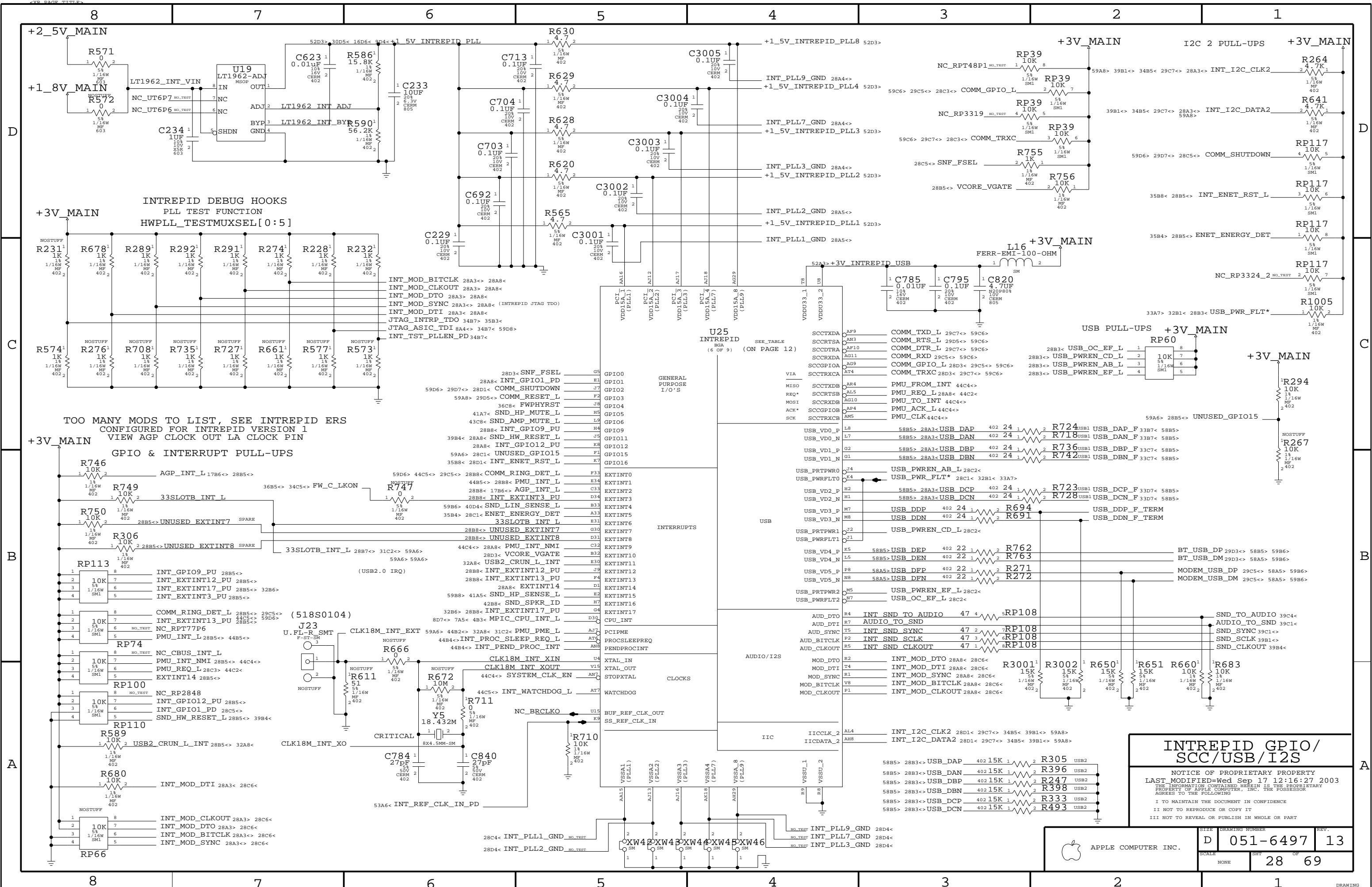
- 57C2> 27B5< 22B5<> DVOD11
- 57C2> 27B5< 22B5<> DVOD10
- 57C2> 27C5< 22B5<> DVOD9
- 57C2> 27C5< 22B5<> DVOD7
- 57C2> 27C5< 22B5<> DVOD6
- 57C2> 27C5< 22B5<> DVOD5
- 57C2> 27C5< 22B5<> DVOD4
- 57C2> 27C5< 22C5<> DVOD1
- 57C2> 27C5< 22C5<> DVOD0

UNDEFINED RESET CONFIGURATION STRAPS

NVIDIA STRAPS 2

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8 7 6 5 4 3 2 1

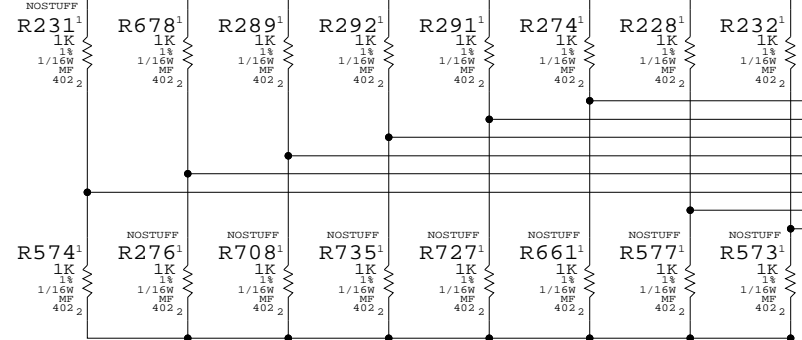
+2_5V_MAIN

+1_8V_MAIN

+3V_MAIN

+3V_MAIN

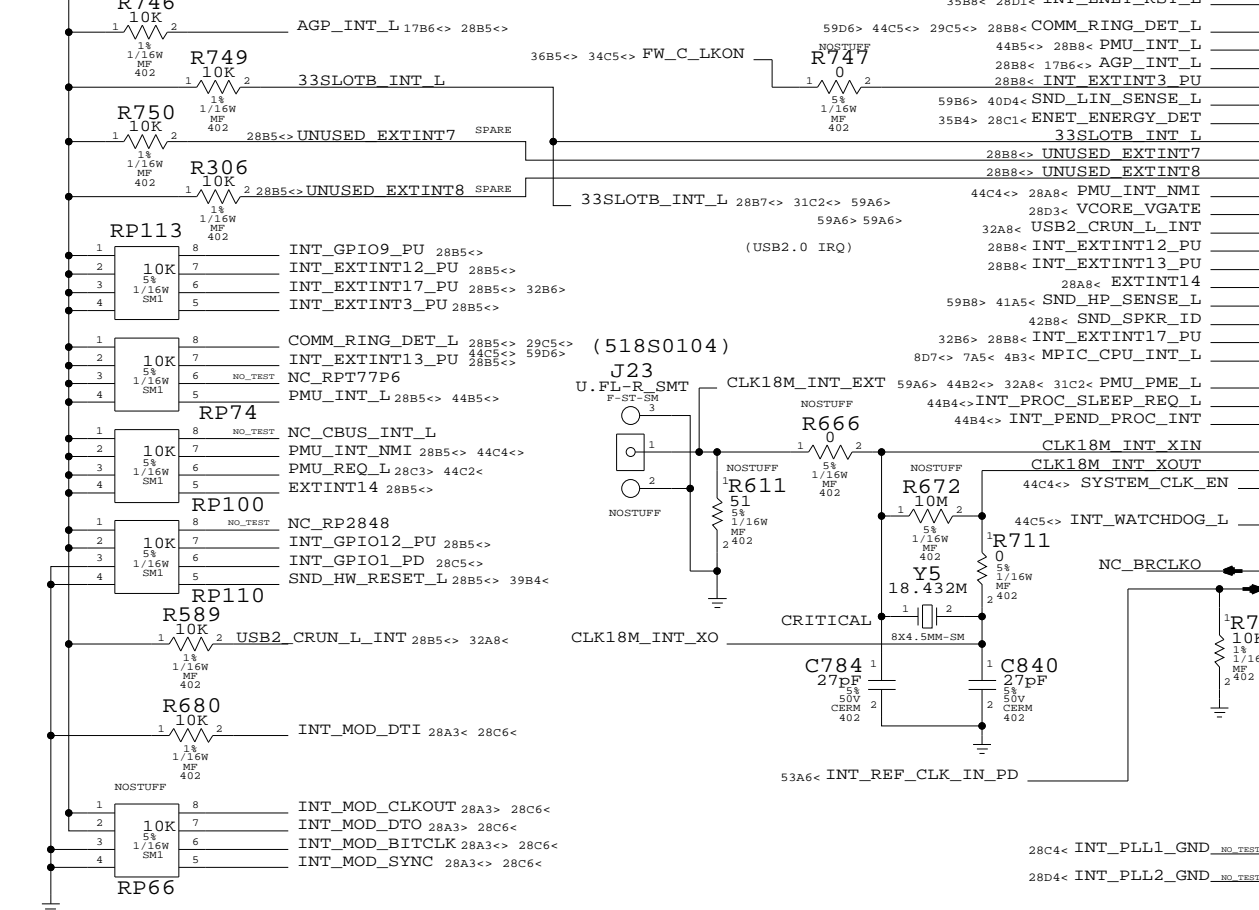
INTREPID DEBUG HOOKS
PLL TEST FUNCTION
HWPLL_TESTMUXSEL[0:5]



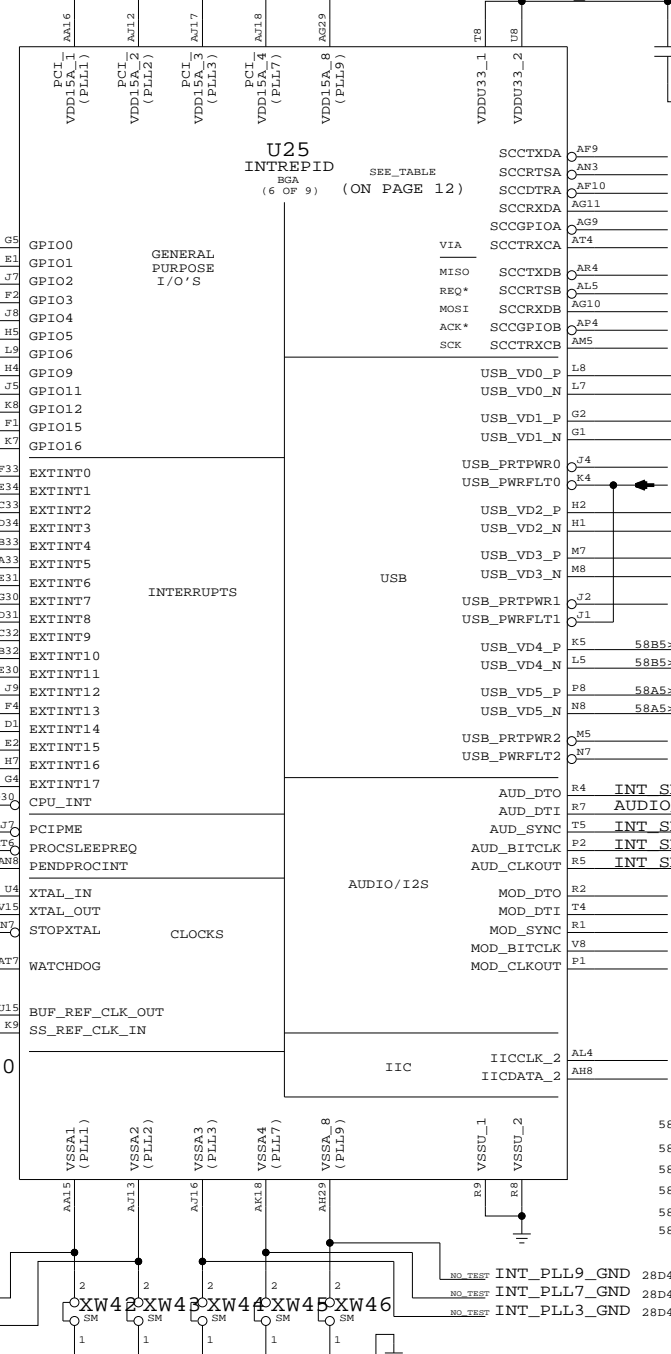
TOO MANY MODS TO LIST, SEE INTREPID ERS
CONFIGURED FOR INTREPID VERSION 1
VIEW AGP CLOCK OUT LA CLOCK PIN

+3V_MAIN

GPIO & INTERRUPT PULL-UPS



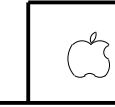
INT_MOD_BITCLK 28A3< 28A8<
INT_MOD_CLKOUT 28A3< 28A8<
INT_MOD.DTO 28A3< 28A8<
INT_MOD_SYNC 28A3< 28A8< (INTREPID JTAG TDO)
INT_MOD.DTI 28A3< 28A8<
JTAG_INTRP_TDO 34B7> 35B3<
JTAG_ASIC_TDI 8A4< 34B7< 59D8>
INT_TST_PLEN_PD 34B7<



INTREPID GPIO/
SCC/USB/I2S

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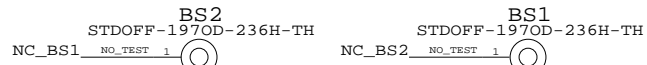
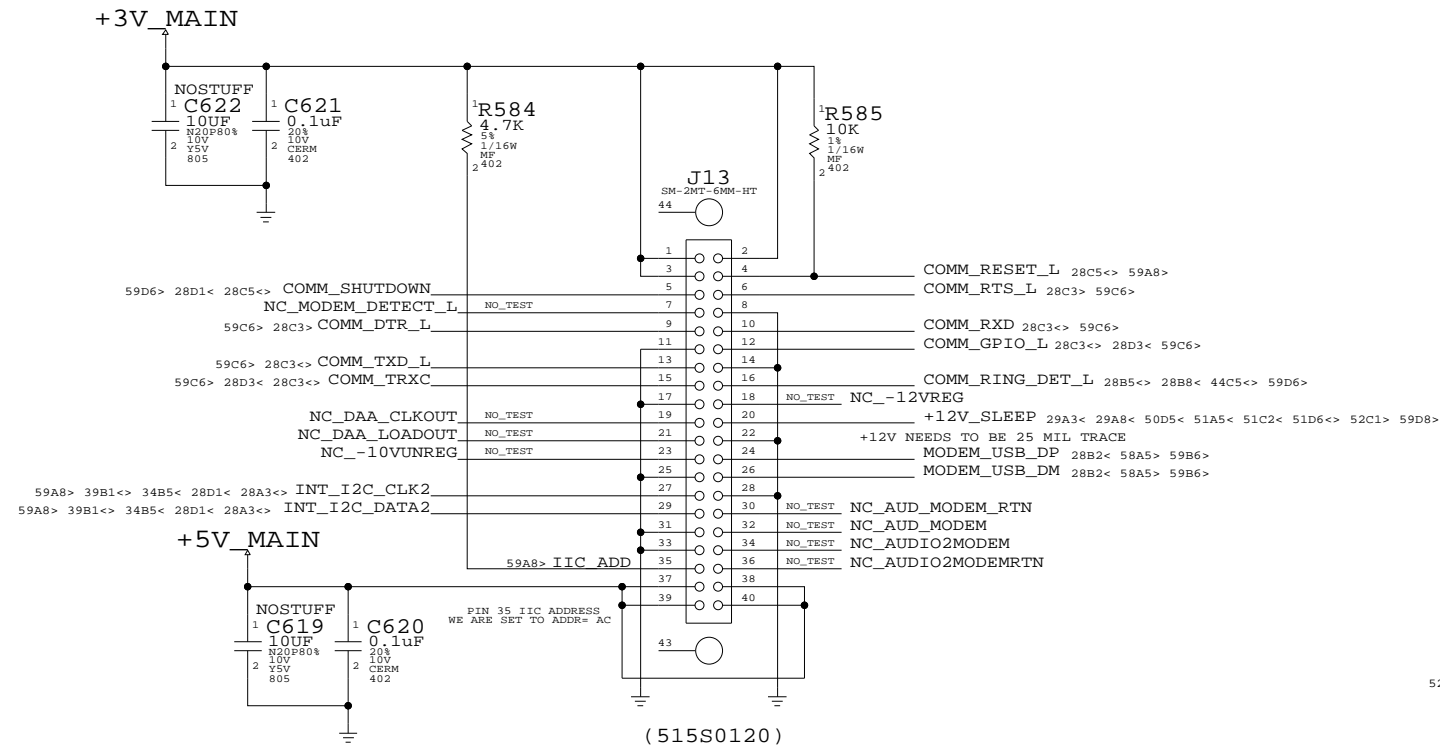
SIZE	DRAWING NUMBER	REV.
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SCALE	SHT	OF
NONE	28	69



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MODEM BOARD CONNECTOR

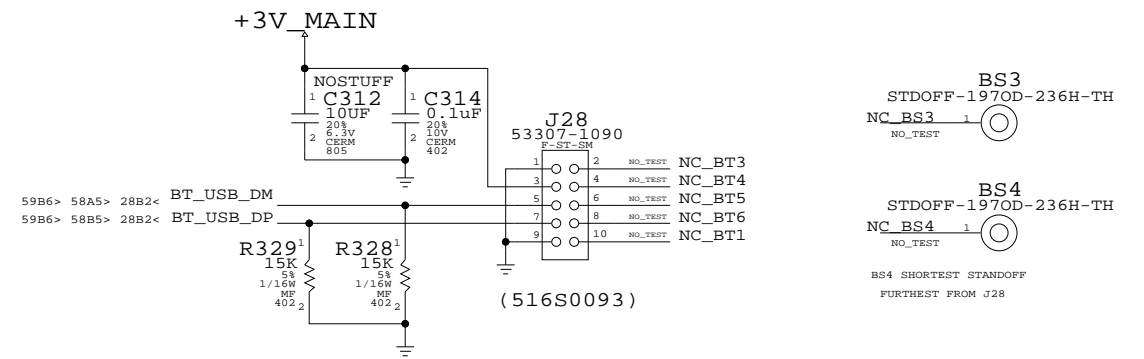
(DASH II)



MODEM STANDOFF SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
860-1034	2	STDOFF-19709-236H-TH	BS1,BS2		

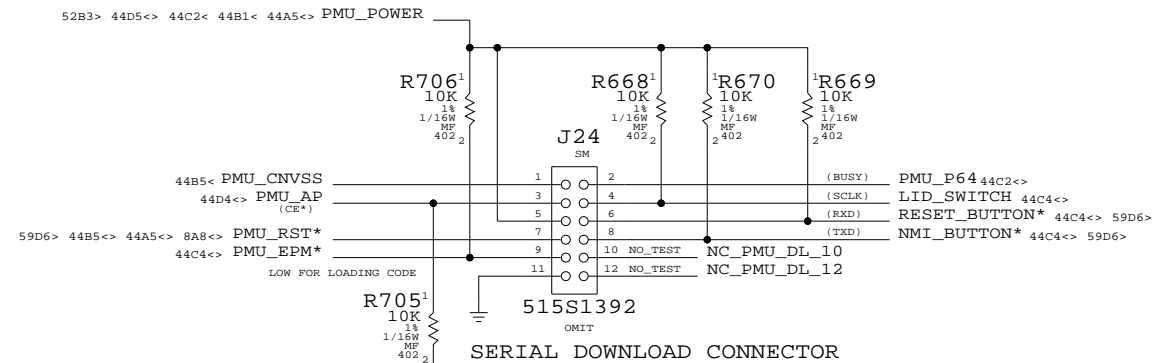
BLUETOOTH CONNECTOR



BLUETOOTH CARD MOUNTING HARDWARE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
860-0170	1	STDOFF, BLUETOOTH, SHORT	BS4		
860-0171	1	STDOFF, BLUETOOTH, LONG	BS3		

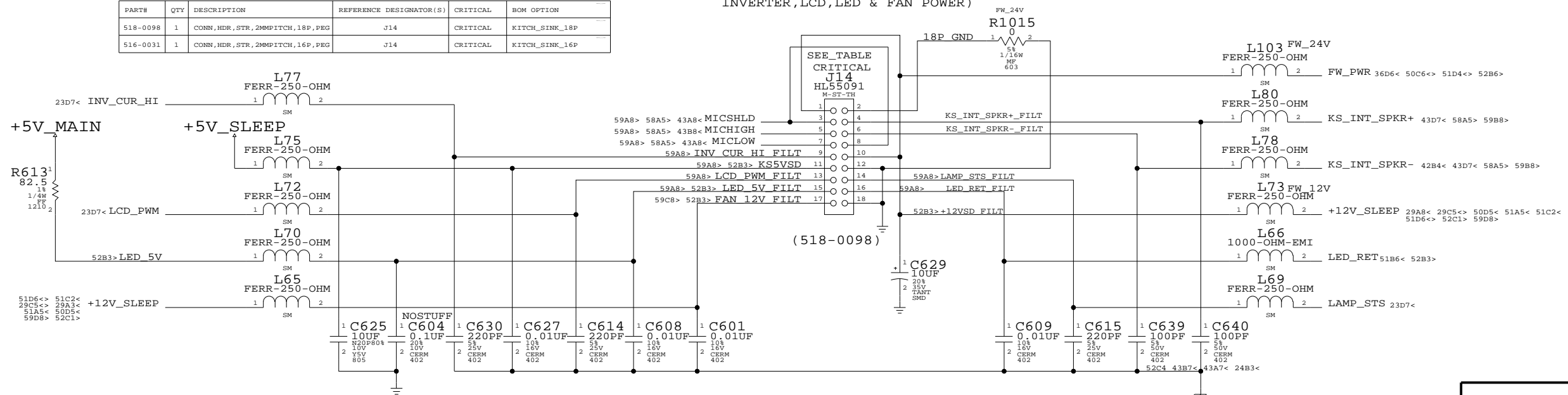
SERIAL DOWNLOAD INTERFACE



SERIAL DOWNLOAD CONNECTOR

'KITCHEN SINK' CONNECTOR
(MICROPHONE, INTERNAL SPEAKER CONNECTIONS
INVERTER, LCD, LED & FAN POWER)

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
518-0098	1	CONN,HDR,STR,2MMPITCH,18P,PEG	J14	CRITICAL	KITCH_SINK_18P
516-0031	1	CONN,HDR,STR,2MMPITCH,16P,PEG	J14	CRITICAL	KITCH_SINK_16P



MODEM, BLUETOOTH,
KITCHEN SINK
& SERIAL DOWNLOAD

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SCALE	SHT	OF	
NONE	29	69	

D

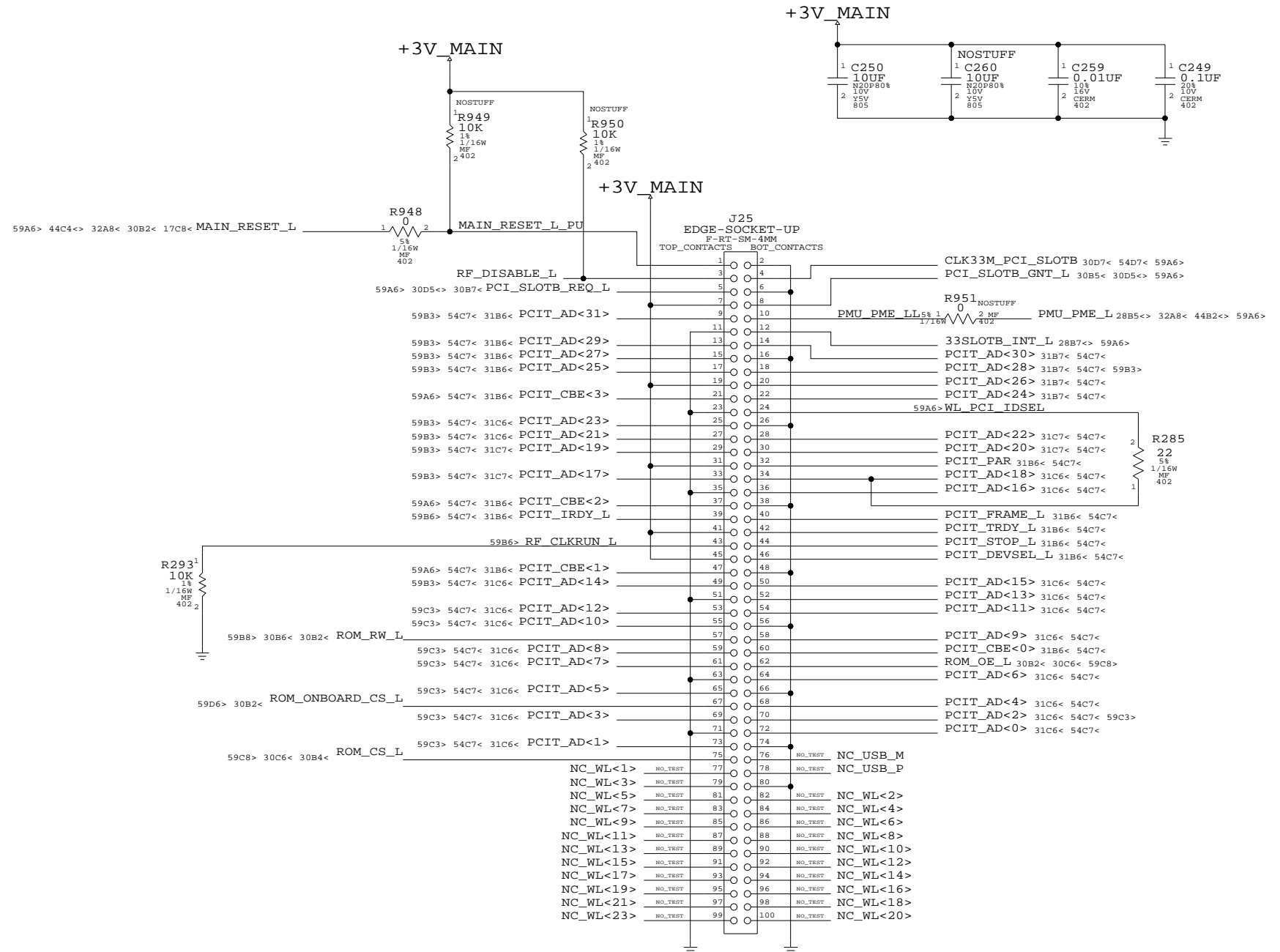
C

B

A

PLACE RP'S NEAR WIRELESS CONNECTOR

59C3> 53A6< 32C6<> 30D4<> 30C2< PCI_AD<0>	1	RP77	8	PCIT_AD<0>	31B2<>	54C7<
53A6< 32C6<> 30D4<> 30C2< PCI_AD<1>	2	RP73	7	NO_TEST	PCIT_AD<1>	31B3<> 54C7< 59C3>
53A6< 32C6<> 30D4<> 30C2< PCI_AD<2>	3	RP73	6	NO_TEST	PCIT_AD<2>	31B2<> 54C7< 59C3>
53A6< 32C6<> 30D4<> 30C2< PCI_AD<3>	4	RP73	5	NO_TEST	PCIT_AD<3>	31B3<> 54C7< 59C3>
59C3> 53A6< 32C6<> 30D4<> 30C2< PCI_AD<4>	1	RP75	8	PCIT_AD<4>	31B2<>	54C7<
53A6< 32C6<> 30D4<> 30C2< PCI_AD<5>	2	RP73	7	NO_TEST	PCIT_AD<5>	31B3<> 54C7< 59C3>
59C3> 53A6< 32C6<> 30D4<> 30C2< PCI_AD<6>	3	RP73	6	NO_TEST	PCIT_AD<6>	31B2<> 54C7<
53A6< 32C6<> 30D4<> 30C2< PCI_AD<7>	4	RP73	5	NO_TEST	PCIT_AD<7>	31B3<> 54C7< 59C3>
53A6< 32C6<> 30D4<> 30C2< PCI_AD<8>	1	RP73	8	PCIT_AD<8>	31B3<>	54C7< 59C3>
59C3> 53A6< 32C6<> 30D4<> 30C2< PCI_AD<9>	2	RP73	7	NO_TEST	PCIT_AD<9>	31B2<> 54C7<
53A6< 32C6<> 30C4<> 30B2< PCI_AD<10>	3	RP73	6	NO_TEST	PCIT_AD<10>	31B3<> 54C7< 59C3>
59C3> 53A6< 32C6<> 30C4<> 30B2< PCI_AD<11>	4	RP73	5	NO_TEST	PCIT_AD<11>	31B2<> 54C7<
53A6< 32C6<> 30C4<> 30B2< PCI_AD<12>	1	RP72	8	PCIT_AD<12>	31B3<>	54C7< 59C3>
59C3> 53A6< 32C6<> 30C4<> 30B2< PCI_AD<13>	2	RP72	7	NO_TEST	PCIT_AD<13>	31B2<> 54C7<
53A6< 32C6<> 30C4<> 30B2< PCI_AD<14>	3	RP72	6	NO_TEST	PCIT_AD<14>	31C3<> 54C7< 59B3>
59B3> 53A6< 32C6<> 30C4<> 30B2< PCI_AD<15>	4	RP72	5	NO_TEST	PCIT_AD<15>	31C2<> 54C7<
59B3> 53A6< 32C6<> 30C4<> 30B2< PCI_AD<16>	1	RP59	8	PCIT_AD<16>	31C2<>	54C7<
59B3> 54C7< 31C3<> PCIT_AD<17>	2	RP59	7	NO_TEST	PCIT_AD<17>	30B2< 30C4<> 32C6<> 53A6<
59B3> 53A6< 32C6<> 30C4<> 30B2< PCI_AD<18>	3	RP58	6	NO_TEST	PCIT_AD<18>	31C2<> 54C7<
59B3> 54C7< 31C3<> PCIT_AD<19>	4	RP58	5	NO_TEST	PCIT_AD<19>	30B2< 30C4<> 32C6<> 53A6<
53A6< 32C6<> 30C4<> PCI_AD<23>	1	RP58	8	PCIT_AD<23>	31C3<>	54C7< 59B3>
53A6< 32C6<> 30C4<> PCI_AD<21>	2	RP58	7	NO_TEST	PCIT_AD<21>	31C3<> 54C7< 59B3>
54C7< 31C2<> PCIT_AD<22>	3	RP58	6	NO_TEST	PCIT_AD<22>	30C4<> 32C6<> 53A6< 59B3>
54C7< 31C2<> PCIT_AD<20>	4	RP58	5	NO_TEST	PCIT_AD<20>	30B2< 30C4<> 32C6<> 53A6< 59B3>
53A6< 32B7<> 30C4<> 30C1<> PCI_AD<27>	1	RP56	8	PCIT_AD<27>	31C3<>	54C7< 59B3>
53A6< 32C6<> 30C4<> 30C1<> PCI_AD<25>	2	RP56	7	NO_TEST	PCIT_AD<25>	31C3<> 54C7< 59B3>
54C7< 31C2<> PCIT_AD<26>	3	RP56	6	NO_TEST	PCIT_AD<26>	30C1<> 30C4<> 32B6<> 53A6< 59B3>
54C7< 31C2<> PCIT_AD<24>	4	RP56	5	NO_TEST	PCIT_AD<24>	30C1<> 30C4<> 32C6<> 53A6< 59B3>
59B3> 54C7< 31C2<> PCIT_AD<28>	1	RP54	8	PCIT_AD<28>	30C1<>	30C4<> 32B6<> 53A6<
54C7< 31C2<> PCIT_AD<30>	2	RP54	7	NO_TEST	PCIT_AD<30>	30C1<> 30C4<> 32B6<> 53A6< 59B3>
53A6< 32B6<> 30C4<> 30C1<> PCI_AD<31>	3	RP61	6	NO_TEST	PCIT_AD<31>	31C3<> 54C7< 59B3>
53A6< 32B6<> 30C4<> 30C1<> PCI_AD<29>	4	RP61	5	NO_TEST	PCIT_AD<29>	31C3<> 54C7< 59B3>
59A6> 54D7< 32B6<> 30C5<> PCI_PAR	1	RP61	8	PCIT_PAR	31C2<>	54C7<
59A6> 53A6< 32B6<> 30C5<> 30B7< PCI_FRAME_L	2	RP61	7	NO_TEST	PCIT_FRAME_L	31C2<> 54C7<
59A6> 54D7< 32B6<> 30C5<> 30B7< PCI_TRDY_L	3	RP61	6	NO_TEST	PCIT_TRDY_L	31C2<> 54C7<
54D7< 32B6<> 30C5<> 30B7< PCI_IRDY_L	4	RP61	5	NO_TEST	PCIT_IRDY_L	31C3<> 54C7< 59B6>
59A6> 54D7< 32B6<> 30C5<> 30B7< PCI_STOP_L	1	RP67	8	PCIT_STOP_L	31C2<>	54C7<
59A6> 54D7< 32B6<> 30C5<> 30B7< PCI_DEVSEL_L	2	RP67	7	NO_TEST	PCIT_DEVSEL_L	31C2<> 54C7<
53A6< 32B6<> 30C5<> PCI_CBE<1>	3	RP64	6	NO_TEST	PCIT_CBE<1>	31C3<> 54C7< 59A6>
59A6> 53A6< 32B6<> 30C5<> PCI_CBE<0>	4	RP64	5	NO_TEST	PCIT_CBE<0>	31B2<> 54C7<
53A6< 32B6<> 30C5<> PCI_CBE<2>	1	RP64	8	PCIT_CBE<2>	31C3<>	54C7< 59A6>
53A6< 32B6<> 30C5<> PCI_CBE<3>	2	RP64	7	NO_TEST	PCIT_CBE<3>	31C3<> 54C7< 59A6>
NC_PCIR0	NO_TEST	3	5	NO_TEST	NC_PCITR0	
NC_PCIR1	NO_TEST	4	5	NO_TEST	NC_PCITR1	



(516S0046)

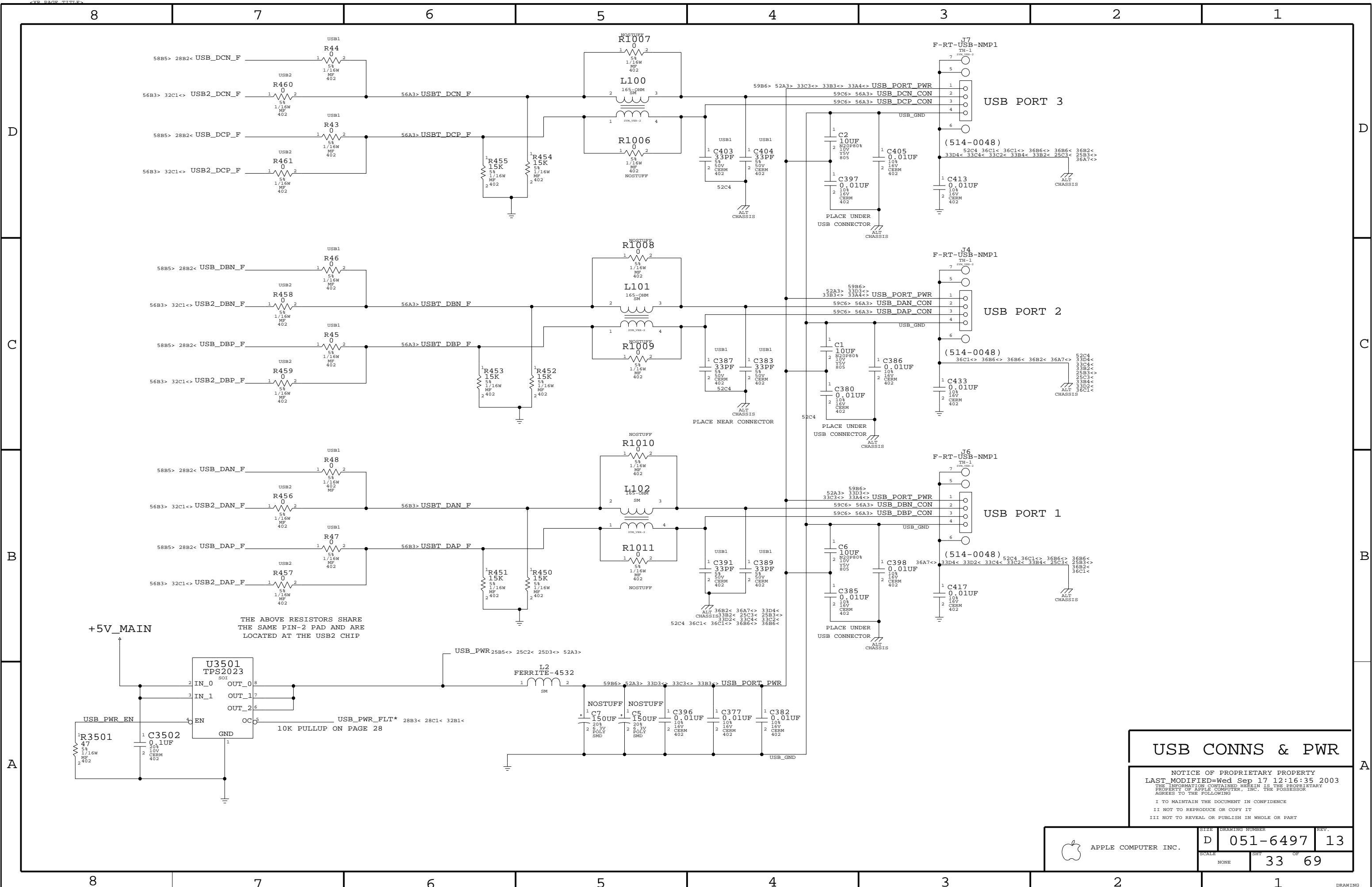
WIRELESS CARD MOUNTING HARDWARE SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
815-7245	1	WIRELESS CARD GUIDE, J25	J251		
452-0411	2	NUT, HEX, M2 X 1.5H, J25	J252, J253		
452-0412	2	SCREW, M2 X 0.4 X 6.0 L, J25	J254, J255		

WIRELESS PCI

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NONE	31	69	



THE ABOVE RESISTORS SHARE THE SAME PIN-2 PAD AND ARE LOCATED AT THE USB2 CHIP

USB CONNS & PWR

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6497	13
SCALE	SHEET		OF
NONE	33		69

D

C

B

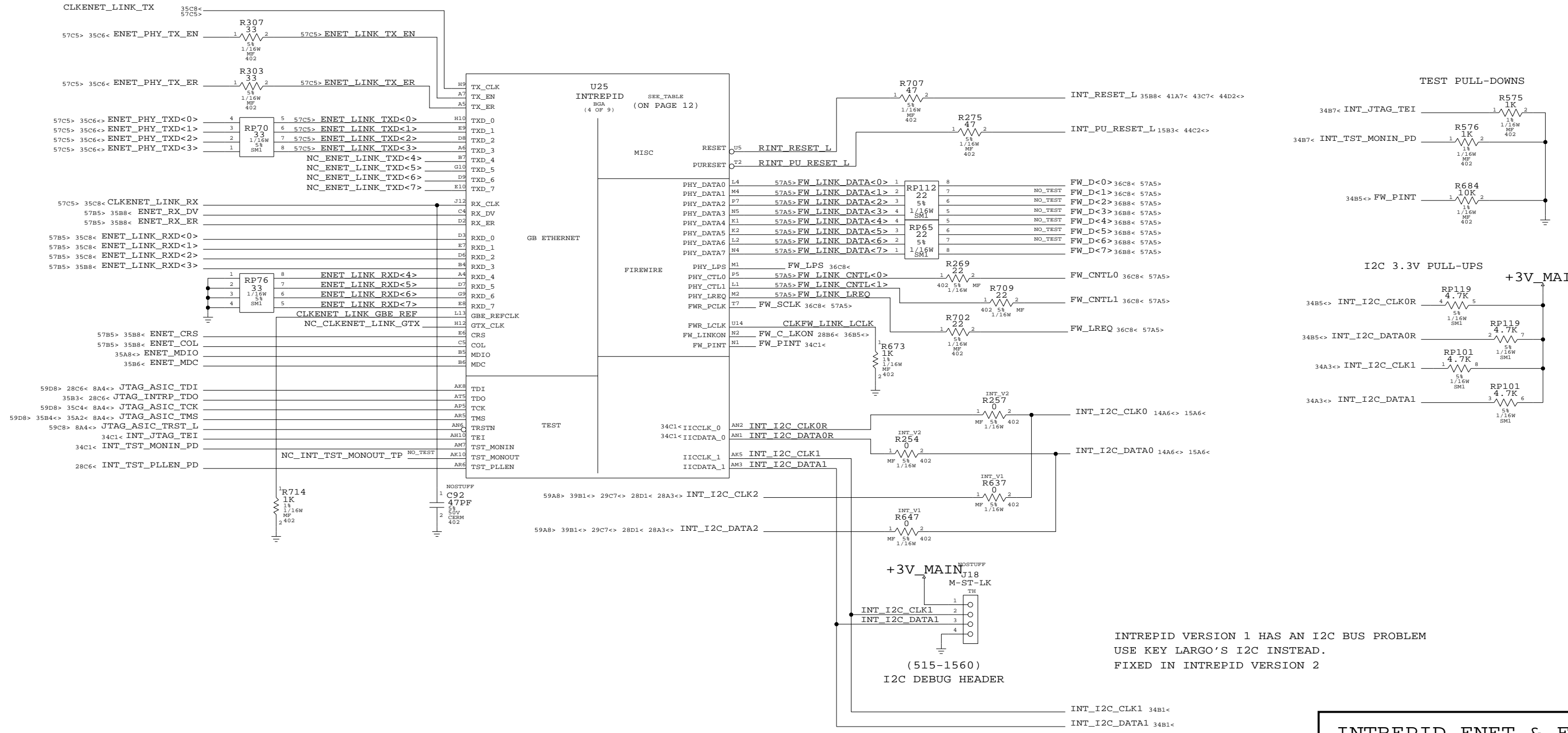
A

D

C

B

A



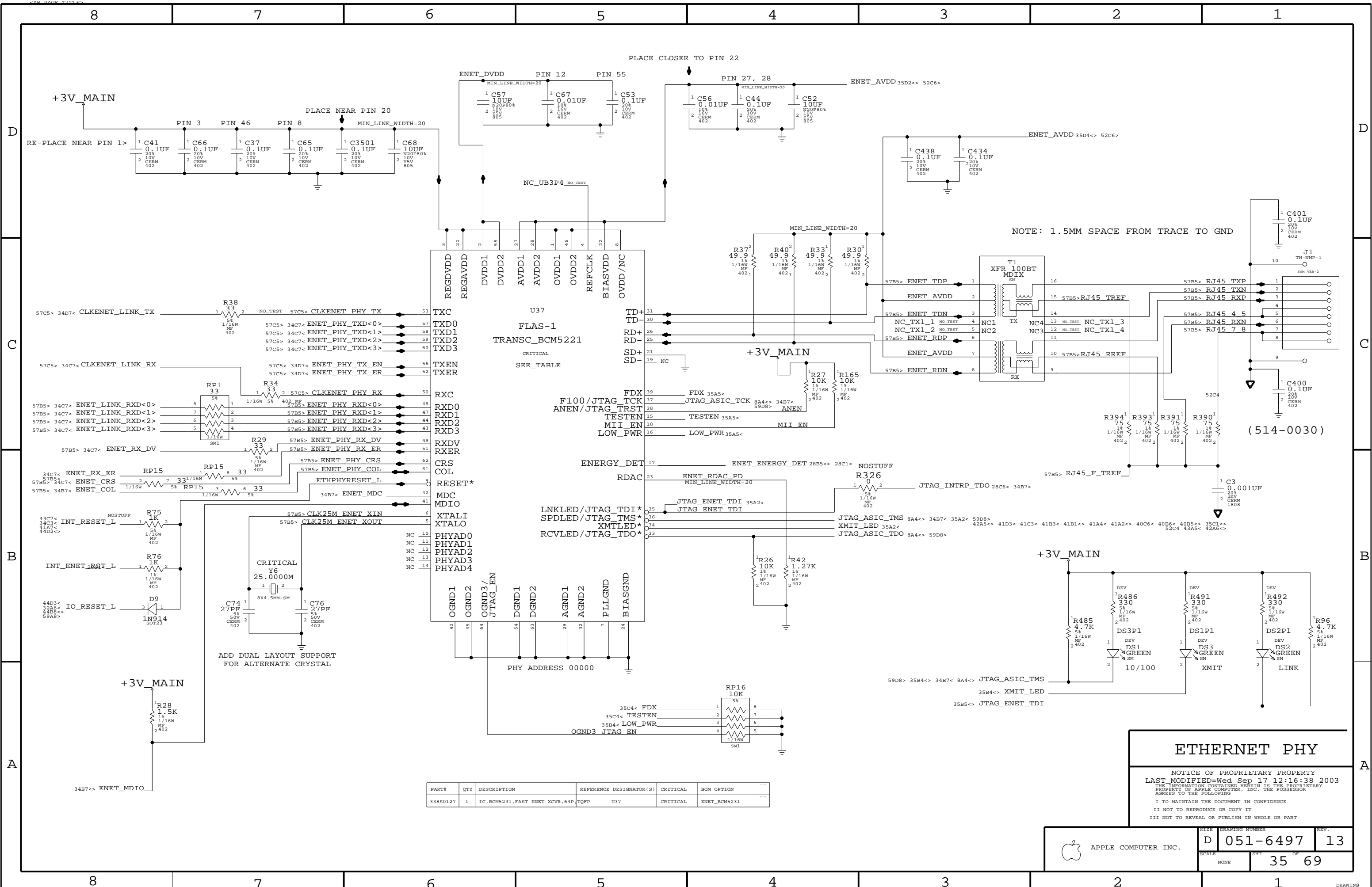
INTREPID VERSION 1 HAS AN I2C BUS PROBLEM
USE KEY LARGO'S I2C INSTEAD.
FIXED IN INTREPID VERSION 2

(515-1560)
I2C DEBUG HEADER

INTREPID ENET & FW

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	D	051-6497	13
SCALE	SHT		OF
NONE	34		69



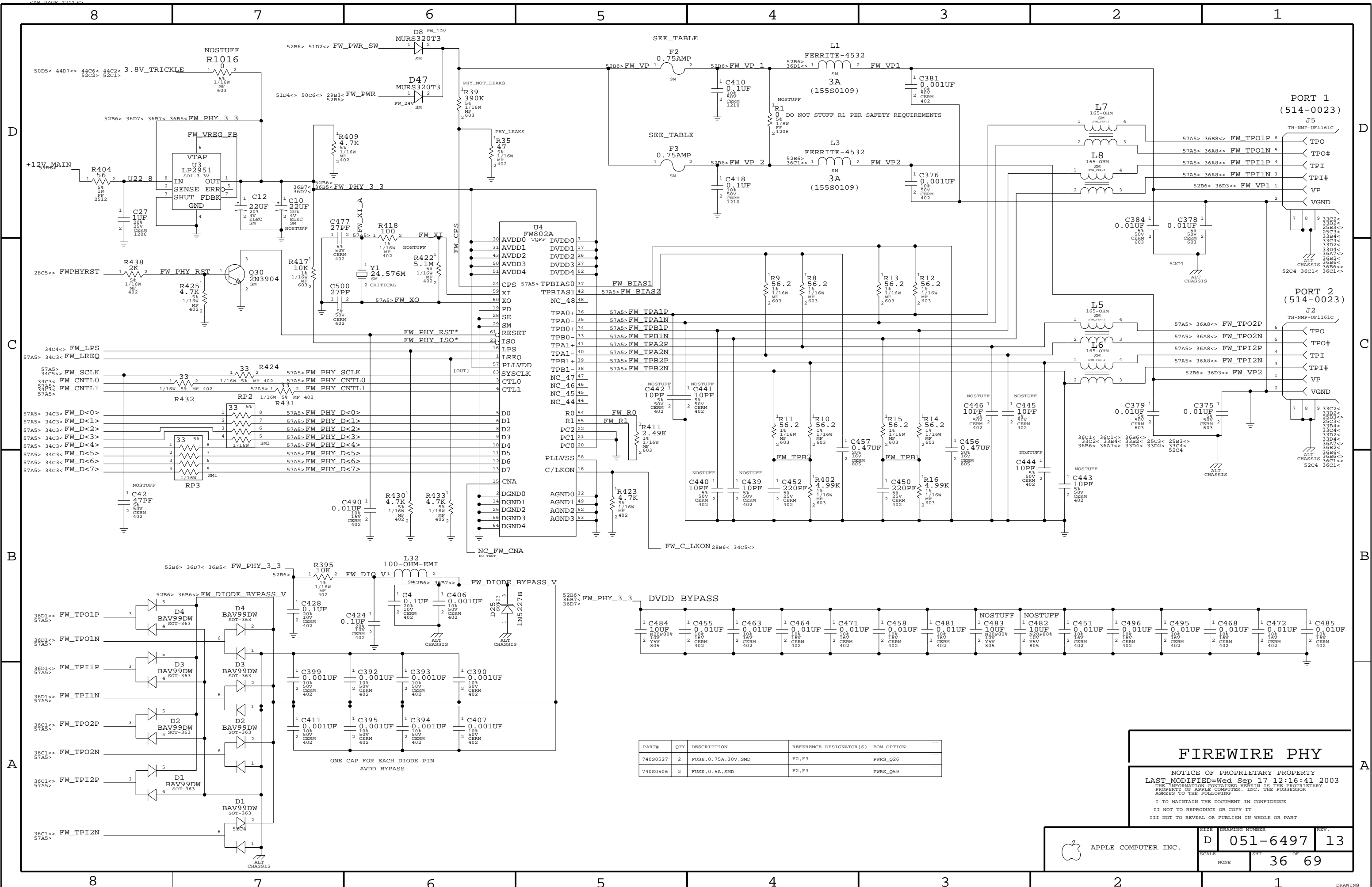
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
338S0127	1	IC, BCM5231, FAST ENET XCVR, 64P, TQFP	U37	CRITICAL	ENET_BCM5231

ETHERNET PHY

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
		D 051-6497	13
SCALE	SHT	OF	
NONE		35	69



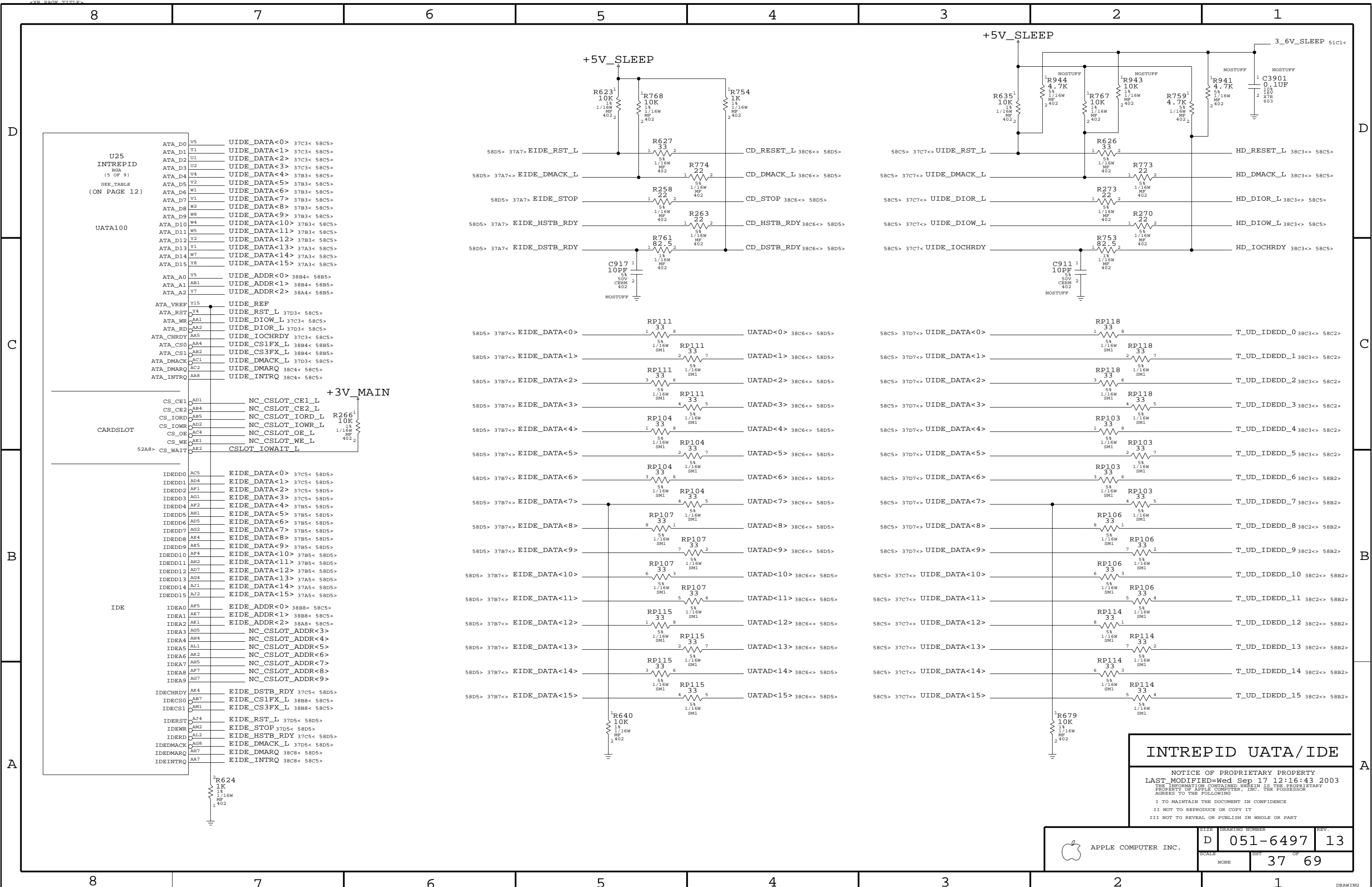
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
740S0527	2	FUSE, 0.75A, 30V, SMD	F2, F3	PWRS_Q26
740S0506	2	FUSE, 0.5A, SMD	F2, F3	PWRS_Q59

FIREWIRE PHY

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APPLE COMPUTER INC.	SCALE	D	DRAWING NUMBER	051-6497	REV.	13
		NONE	SHT	36	OF	69



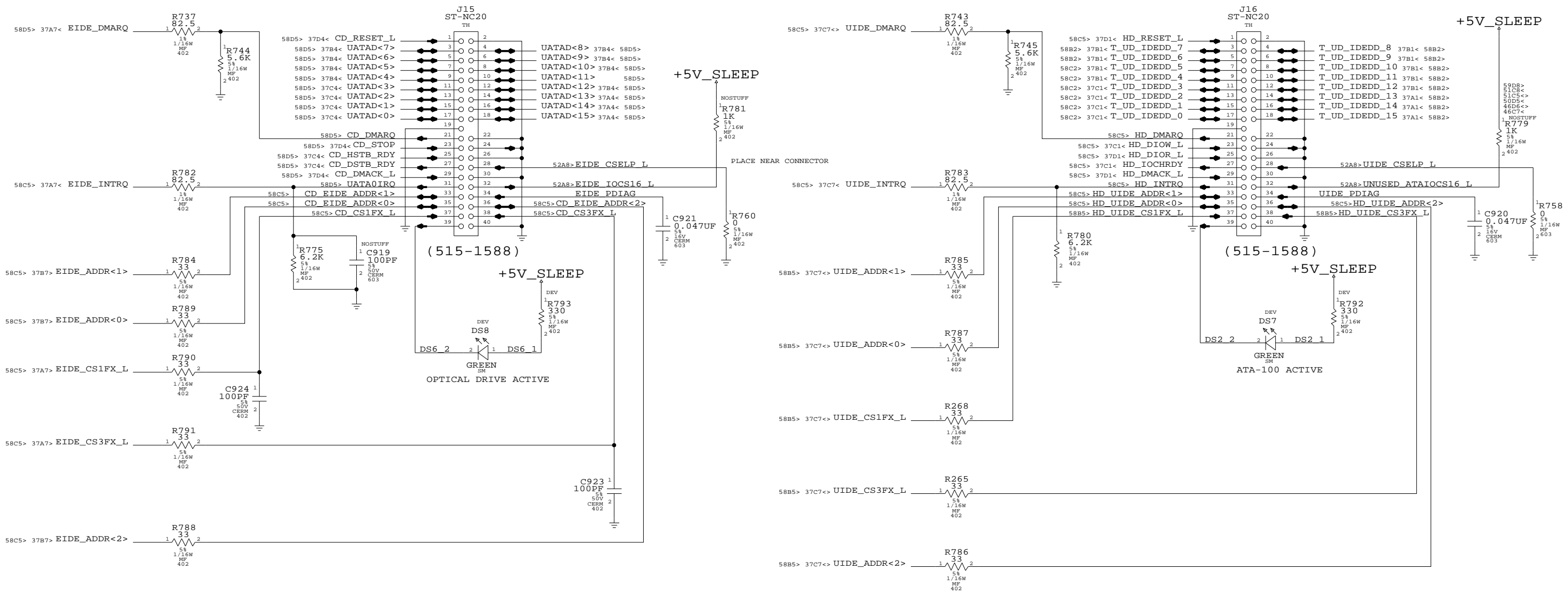
INTREPID UATA/IDE

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	D	051-6497	13
SCALE	SHT	OF	
NONE	37	69	

OPTICAL DRIVE INTERFACE

ATA-100 INTERFACE

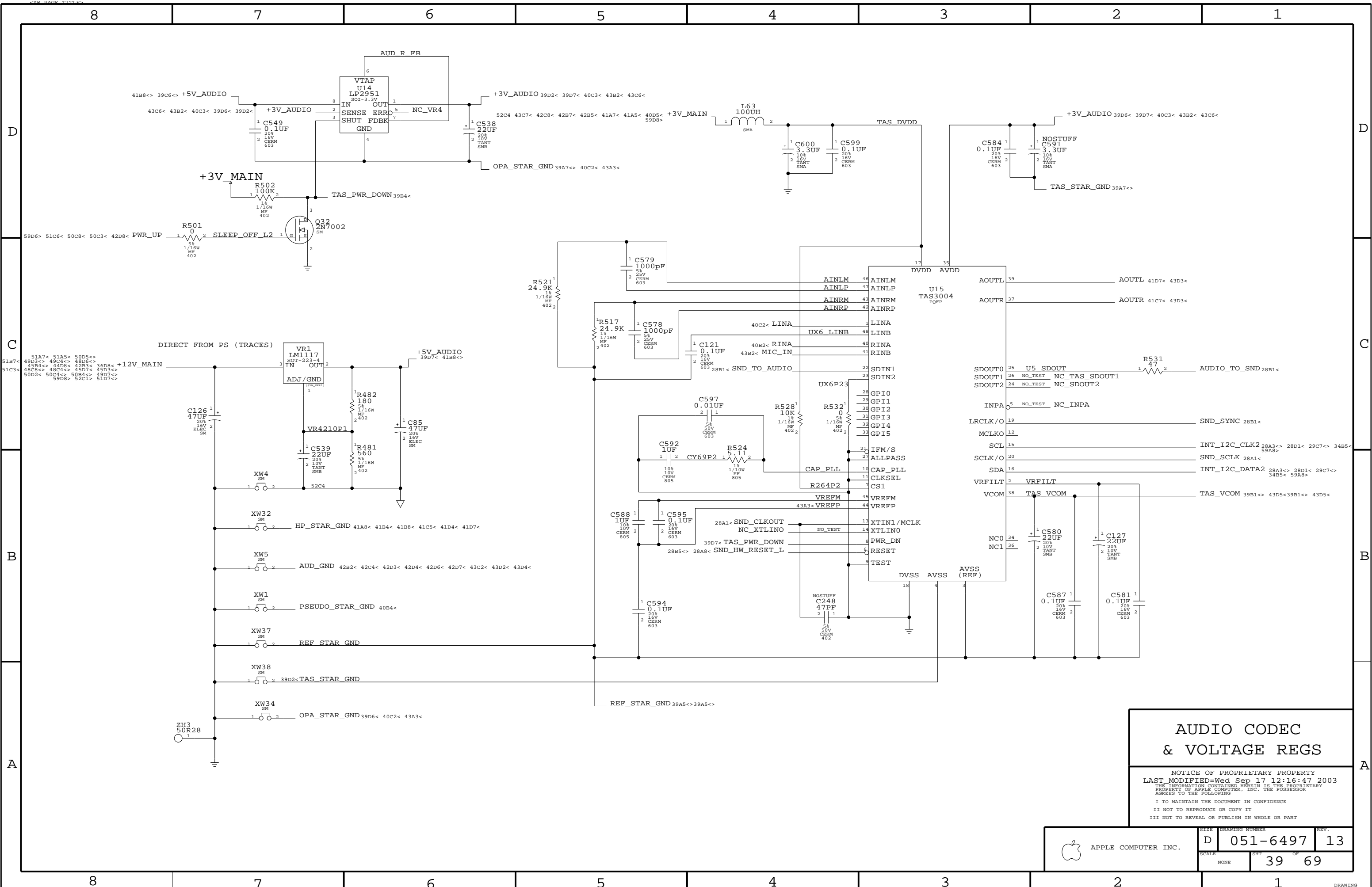


CD/HD CONS

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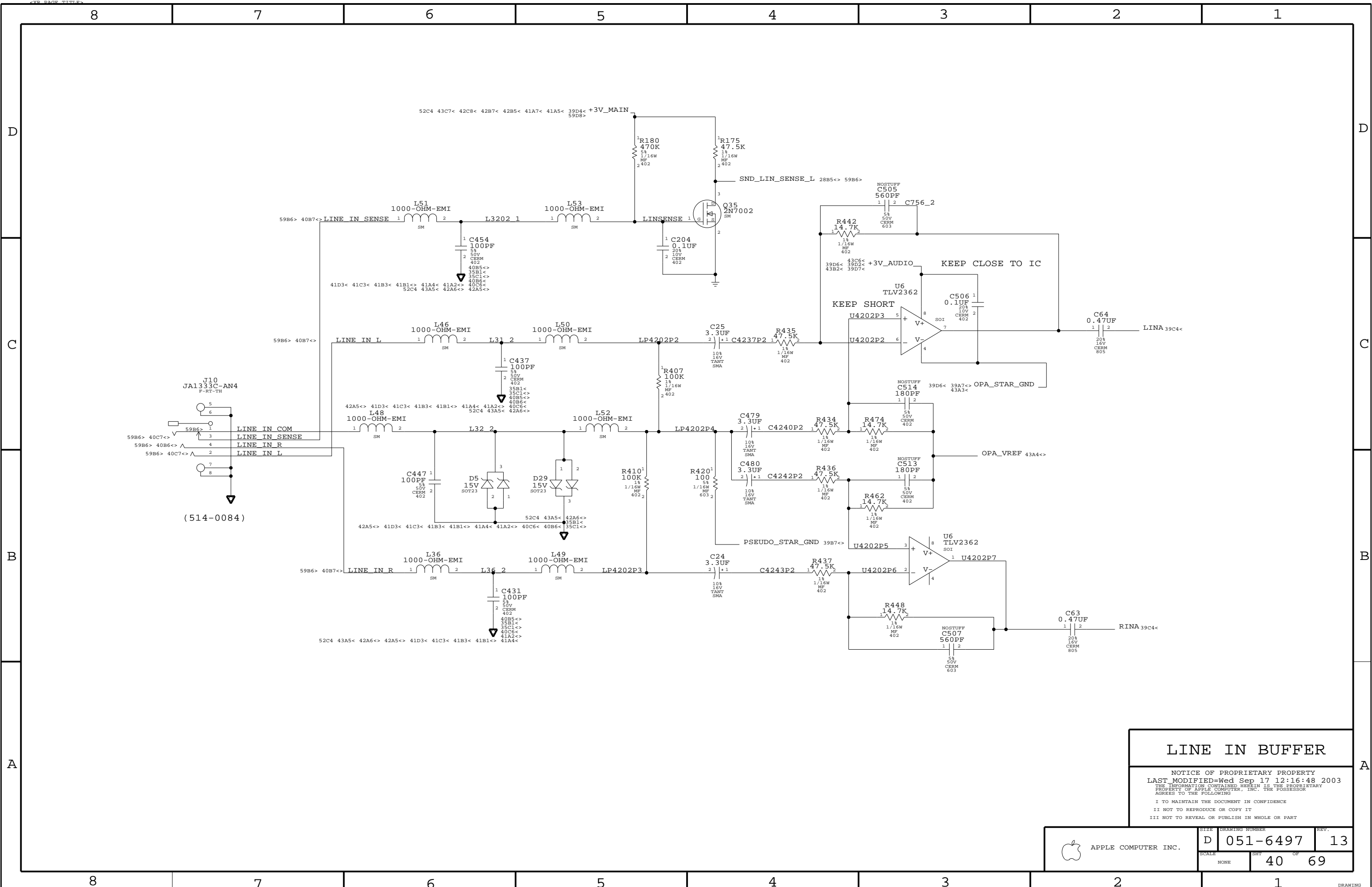
APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	NONE	051-6497	13
		SHT	OF
		38	69



AUDIO CODEC & VOLTAGE REGS

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	SCALE: SHEET OF NONE 39 OF 69



(514-0084)

LINE IN BUFFER

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APPLE COMPUTER INC.	SIZE DRAWING NUMBER D 051-6497	REV. 13
	SCALE SHEET OF NONE 40 OF 69	

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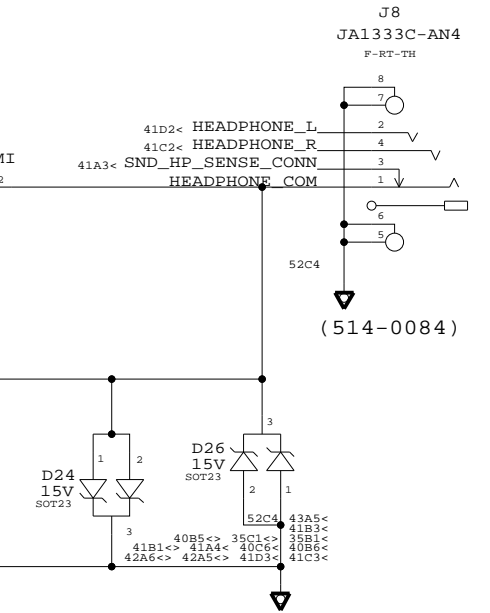
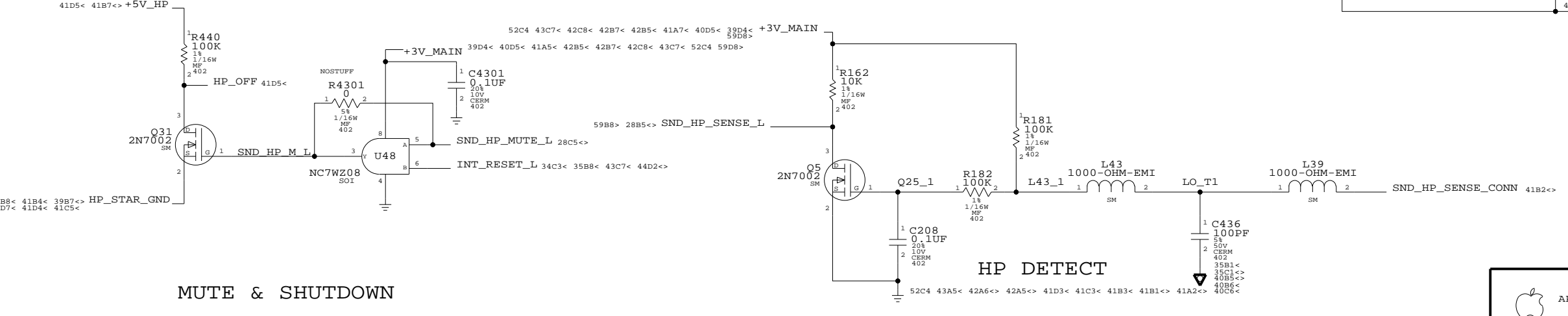
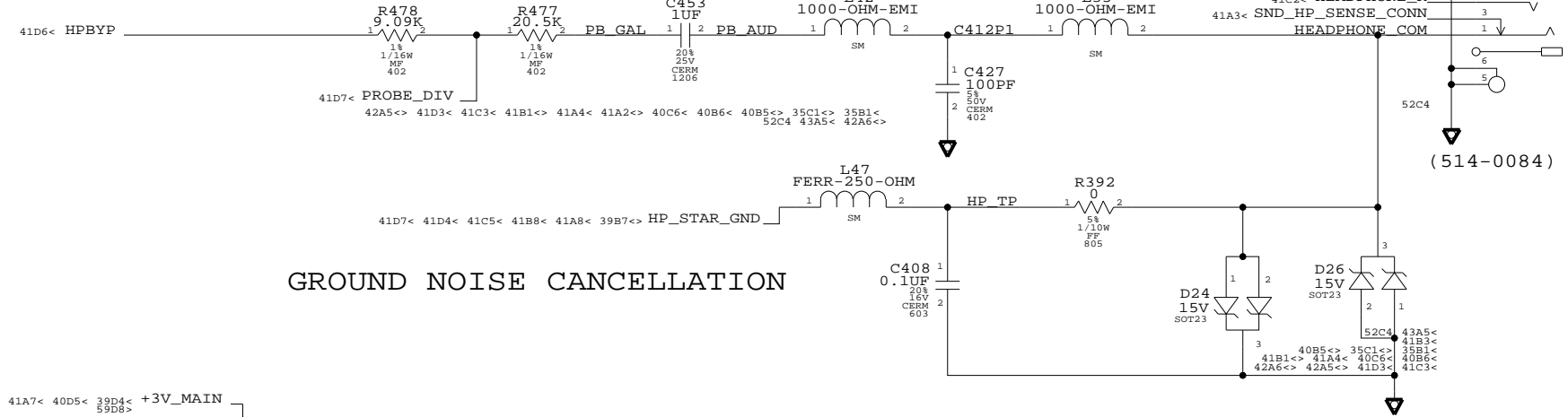
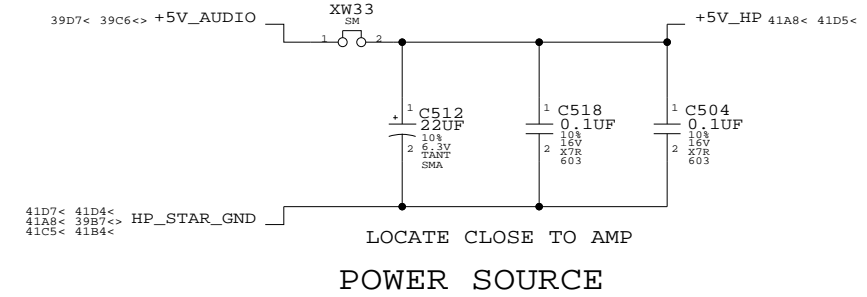
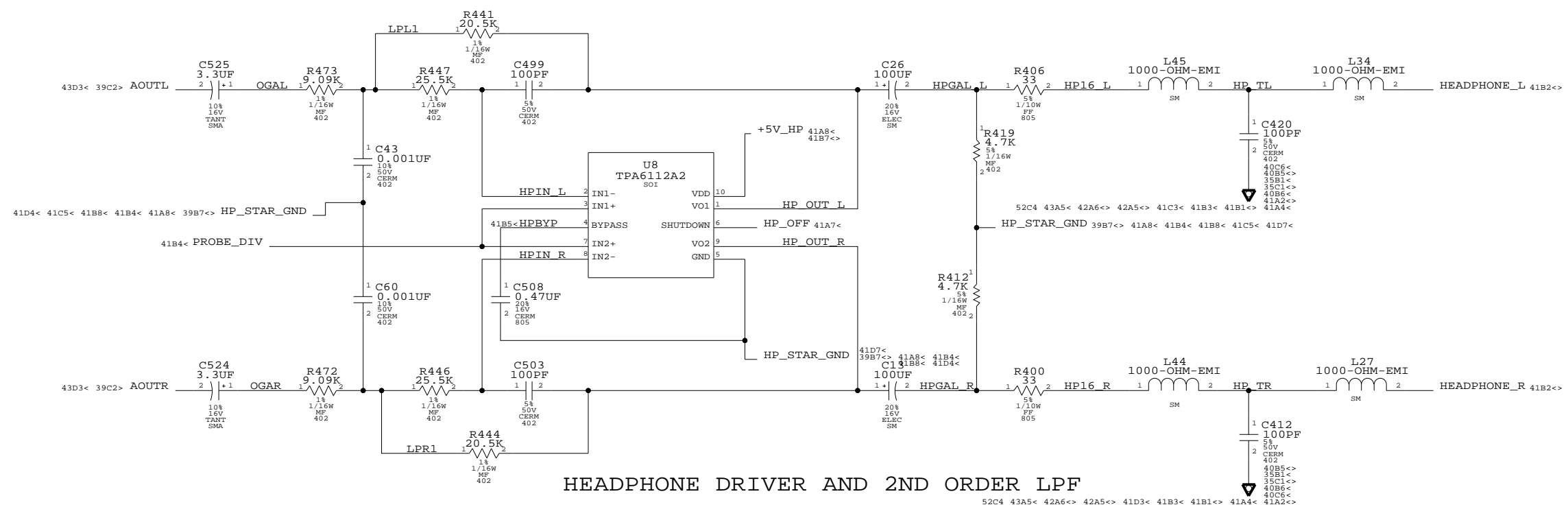
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HEADPHONE OUT AMP

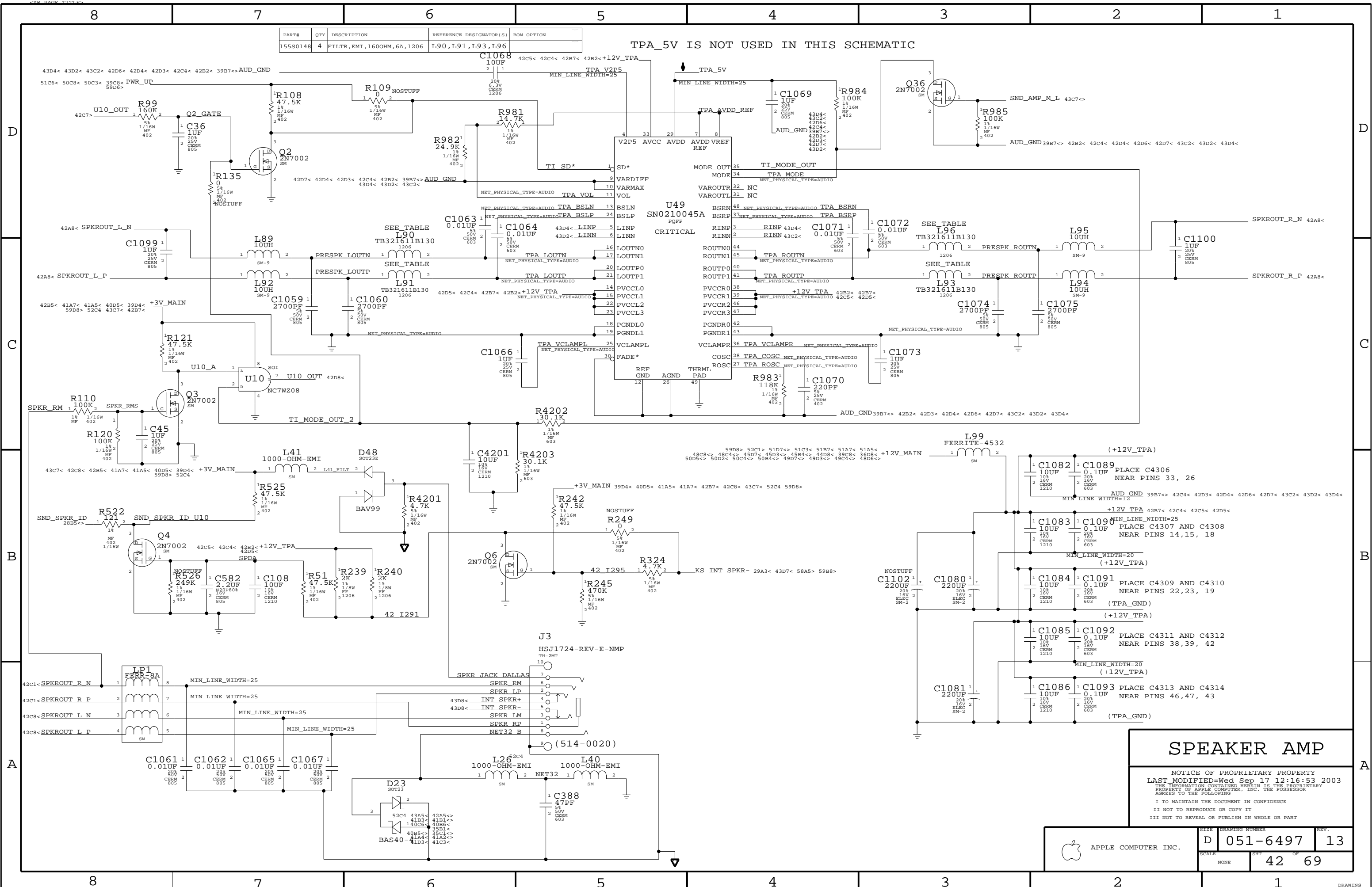
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SCALE		SHT	OF
NONE		41	69

PARTS	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
155S0148	4	FILTR,EMI,160OHM,6A,1206	L90,L91,L93,L96	

TPA_5V IS NOT USED IN THIS SCHEMATIC



SPEAKER AMP

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APPLE COMPUTER INC.	SCALE	SHT	REV.
	NONE	42	69

D

C

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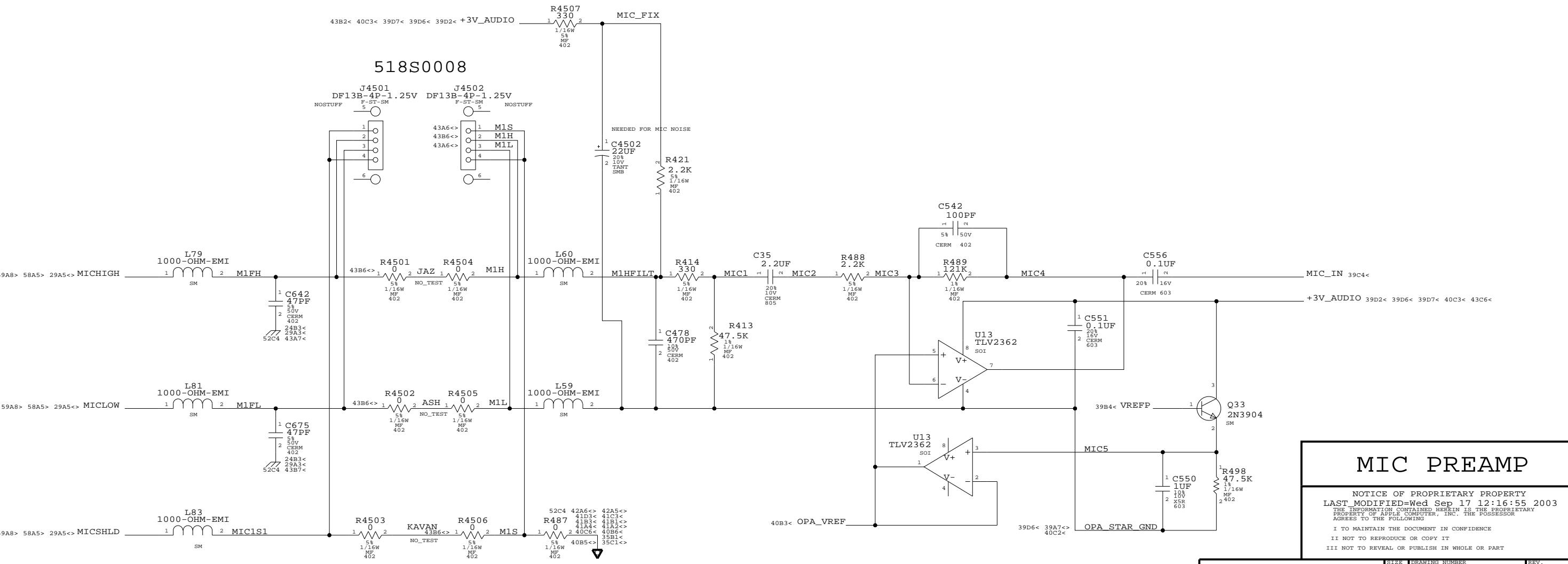
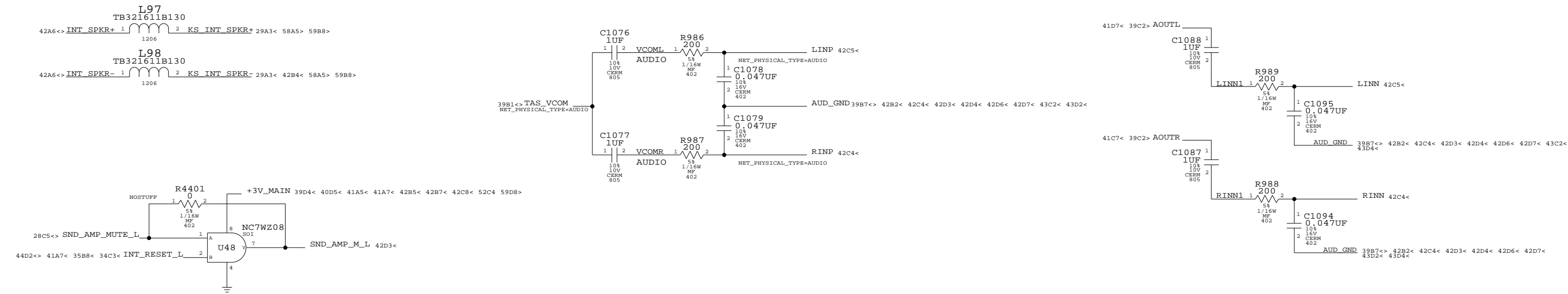
A

D

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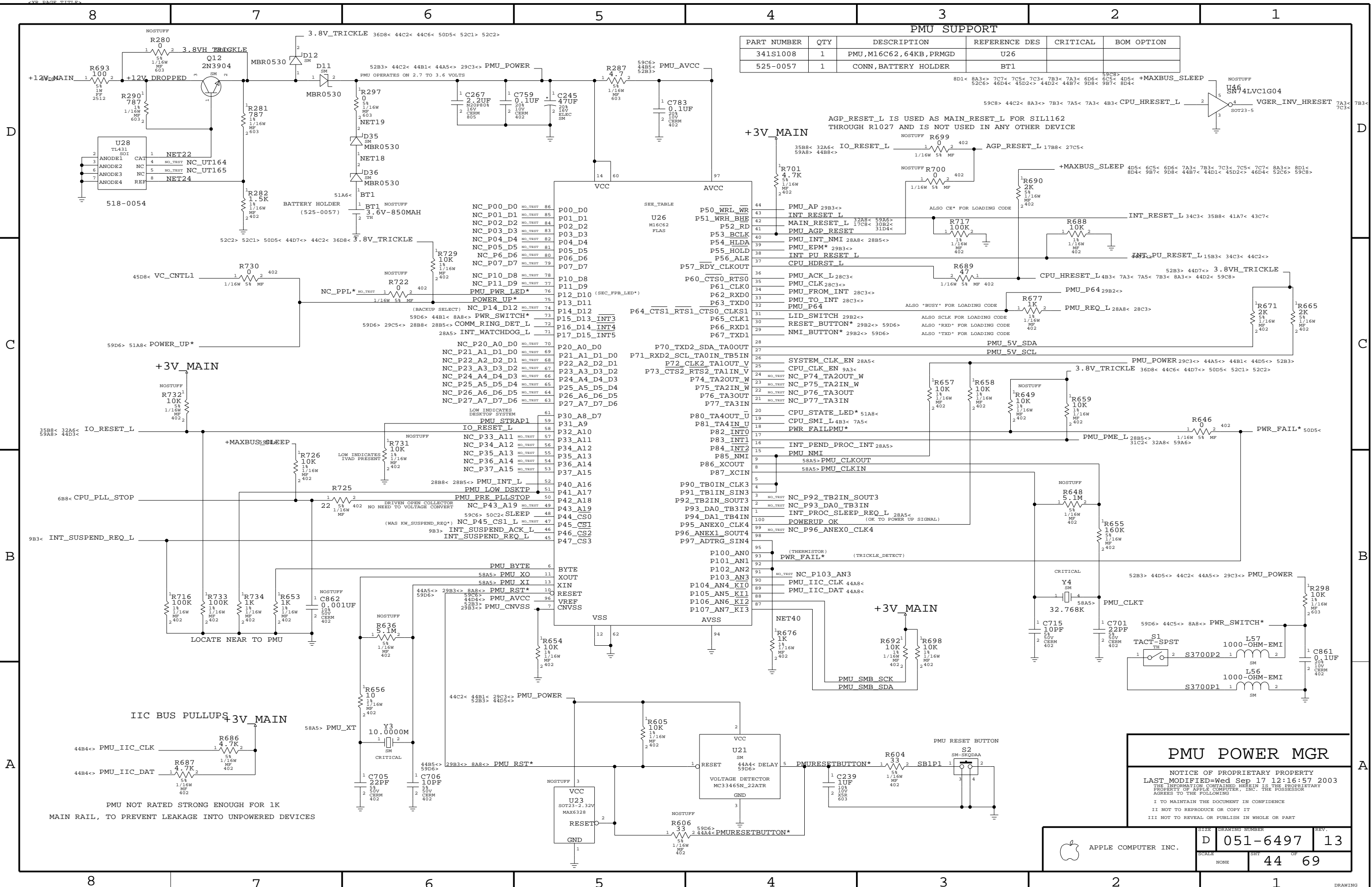
PLACE R4501, R4502 AND R4503 NEAR AUDIO

PLACE R4504, R4505 AND R4506 NEAR KITCHENSINK

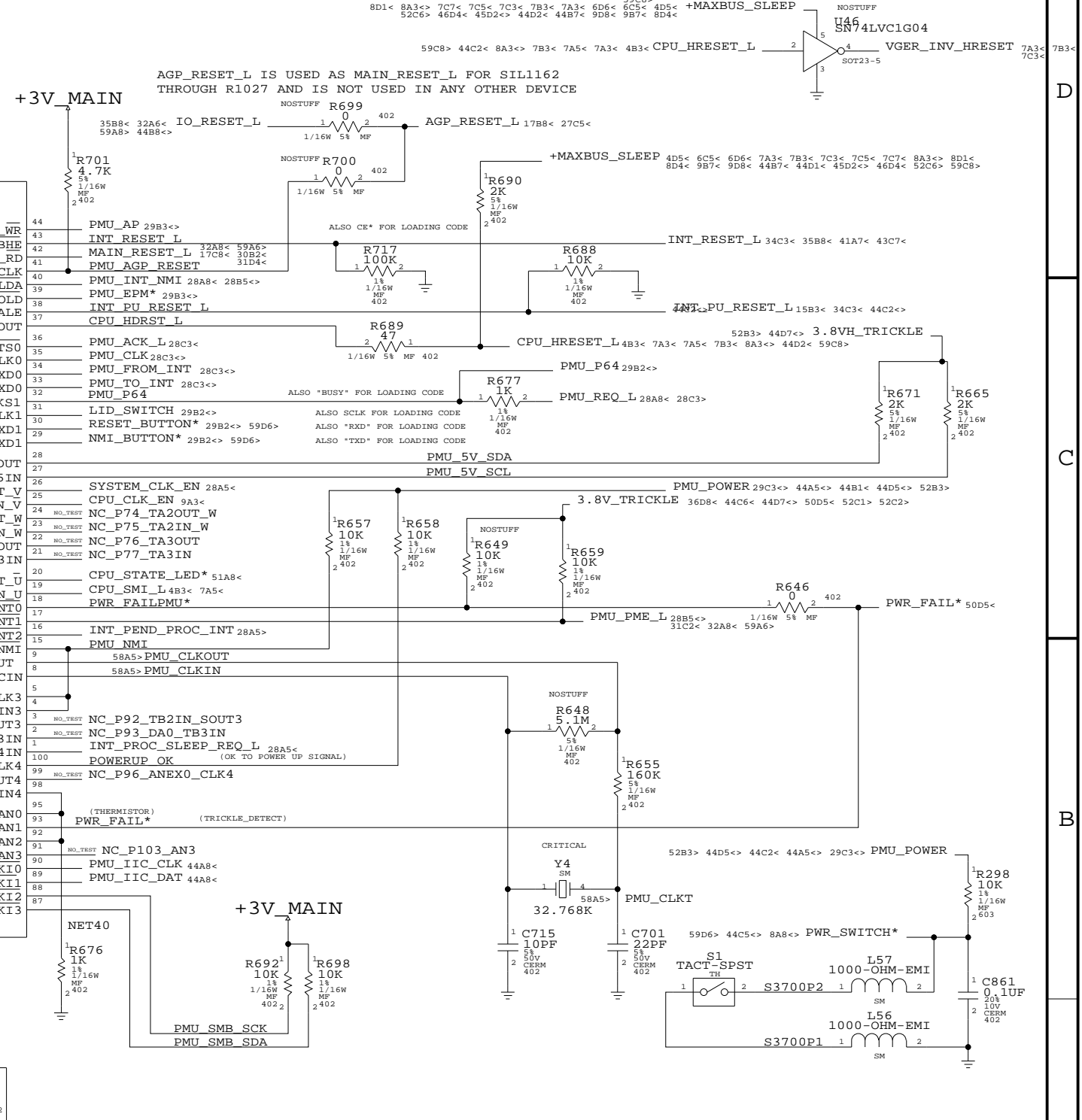
MIC PREAMP

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	D	051-6497	13
SCALE	SHT	OF	
NONE	43	69	



PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
341S1008	1	PMU, M16C62, 64KB, PRMGD	U26		
525-0057	1	CONN, BATTERY HOLDER	BT1		

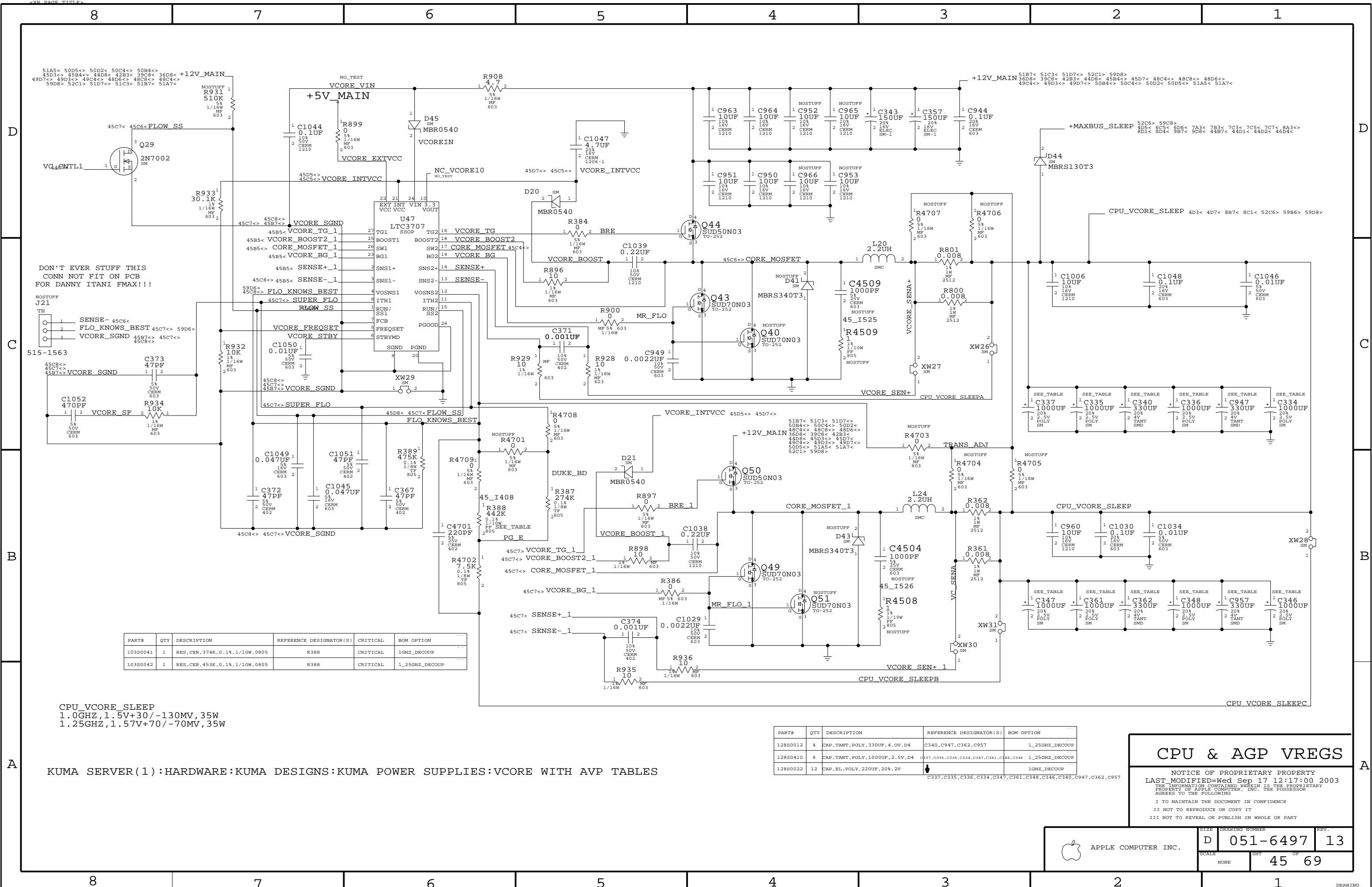


PMU POWER MGR

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	D	051-6497	13
SCALE	SHEET	OF	
NONE	44	69	

PMU NOT RATED STRONG ENOUGH FOR 1K MAIN RAIL, TO PREVENT LEAKAGE INTO UNPOWERED DEVICES



DON'T EVER STUFF THIS
CONN NOT FIT ON PCB
FOR DANNY ITANI FMAX!!!

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
103S0041	1	RES,CER,374K,0.1%,1/10W,0805	R388	CRITICAL	1GHZ_DECOUP
103S0042	1	RES,CER,453K,0.1%,1/10W,0805	R388	CRITICAL	1_25GHZ_DECOUP

CPU_VCORE_SLEEP
1.0GHZ,1.5V+30/-130MV,35W
1.25GHZ,1.57V+70/-70MV,35W

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
128S0012	4	CAP,TANT,POLY,330UF,4.0V,D4	C340,C947,C362,C957	1_25GHZ_DECOUP
128S0410	8	CAP,TANT,POLY,1000UF,2.5V,D4	C337,C335,C336,C334,C347,C361,C48,C346	1_25GHZ_DECOUP
128S0022	12	CAP,EL,POLY,220UF,20%,2V		1GHZ_DECOUP

C337,C335,C336,C334,C347,C361,C348,C346,C340,C947,C362,C957

KUMA SERVER(1):HARDWARE:KUMA DESIGNS:KUMA POWER SUPPLIES:VCORE WITH AVP TABLES

CPU & AGP VREGS

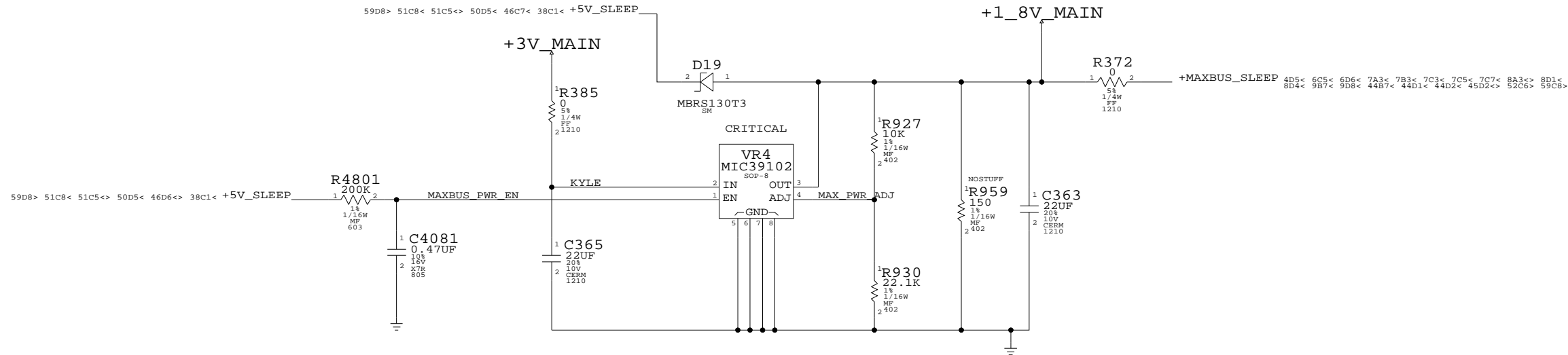
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SCALE	SHT	OF	
NONE	45	69	

INTREPID MAXBUS & CPU OVDD POWER CONVERTER
(OFF DURING SLEEP)

+MAXBUS_SLEEP 1.8V, +/-2%, .606W

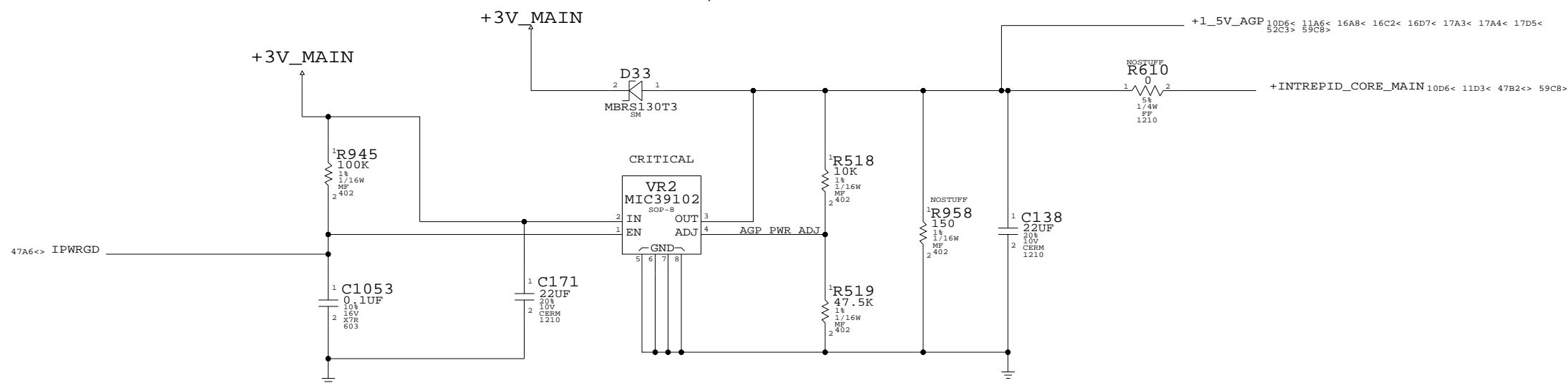


MAXBUS I/O SUPPLY SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
114S4754	1	RES, FF, 47.5K-OHM, 1%	R930		MAXIO_1'50V
114S3014	1	RES, FF, 30.1K-OHM, 1%	R930		MAXIO_1'65V
114S2674	1	RES, FF, 26.7K-OHM, 1%	R930		MAXIO_1'70V
114S2214	1	RES, FF, 22.1K-OHM, 1%	R930		MAXIO_1'80V *

+1_5V_AGP 1.5V, +/-5%, .6W

AGP I/O POWER CONVERTER



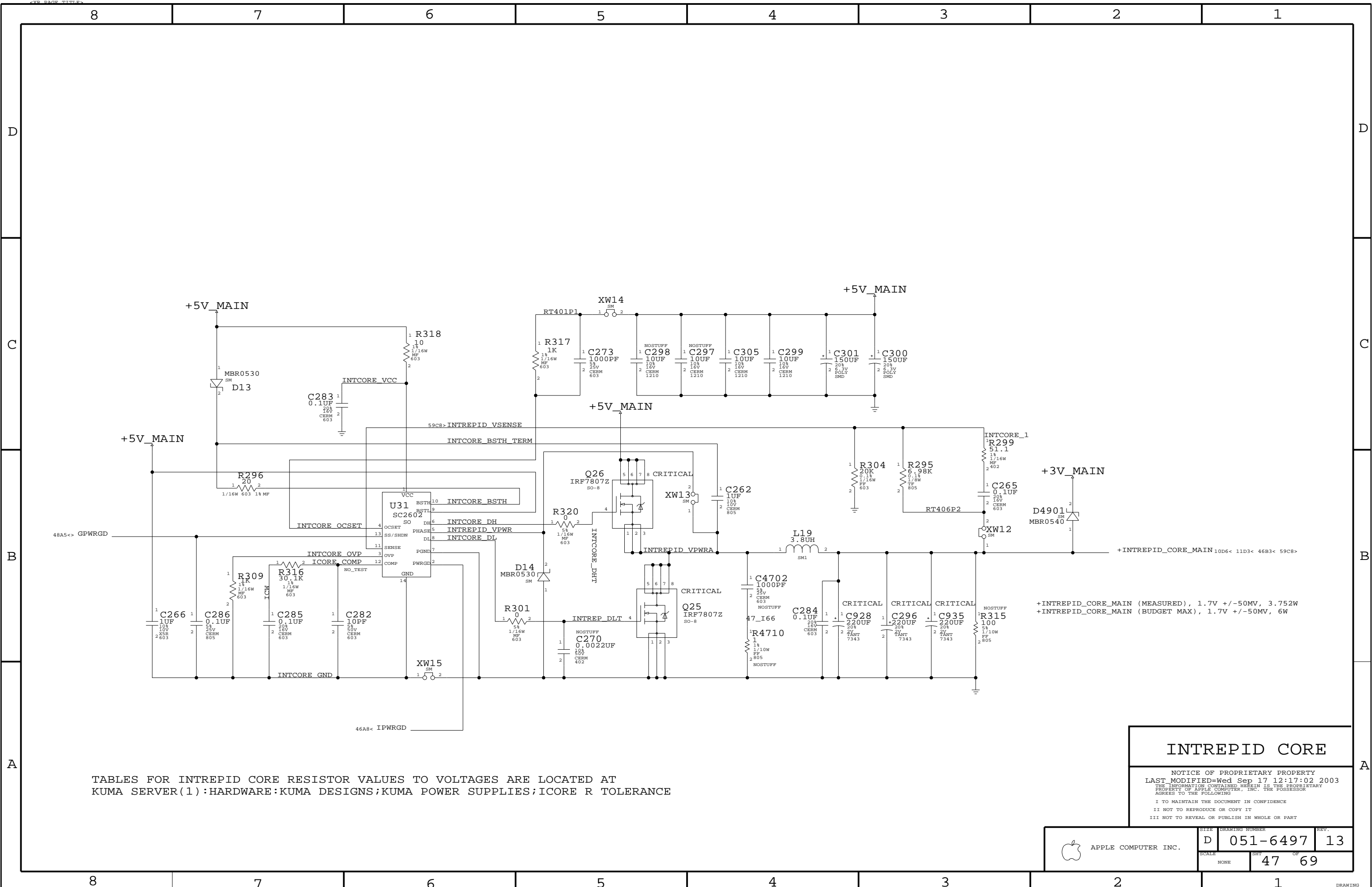
AGP I/O SUPPLY SUPPORT

PART NUMBER	QTY	DESCRIPTION	REFERENCE DES	CRITICAL	BOM OPTION
114S4754	1	RES, FF, 47.5K-OHM, 1%	R519		AGPIO_1'50V *
114S3014	1	RES, FF, 30.1K-OHM, 1%	R519		AGPIO_1'65V
114S2674	1	RES, FF, 26.7K-OHM, 1%	R519		AGPIO_1'70V
114S2214	1	RES, FF, 22.1K-OHM, 1%	R519		AGPIO_1'80V

CPU & AGP VREGS

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SCALE		SHT	OF
NONE		46	69

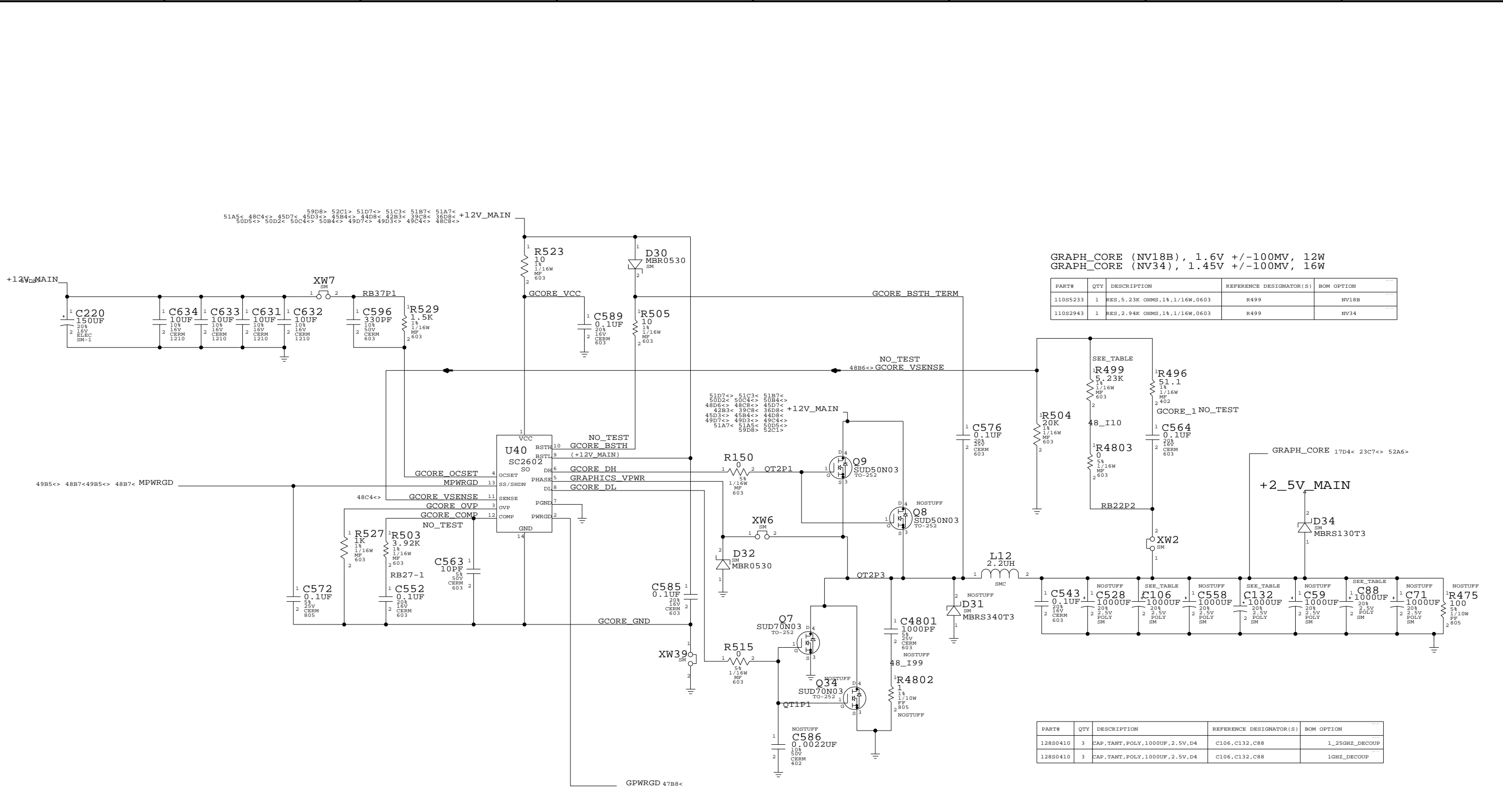


TABLES FOR INTREPID CORE RESISTOR VALUES TO VOLTAGES ARE LOCATED AT
 KUMA SERVER(1):HARDWARE:KUMA DESIGNS;KUMA POWER SUPPLIES;ICORE R TOLERANCE

INTREPID CORE

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SCALE	SHEET		OF
NONE	47		69



GRAPH_CORE (NV18B), 1.6V +/-100MV, 12W
 GRAPH_CORE (NV34), 1.45V +/-100MV, 16W

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
110S233	1	RES,5.23K OHMS,1%,1/16W,0603	R499	NV18B
110S2943	1	RES,2.94K OHMS,1%,1/16W,0603	R499	NV34

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
128S0410	3	CAP,TANT,POLY,1000UF,2.5V,D4	C106,C132,C88	1_25GHZ_DECOUP
128S0410	3	CAP,TANT,POLY,1000UF,2.5V,D4	C106,C132,C88	1GHZ_DECOUP

GRAPHICS CORE

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SCALE	SHT		OF
NONE	48		69

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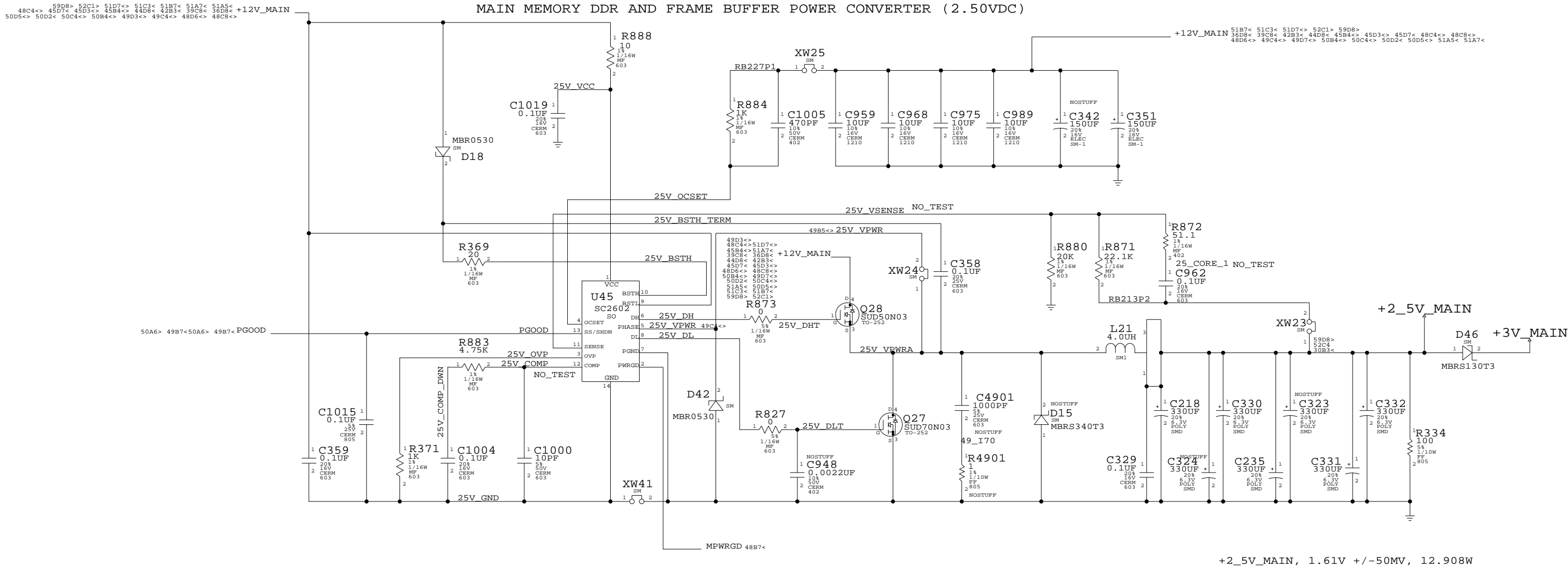
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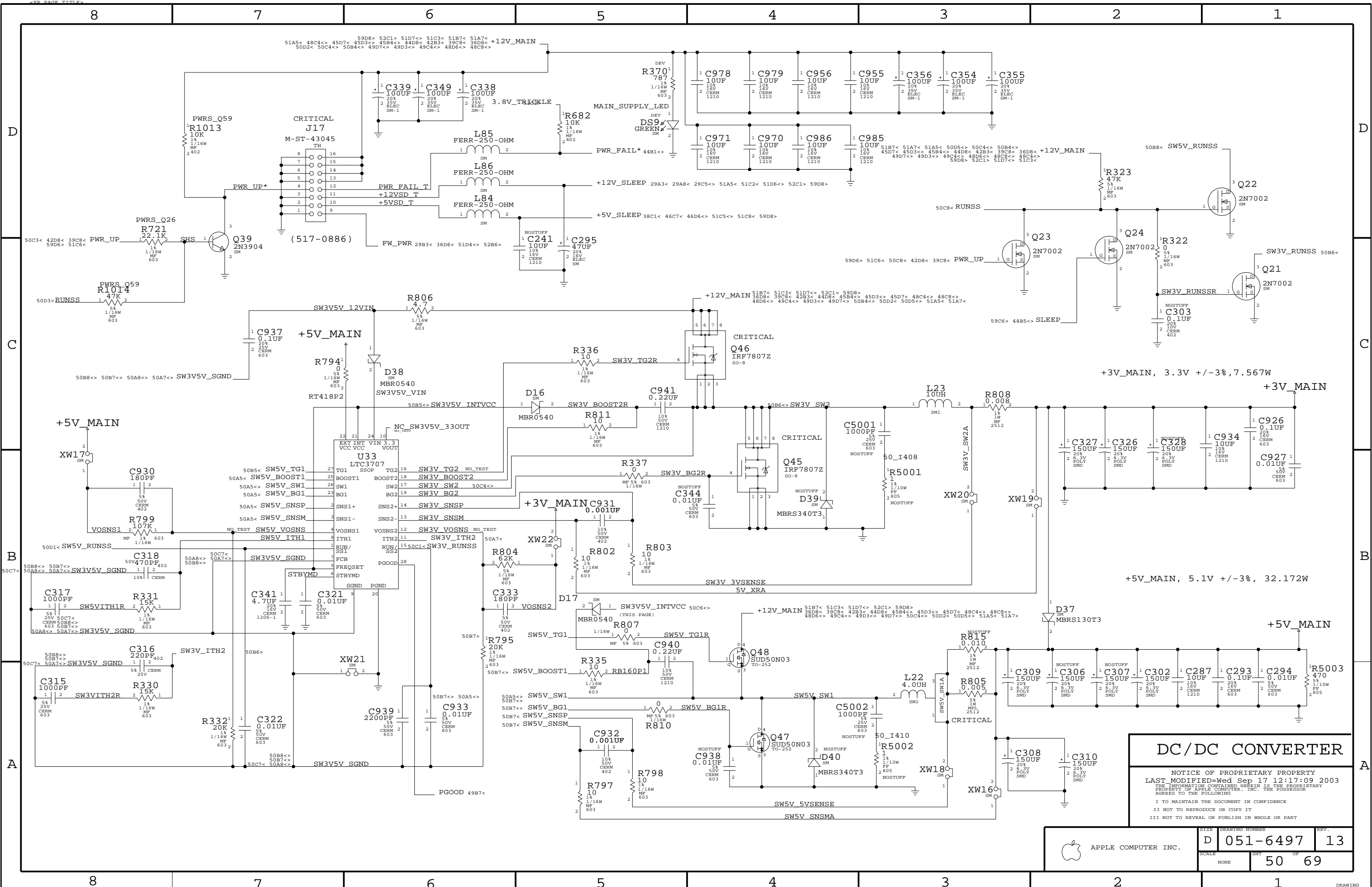


MEMORY PS

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	NONE	D 051-6497	13
SCALE		SHT 49 OF 69	



DC/DC CONVERTER

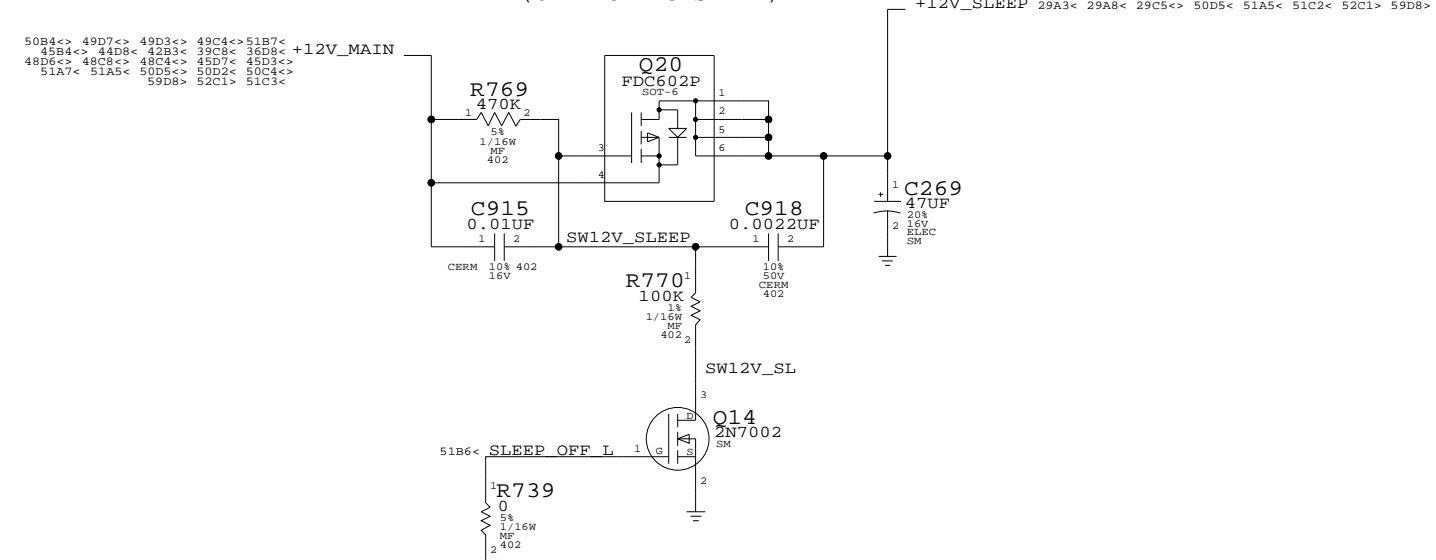
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SCALE: SHEET OF NONE 50 OF 69	

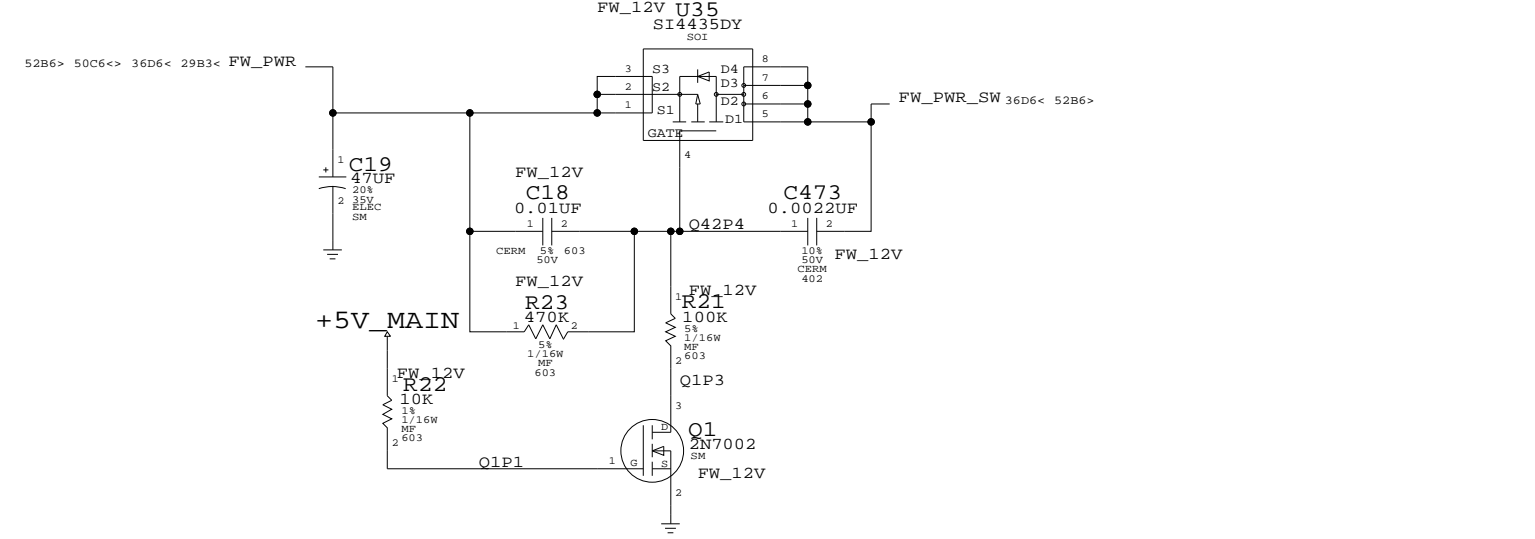
8 7 6 5 4 3 2 1

8 7 6 5 4 3 2 1

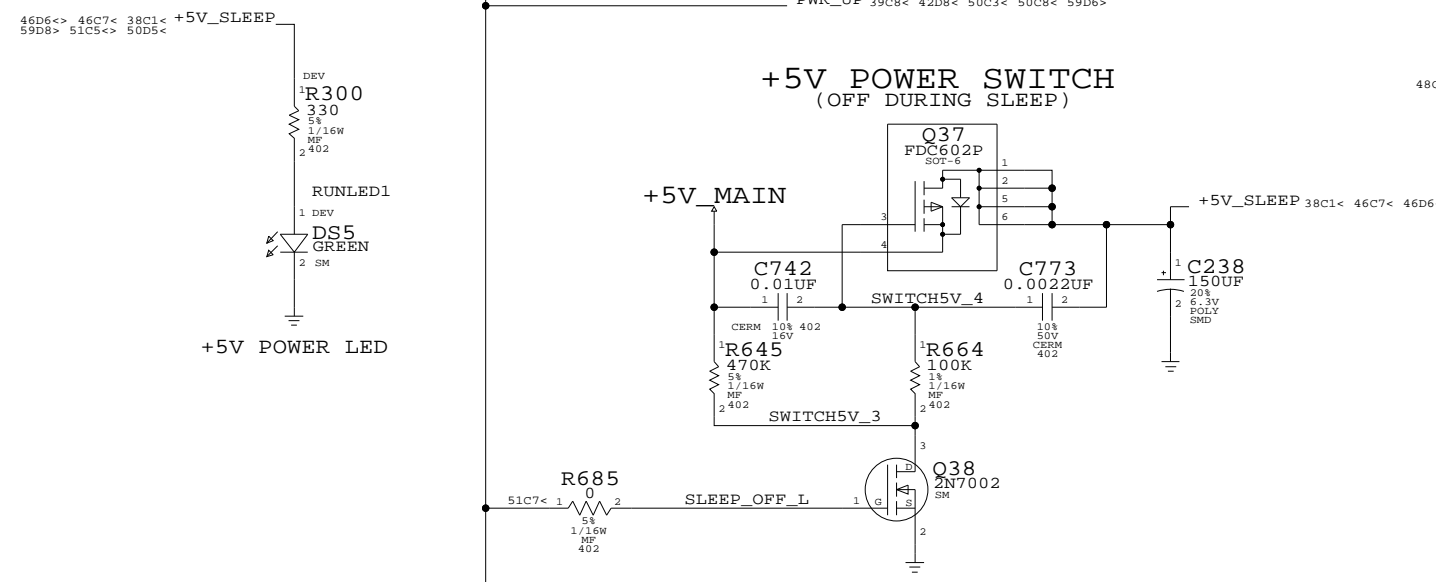
+12V MAIN POWER SWITCH (OFF DURING SLEEP)



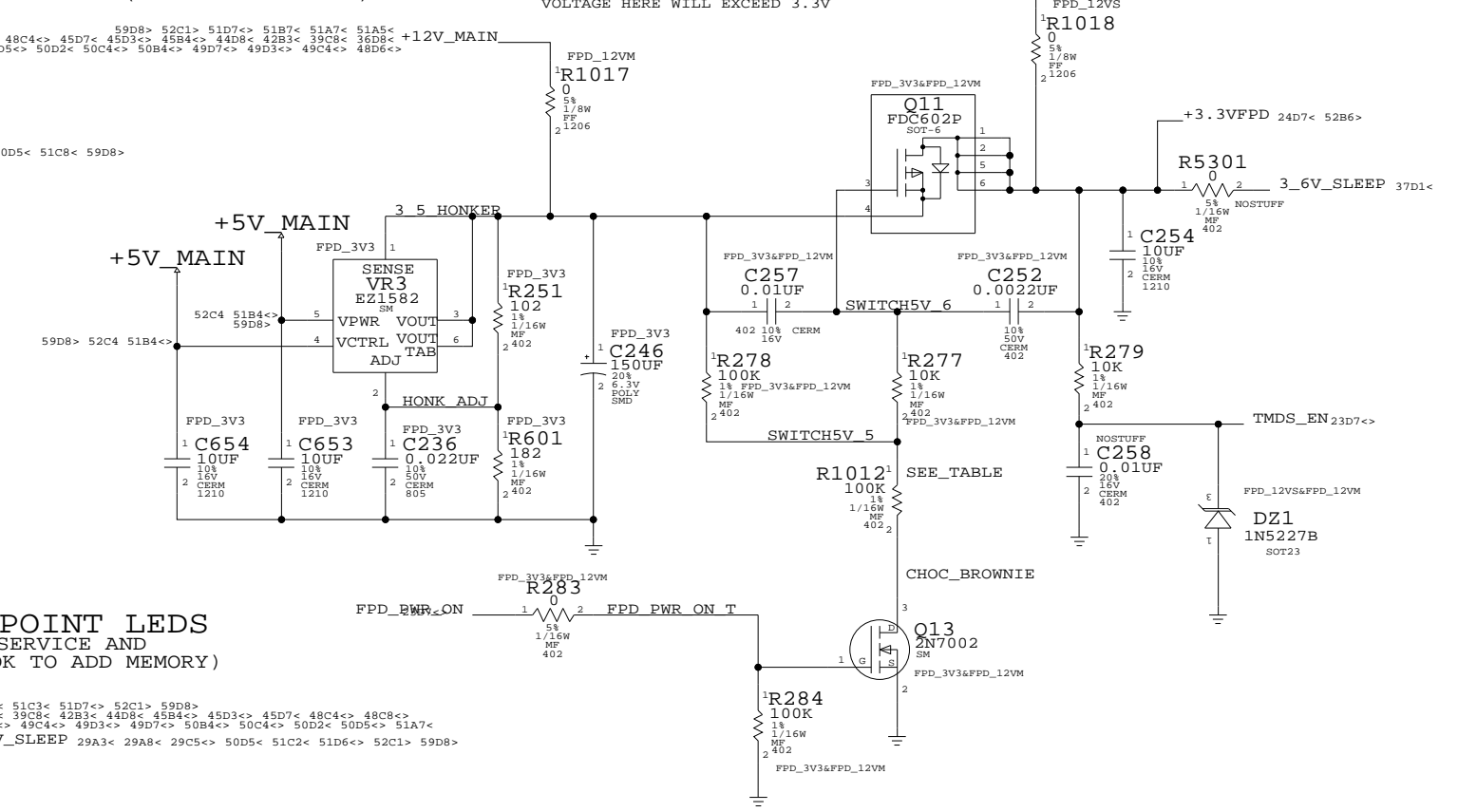
FIREWIRE POWER SWITCH EVALUATE CIRCUIT FOR SURGE PROTECTION FOR Q59C



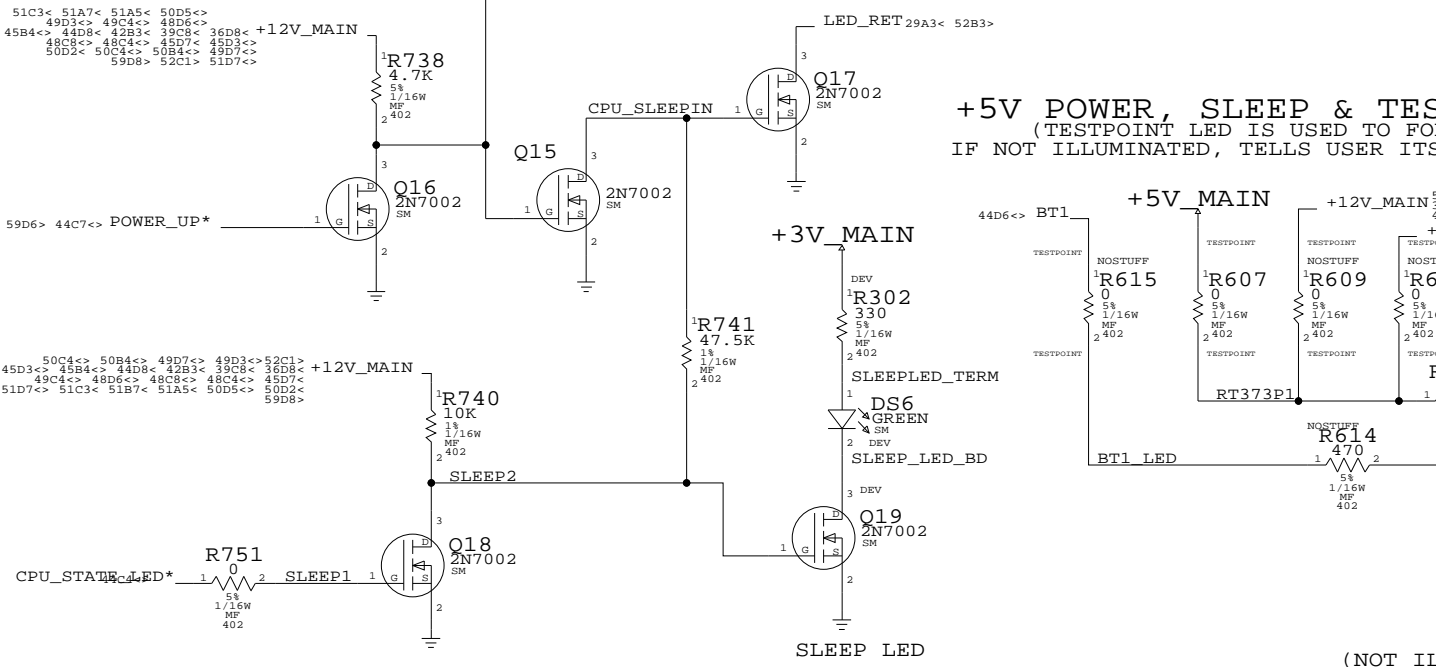
+5V POWER SWITCH (OFF DURING SLEEP)



TMDS POWER CONVERTER & SWITCH (OFF DURING SLEEP)



+5V POWER, SLEEP & TESTPOINT LEDES (TESTPOINT LED IS USED TO FOR SERVICE AND IF NOT ILLUMINATED, TELLS USER ITS OK TO ADD MEMORY)



PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
114S1005	1	RES,100K OHM,1%,1/16W,0402,SMD	R1012	FPD_12VM
116S1000	1	RES,0 OHM,5%,1/16W,0402,SMD	R1012	FPD_3V3

+5V/+12V, AUDIO FW & TMDS PWR

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APPLE COMPUTER INC.	SCALE	DRAWING NUMBER	REV.
	NONE	D 051-6497	13
SHEET		OF	
51		69	

CPU POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+MAXBUS_SLEEP	10	1.8	20
CPU_AVDD	10	1.85	20
CPU_VCORE_SLEEP	10	1.85	20

4D5< 6C5< 6D6< 7A3< 7B3< 7C3< 7C5< 7C7< 8A3< 8D1< 8D4< 9B7< 9D8< 44B7<
 44D1< 44D2< 45D2<> 46D4< 59C8>
 4D3< 4D7< 8B7< 8C1< 45D2<> 59B6< 59D8>

ETHERNET POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
ENET_AVDD	10	2.5	20

43C7< 42C8< 42B7< 42B5< 41A7< 41A5< 40D5< 39D4< 59D8>
 59D8> 49B2<> 30B3<
 59D8> 51B4<>

FIREWIRE POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
FW_DIO_V	10	3.3	20
FW_DIODE_BYPASS_V	10	3.3	20
FW_PWR	10	24	20
FW_PWR_SW	10	24	20
FW_PHY_3_3	10	3.3	20
FW_VGND	10	0	20
FW_VP	10	1.2	20
FW_VP1	10	1.2	20
FW_VP2	10	1.2	20
FW_VP_1	10	1.2	20
FW_VP_2	10	1.2	20

35D2<> 35D4<>
 39B7<>
 41B3< 41B1<> 41A4< 41A2<> 40C6< 40B6< 40B5<> 35C1<> 35B1<
 36B2< 36A7<> 33D4< 33D2< 33C4< 33C2< 31B4< 31B2< 29C3< 29B3<>
 36C1< 36C1<> 36B6<> 36B6<
 43B7< 43A7< 29A3< 24B5<

GRAPHICS POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+3.3VFPD	10	3.6	20
DAC2VDD	10	3.3	20
DACVDD	10	3.3	20
DDC_VCC_3	10	3.3	20
DDC_VCC_5	10	5	20
DDR_VREF	10	1.25	20
IFP0AVCC	10	3.8	20
IFP0VREF	10	3.8	20
INT_TMDS_3V	10	3.6	20
GPU_AGP_VREF	10	0.75	20
GPU_FB_VREF	10	1.25	20
GRAPH_CORE	10	1.6	20
NVPLLVD	10	3.3	20
SGRAVREF	10	1.25	20
SGRBVREF	10	1.25	20

36B6<
 36B6<> 36B7<>
 29B3< 36D6< 50C6<> 51D4<>
 36D6< 51D2<>
 36B5< 36B7< 36D7<
 36D5<
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 18C8<
 17D4< 23C7<> 48C2<>
 22D5<
 20A3< 20C4< 20C8<
 21A3< 21C4< 21C8<

17B5<> GPU_50PULLUP	1.5	
17A5<> GPU_50PULLDOWN	0	
17A5< GPU_TMODE	0	
22B2< 22A5< GPU_XTALSSIN	0	
22D4< VIPCLK	0	
37B7< CSL0T_IOWAIT_L	3.3	
38C6<> EIDE_CSELP_L	0	
38C6<> EIDE_IOCS16_L	5	
38C2<> UIIDE_CSELP_L	0	
38C2<> UNUSED_ATAI0CS16_L	5	

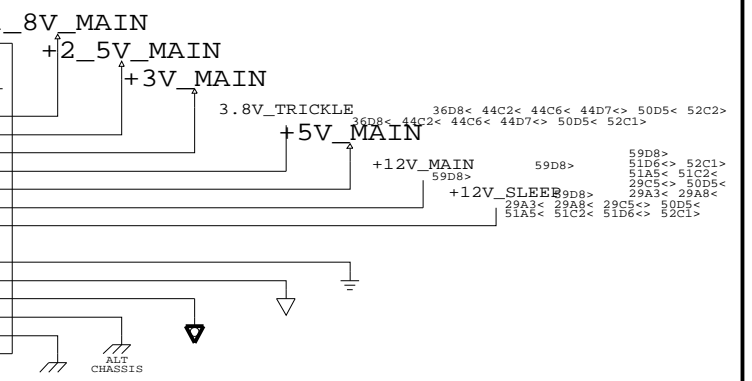
INTREPID POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+1_5V_INTREPID_PLL	10	1.5	20
+1_5V_INTREPID_PLL1	10	1.5	20
+1_5V_INTREPID_PLL2	10	1.5	20
+1_5V_INTREPID_PLL3	10	1.5	20
+1_5V_INTREPID_PLL4	10	1.5	20
+1_5V_INTREPID_PLL5	10	1.5	20
+1_5V_INTREPID_PLL6	10	1.5	20
+1_5V_INTREPID_PLL7	10	1.5	20
+1_5V_INTREPID_PLL8	10	1.5	20
+1_5V_AGP	10	1.5	20
INT_AGP_VREF	10	0.75	20

9D4< 16D6< 28D6<> 30D5<
 28C4<
 28D4<
 28D4<
 28D4<
 16D5<
 30D4<
 9D2<
 28D4<
 10D6< 11A6< 16A8< 16C2< 16D7< 17A3< 17A4< 17D5< 46B4<> 59C8>
 16A7< 16C6<>

MAIN POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+1_8V_MAIN	10	1.8	20
+2_5V_MAIN	10	2.5	20
+3V_MAIN	10	3.3	20
3.8V_TRICKLE	10	3.8	20
+5V_MAIN	10	5	20
+12V_MAIN	10	12	20
+12V_SLEEP	10	12	20
GND	10	0	20
AGND	10	0	20
ANALOGGND	10	0	20
ALTCHGND	10	0	20
CHGND	10	0	20



PMU POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
3.8VH_TRICKLE	10	3.8	20
PMU_AVCC	10	3.5	20
PMU_POWER	10	3.5	20

44C1< 44D7<>
 44B5< 44D4<> 59C6>
 29C3<> 44A5<> 44B1< 44C2< 44D5<>

SYSTEM POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+12VSD_FILT	10	12	20
FAN_12V_FILT	10	12	20
KSSVSD	10	5	20
LED_5V	10	5	20
LED_5V_FILT	10	5	20
LED_RET	10	0	20
LED_RET_FILT	10	0	20

29A5<>
 29A5<> 59C8>
 29A5<> 59A8>
 29A8<
 29A5<> 59A8>
 29A3< 51B6<
 29A5<> 59A8>

USB POWER CONSTRAINT TABLE

SIG_NAME	MIN_NECK_WIDTH	VOLTAGE	MIN_LINE_WIDTH
+3V_INTREPID_USB	10	3.3	20
NEC_AVDD	10	3.3	20
USB_GND	10	0	20
USB_PORT_PWR	10	5	20
USB_PWR	10	5	20

28C4<
 32D5<
 33A4<> 33B3<> 33C3<>
 33B3<> 33B3<> 25D3<> 33A6<>

POWER CONSTRAINTS

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6497	13
SCALE	SHT	OF	
NONE	52	69	

SIG_NAME	RATSNEST_SCHEDULE	RELATIVE_PROPAGATION_DELAY	MIN_NECK_WIDTH	MAX_EXPOSED_LENGTH	NO_TEST	FUNC_TEST	PULSE_PARAM
13C4<> 13B6<> 13B3<> 13A6<> 12D8<> 12C8<> 12B8<> MEM_DATA<0..63>	MEM_GROUP0:G:L:S:0:150	8 L:S::1300	3				167 MHZ
14D6<> 14D4<> 14C6<> 13C8<> 13B8<> 13B2<> RAM_DATA_A<0..63>	RAM_GROUP0_A:G:L:S:0:180	8 L:S::1800	3				167 MHZ
13D4<> 13C7<> 13C4<> 13C2<> 13B6<> 13B2<> RAM_DATA_B<0..63>	RAM_GROUP0_B:G:L:S:0:180	2 L:S::2400	3				167 MHZ
14C4<> 14B6<> 14B4<> 14A6<> 13D7<>	MEM_GROUP0:G:L:S:0:180	3 L:S::1300	3				167 MHZ
13C8<> 13C4<> 13B3<> 13A6<> 12C6<> MEM_DQS<0..7>	MEM_GROUP0_A:G:L:S:0:180	3 L:S::1700	3				167 MHZ
14A6<> 13D7<> 13D4<> 13C7<> 13C4<> 13B5<> 13B2<> RAM_DQS_A<0..7>	RAM_GROUP0_B:G:L:S:0:180	2 L:S::2400	3				167 MHZ
13C7<> 13C4<> 13B7<> 13B4<> 13B2<> 13A8<> 13A6<> 13A4<> RAM_DQS_B<0..7>	MEM_GROUP0:G:L:S:0:180	3 L:S::1300	3				167 MHZ
15D6<> 15C8<> 15C6<> 15B8<> 15B6<> 15A8<> 15A6<> MEM_DQM<0..7>	RAM_GROUP0_A:G:L:S:0:180	3 L:S::1800	3				167 MHZ
13D7<> 13D4<> 13C7<> 13C4<> 13B5<> 13B2<> 13A5<> RAM_DQM_A<0..7>	RAM_GROUP0_B:G:L:S:0:180	2 L:S::2400	3				167 MHZ
13C7<> 13C4<> 13B7<> 13B4<> 13B2<> 13A8<> 13A6<> 13A4<> RAM_DQM_B<0..7>	MEM_ADDR:G:L:S:0:200	3 L:S::600					
12D6<> 12D3<> 12D2<> 12C3<> 12C2<> 12B3<> MEM_ADDR<0..12>	RAM_ADDR:G:L:S:0:1300	4 L:S::3500	200				
15B4<> 14B6<> 14B4<> 14B2<> 12D1<> 12C3<> 12C1<> 12B3<> RAM_ADDR<0..12>	MEM_ADDR:G:L:S:0:1300	3 L:S::600					
15C6<> 15C4<> 15B6<> 15B4<> 15A6<> 15A4<> 15A2<> 15A0<> MEM_BA<0..1>	RAM_ADDR:G:L:S:0:1300	4 L:S::4000	200				
15B6<> 14B6<> 14B4<> 14B2<> 12B3<> RAM_BA<0..1>	MEM_ADDR:G:L:S:0:200	3 L:S::600	10 MIL SPACING				
12C6<> 12C2<> 12B2<> MEM_CS_L<0..3>	RAM_CS_GROUP0:G:L:S:0:400	3 L:S:2000:3500	10 MIL SPACING				
14B6<> 14B4<> 12C1<> RAM_CS_L<0..1>	RAM_CS_GROUP1:G:L:S:0:350	2 L:S:2000:3500	10 MIL SPACING				
15B4<> 12B1<> RAM_CS_L<2..3>	MEM_ADDR:G:L:S:0 MIL:200 MIL	3 L:S::600 MIL					
12C6<> 12A3<> MEM_RAS_L	MEM_ADDR:G:L:S:0 MIL:200 MIL	3 L:S::600 MIL					
12C6<> 12A3<> MEM_CAS_L	MEM_ADDR:G:L:S:0 MIL:280 MIL	3 L:S::600 MIL					
12C6<> 12B3<> MEM_WE_L	RAM_ADDR:G:L:S:0 MIL:2000 MIL	4 L:S::4000 MIL	200				
15B6<> 14B4<> 12A2<> RAM_RAS_L	RAM_ADDR:G:L:S:0 MIL:2000 MIL	4 L:S::4000 MIL	200				
15B4<> 14B4<> 12A2<> RAM_RAS_L	RAM_ADDR:G:L:S:0 MIL:2000 MIL	4 L:S::4000 MIL	200				
15B6<> 14B6<> 12B3<> RAM_WE_L	MEM_ADDR:G:L:S:0 MIL:2000 MIL	4 L:S::4000 MIL	200				
12C6<> 12C2<> 12B6<> 12B2<> MEM_CKE<0..3>	MEM_ADDR:G:L:S:0:200	3 L:S::600	10 MIL SPACING				
15C1<> 14B6<> 14B4<> 12C1<> 12B1<> RAM_CKE<0..1>	RAM_CS_GROUP0:G:L:S:0:400	3 L:S::2500	10 MIL SPACING				
15C6<> 15C4<> 15B1<> 15A1<> 12C1<> 12B1<> RAM_CKE<2..3>	RAM_CS_GROUP1:G:L:S:0:350	2 L:S::2500	10 MIL SPACING				
12B6<> MEM_MUXSEL_H<0..1>		3 L:S::1000					
12B6<> MEM_MUXSEL_L<0..1>		3 L:S::1000					167 MHZ
13C4<> 13A3<> 12D4<> MUX_SEL_H		4 L:S::2000 MIL	200				167 MHZ
13C8<> 13A6<> 12D4<> MUX_SEL_L		4 L:S::2000 MIL	200				167 MHZ
12B6<> SYSCLK_DDRCLK_A0_UF		B:S:500 MIL:850 MIL	8 MIL SPACING	270			167 MHZ
14D6<> 12C4<> SYSCLK_DDRCLK_A0_L	SYSCLK_DDRCLKA0:G:L:S:0 MIL:100 MIL	3 L:S::2600 MIL	200	8 MIL SPACING			167 MHZ
12B6<> SYSCLK_DDRCLK_A1_UF		B:S:500 MIL:850 MIL	8 MIL SPACING	270			167 MHZ
14A4<> 12C4<> SYSCLK_DDRCLK_A1_L	SYSCLK_DDRCLKA1:G:L:S:0 MIL:100 MIL	3 L:S::2600 MIL	200	8 MIL SPACING			167 MHZ
12B6<> SYSCLK_DDRCLK_A2_UF		B:S:500 MIL:850 MIL	8 MIL SPACING	270			167 MHZ
12B6<> SYSCLK_DDRCLK_B0_UF		B:S:500 MIL:850 MIL	8 MIL SPACING	270			167 MHZ
15B4<> 12B4<> SYSCLK_DDRCLK_B0_L	SYSCLK_DDRCLKB0:G:L:S:0 MIL:100 MIL	3 L:S::3500 MIL	200	8 MIL SPACING			167 MHZ
12B6<> SYSCLK_DDRCLK_B1_UF		B:S:500 MIL:850 MIL	8 MIL SPACING	270			167 MHZ
15D6<> 12A4<> SYSCLK_DDRCLK_B1_L	SYSCLK_DDRCLKB1:G:L:S:0 MIL:100 MIL	3 L:S::3500 MIL	200	8 MIL SPACING			167 MHZ
12B6<> SYSCLK_DDRCLK_B2_UF		B:S:500 MIL:850 MIL	8 MIL SPACING	270			167 MHZ
15A6<> 12A4<> SYSCLK_DDRCLK_B2_L	SYSCLK_DDRCLKB2:G:L:S:0 MIL:100 MIL	3 L:S::3500 MIL	200	8 MIL SPACING			167 MHZ
28A6< INT_REF_CLK_IN_PD		8 L:S::2500 MIL	10 MIL SPACING	270			66.56 MHZ
31C6< 31B7< 31B6< 30D4<> 30C4<> 30C2< 30C1<> 30B2< PCI_AD<31..0>	MIN_DAISSY_CHAIN	6 L:S:6000:8000	500				33 MHZ
59C3< 59B3< 32B6<> 32B4<> 32B2<> 32B0<> 31B7<> 31B5<> 31B3<> 31B1<> 30A5<> 30A3<> 30A1<> 30A0<> PCI_CBE<3..0>	MIN_DAISSY_CHAIN	6 L:S:6000:8000	500				33 MHZ
59A6< 32B6<> 31B7< 30C5<> 30B7< PCI_FRAME_L	MIN_DAISSY_CHAIN	L:S:6000 MIL:8000 MIL	500				33 MHZ

DIGITAL SIGNAL CONSTRAINTS

SIGNAL CONSTRAINTS

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SCALE: NONE SHEET: 53 OF 69

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SIG_NAME	RATSNEST_SCHEDULE	RELATIVE_PROPAGATION_DELAY	MAX_VIAS	PROPAGATION_DELAY	STUB_LENGTH	NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	DIFFERENTIAL_PAIR	FUNC_TEST	PULSE_PARAM
32B6<> 31B7< 30C5<> 30B7< PCI_IRDY_L	MIN DAISY_CHAIN	16	S:6000	MIL:8000	M200					33 MHZ
59A6> 32B6<> 31B7< 30C5<> 30B7< PCI_TRDY_L	MIN DAISY_CHAIN	16	S:6000	MIL:8000	M200					33 MHZ
59A6> 32B6<> 31B7< 30C5<> 30B7< PCI_DEVSEL_L	MIN DAISY_CHAIN	16	S:6000	MIL:8000	M200					33 MHZ
59A6> 32B6<> 31B7< 30C5<> 30B7< PCI_STOP_L	MIN DAISY_CHAIN	16	S:6000	MIL:8000	M200					33 MHZ
59A6> 32B6<> 31B7< 30C5<> PCI_PAR	MIN DAISY_CHAIN	16	S:6000	MIL:8000	M200					33 MHZ
30D5<> CLK33M_PCI_SLOTB_UF			B:S:600	MIL:1000	M200		450			33 MHZ
30D5<> CLK33M_PCI_SLOTC_UF			B:S:600	MIL:1200	M200		450			33 MHZ
30D5<> CLK33M_PCI_SLOTD_UF			B:S:600	MIL:1000	M200		450			33 MHZ
59A6> 31C2<> 30D7< CLK33M_PCI_SLOTB			I:S:3000	MIL:4000	M200	10 MIL SPACING	450			33 MHZ
30C5<> INT_PCI_FB_OUT			4	L:S::1000	MIL 200		450			33 MHZ
30D8< PCI_FBO_PLUS2			4	L:S::200	MIL 200		450			33 MHZ
30C8< PCI_FB_PLUS4			14	S:1900	MIL:2000	MIL	450			33 MHZ
30C8< PCI_FBI_PLUS2			14	S:1900	MIL:2000	M200	450			33 MHZ
30C7< PCI_FBI_EQUAL			14	S:2000	MIL:3000	M200	450			33 MHZ
30C7< PCI_FB_PLUS6			14	S:5900	MIL:6000	MIL	450			33 MHZ
30C5< INT_PCI_FB_IN			4	L:S::1080	MIL 200		450			33 MHZ
31C6< 31C3<> 31C2<> 31B7< 31B6< 31B3<> 31B2<> PCIT_AD<31..0>			3	L:S::1000						33 MHZ
59A6> 31C3<> 31B6< 31B2<> PCIT_CBE<31..0>			3	L:S::1000						33 MHZ
31C2<> 31B6< PCIT_FRAME_L			3	L:S::1000	MIL					33 MHZ
59B6> 31C3<> 31B6< PCIT_IRDY_L			3	L:S::1000	MIL					33 MHZ
31C2<> 31B6< PCIT_TRDY_L			3	L:S::1000	MIL					33 MHZ
31C2<> 31B6< PCIT_DEVSEL_L			3	L:S::1000	MIL					33 MHZ
31C2<> 31B6< PCIT_STOP_L			3	L:S::1000	MIL					33 MHZ
31C2<> 31B6< PCIT_PAR			3	L:S::1000	MIL					33 MHZ
17D8< 17C8< 16C4<> AGP_AD<0..15>	AGP_GROUP0:G:L:S:0:280	5	L:S:4500							266 MHZ
17C8< 16B4<> AGP_CBE<0..1>	AGP_GROUP0:G:L:S:0:330	5	L:S:4500							266 MHZ
17B8< 16B3< 16A4<> AGP_AD_STB<0>	AGP_GROUP0:G:L:S:0 MIL:330	MIL	L:S:4400	MIL 200	8 MIL SPACING	500	AGP_ADSTBDP0			133 MHZ
17B8< 16D1< 16A4<> AGP_AD_STB_L<0>	AGP_GROUP0:G:L:S:0 MIL:330	MIL	L:S:4400	MIL 200	8 MIL SPACING	500	AGP_ADSTBDP0			133 MHZ
17C8< 16C4<> 16B4<> AGP_AD<16..31>	AGP_GROUP0:G:L:S:0:280	5	L:S:4500							266 MHZ
17C8< 16B4<> AGP_CBE<2..3>	AGP_GROUP0:G:L:S:0:280	5	L:S:4500							266 MHZ
17B8< 16B3< 16A4<> AGP_AD_STB<1>	AGP_GROUP0:G:L:S:0 MIL:280	MIL	L:S:4400	MIL 200	8 MIL SPACING	500	AGP_ADSTBDP1			133 MHZ
17B8< 16D1< 16A4<> AGP_AD_STB_L<1>	AGP_GROUP0:G:L:S:0 MIL:330	MIL	L:S:4400	MIL 200	8 MIL SPACING	500	AGP_ADSTBDP1			133 MHZ
17B8< 16C3< 16B4<> AGP_FRAME_L			I:S:4000	MIL:4500	MIL					66 MHZ
17B8< 16C3< 16B4<> AGP_IRDY_L			I:S:4000	MIL:4500	MIL					66 MHZ
17B8< 16B4<> 16B3< AGP_TRDY_L			I:S:4000	MIL:4500	MIL					66 MHZ
17B8< 16C3< 16B4<> AGP_DEVSEL_L			I:S:4000	MIL:4500	MIL					66 MHZ
17B8< 16B4<> 16B3< AGP_STOP_L			I:S:4000	MIL:4500	MIL					66 MHZ
17B8< 16B4<> AGP_PAR			I:S:4000	MIL:4500	MIL					66 MHZ
17A8< 16C1< 16B4<> 16B4<> 16B1< 16A4<> AGP_SBA<0..7>			5	L:S:4000:4500						AGP_SBSTBB
17B8< 16B3< 16A4<> AGP_SB_STB	AGP_GROUP99:G:L:S:0 MIL:200	MIL	L:S:4500	MIL						AGP_SBSTBB
17A8< 16D1< 16A4<> AGP_SB_STB_L	AGP_GROUP99:G:L:S:0 MIL:200	MIL	L:S:4500	MIL						AGP_SBSTBB
17B6< 16B1< 16A4<> AGP_ST<0..2>			5	L:S:4500:5000						
17B8< 16B3< 16A4<> AGP_PIPE_L			I:S:4000	MIL:4500	MIL					
17B8< 16B3< 16A4<> AGP_RBF_L			I:S:4000	MIL:4500	MIL					
17B7<> 16C4<> 16C3< AGP_REQ_L			I:S:4500	MIL:5000	MIL					
17B7< 16C4<> 16C3< AGP_GNT_L			I:S:4500	MIL:5000	MIL					
17B8< 16B1< 16A6<> AGP_WBF_L			I:S:4000	MIL:4500	MIL					
17A8> 16D3< 16D1< 16C6<> AGP_BUSY_L			I:S:4500	MIL:5000	MIL					
17A8< 16D3< 16C6<> STOP_AGP_L			I:S:4500	MIL:5000	MIL					
17D7<> 17D6<> 17C6<> GPU_AGP_AD<0..15>	GPU_AGP_GROUP0:G:L:S:0:1003		L:S:600							266 MHZ
17C6<> GPU_AGP_CBE<0..1>	GPU_AGP_GROUP0:G:L:S:0:1003		L:S:600							266 MHZ
AGP_AD_STB_GPU<0>	GPU_AGP_STB0:G:L:S:0 MIL:50	MIL	L:S:800	MIL	8 MIL SPACING	500	GPU_ADSTBDP0			133 MHZ
AGP_AD_STB_L_GPU<0>	GPU_AGP_STB0:G:L:S:0 MIL:50	MIL	L:S:800	MIL	8 MIL SPACING	500	GPU_ADSTBDP0			133 MHZ
17C6<> GPU_AGP_AD<16..31>	GPU_AGP_GROUP1:G:L:S:0:1003		L:S:600							266 MHZ
17C6<> GPU_AGP_CBE<2..3>	GPU_AGP_GROUP1:G:L:S:0:1003		L:S:600							266 MHZ
AGP_AD_STB_GPU<1>	GPU_AGP_STB1:G:L:S:0 MIL:50	MIL	L:S:800	MIL	8 MIL SPACING	500	GPU_ADSTBDP1			133 MHZ
AGP_AD_STB_L_GPU<1>	GPU_AGP_STB1:G:L:S:0 MIL:50	MIL	L:S:800	MIL	8 MIL SPACING	500	GPU_ADSTBDP1			133 MHZ
17B6<> GPU_AGP_FRAME_L			I:S:300	MIL:600	MIL					66 MHZ
17B6<> GPU_AGP_IRDY_L			I:S:300	MIL:600	MIL					66 MHZ
17B6<> GPU_AGP_TRDY_L			I:S:300	MIL:600	MIL					66 MHZ
17B6<> GPU_AGP_DEVSEL_L			I:S:300	MIL:600	MIL					66 MHZ
17B6<> GPU_AGP_STOP_L			I:S:300	MIL:600	MIL					66 MHZ
17B6<> GPU_AGP_PAR			I:S:300	MIL:600	MIL					66 MHZ
17A6<> GPU_AGP_SBA<0..7>			3	L:S:300:600						
17B6<> GPU_AGP_SB_STB	GPU_AGP_SBSTB:G:L:S:0 MIL:150	MIL	I:S:300	MIL:600	MIL					GPU_SBSTBB
17A6<> GPU_AGP_SB_STB_L	GPU_AGP_SBSTB:G:L:S:0 MIL:150	MIL	I:S:300	MIL:600	MIL					GPU_SBSTBB
17B6<> GPU_AGP_PIPE_L			I:S:300	MIL:600	MIL					
17B6<> GPU_AGP_RBF_L			I:S:300	MIL:600	MIL					
16C6<> CLK66M_GPU_UF			I:S:1000	MIL:1100	MIL	10 MIL SPACING	250			66 MHZ
17C7< 16D8< CLK66M_GPU_AGP			I:S:3700	MIL:3900	M200	10 MIL SPACING	250			66 MHZ
16C6<> INT_AGP_FB_OUT			I:S:1400	MIL:1500	M200		250			66 MHZ
16B7< AGP_FBO_EQUAL			B:S:900	MIL:1080	M200		250			66 MHZ
16B8< AGP_FB_PLUS2			I:S:1900	MIL:2000	MIL		250			66 MHZ
16C7< AGP_FBI_EQUAL			4	L:S::200	MIL 200		250			66 MHZ
16C6< INT_AGP_FB_IN			4	L:S::1200	MIL 200		250			66 MHZ
30C5<> 30A7< 16D7< INT_ROM_OVERLAY_PU			I:S:600	MIL:800	MIL	10 MIL SPACING	250			66 MHZ
59A8> 56B3> 16C7< 9B4< 8A2< INT_ANALYZER_CLK			3	L:S::2800	MIL	8 MIL SPACING	250			166 MHZ
32A6< 30D7< CLK33M_PCI_SLOTD			I:S:3000	MIL:3500	M200	8 MIL SPACING	250			33 MHZ

SIGNAL CONSTRAINTS

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	D	051-6497	13
SCALE	SHT	OF	
NONE	54	69	

DIGITAL SIGNALS

GROUP	SIG_NAME	RELATIVE_PROPAGATION_DELAY	MAX VIAS	PROPAGATION_DELAY	STUB_LENGTH	NET_SPACING_TYPE	MAX EXPOSED LENGTH	PULSE PARAM	
	FBD<0..63>	GPU_FBDDATA_A:G:L:S:0:225		L:S:800				300 MHZ	18E8<> 18F8<> 18G8<> 19C5< 19C8< 19D5< 19D8<
	RFBD<0..63>	RAM_FBDDATA_A:G:L:S:0:300		L:S:1000				300 MHZ	19C4< 19C7< 19D4< 19D7< 20B1<> 20B5<> 20C1<> 20C5<>
	FBDQM<0..7>	GPU_FBDQM_A:G:L:S:0:200		L:S:800				300 MHZ	18D8< 18G3<
	RFBDQM<0..7>	RAM_FBDQM_A:G:L:S:0:200		L:S:1000				300 MHZ	18G2< 20C2< 20C6<
	FBA<0..12>	GPU_FBADDR_A:G:L:S:0:200		L:S:700				300 MHZ	18C8< 18D8< 18E3< 18F3<
	RFBA<0..12>	RAM_FBADDR_A:G:L:S:0:330		L:S:2400	2350			300 MHZ	18E2<> 18F2<> 20C2< 20C6< 20D2< 20D6<
	FBABA<0..1>	GPU_FBADDR_A:G:L:S:0:200		L:S:600				300 MHZ	18C8<> 18E3<
	RFBABA<0..1>	RAM_FBADDR_A:G:L:S:0:330		L:S:2400	50			300 MHZ	18E2<> 20C2< 20C6<
	FBARAS_L	GPU_FBCNTL_A:G:L:S:0 MIL:200 MTS:400 MIL						300 MHZ	18C8< 18G3<
	FBACAS_L	GPU_FBCNTL_A:G:L:S:0 MIL:200 MTS:400 MIL						300 MHZ	18C8< 18G3<
	FBARE_L	GPU_FBCNTL_A:G:L:S:0 MIL:200 MTS:400 MIL						300 MHZ	18C8< 18F3<
	FBACSO_L	GPU_FBCNTL_A:G:L:S:0 MIL:200 MTS:400 MIL						300 MHZ	18C8< 18F3<
	FBACKE	GPU_FBCNTL_A:G:L:S:0 MIL:200 MTS:400 MIL			100			300 MHZ	18D3< 18D7<>
	RFBARAS_L	RAM_FBCNTL_A:G:L:S:0 MIL:350 MTS:2700 MIL			50			300 MHZ	18G2<> 20B2< 20B6<
	RFBACAS_L	RAM_FBCNTL_A:G:L:S:0 MIL:350 MTS:2700 MIL			50			300 MHZ	18G2<> 20B2< 20B6<
	RFBAWE_L	RAM_FBCNTL_A:G:L:S:0 MIL:500 MTS:2700 MIL			50			300 MHZ	18F2<> 20B2< 20B6<
	RFBACSO_L	RAM_FBCNTL_A:G:L:S:0 MIL:350 MTS:2700 MIL			50			300 MHZ	18F2<> 20B2< 20B6<
	RFBACKE	RAM_FBCNTL_A:G:L:S:0 MIL:500 MTS:2700 MIL			50			300 MHZ	18D2<> 20C2< 20C6<
	FBDQS<0..7>	GPU_FBDQS_A:G:L:S:0:100		L:S:350				300 MHZ	18C7< 19A8<
	FBDOSTERM<0..7>	FB_DQSTERM_A:G:L:S:0:50		L:S:1500		10 MIL SPACING		300 MHZ	19A7<
	RFBDQS<0..7>	RAM_FBDQS_A:G:L:S:0:55		L:S:150		10 MIL SPACING		300 MHZ	19A6< 20C2<> 20C6<>
	FBCLK0	GPU_FBCLK_A:G:L:S:0 MIL:50 MIL:S:150 MIL					200	300 MHZ	18D7< 19C3<
	FBCLK0_L	GPU_FBCLK_A:G:L:S:0 MIL:50 MIL:S:150 MIL					200	300 MHZ	18D7< 19C3<
	FBCLK1	GPU_FBCLK_A:G:L:S:0 MIL:50 MIL:S:150 MIL					200	300 MHZ	18D7< 19D3<
	FBCLK1_L	GPU_FBCLK_A:G:L:S:0 MIL:50 MIL:S:150 MIL					200	300 MHZ	18D7< 19D3<
	RFBCLK1	RAM_FBCLK_A:G:L:S:0 MIL:80 MIL:S:2500 MIL					200	300 MHZ	19D1< 20C2<
	RFBCLK1_L	RAM_FBCLK_A:G:L:S:0 MIL:80 MIL:S:2500 MIL					200	300 MHZ	19D1< 20C2<
	RFBCLK0	RAM_FBCLK_A:G:L:S:0 MIL:70 MIL:S:2500 MIL					200	300 MHZ	19C1< 20C6<
	RFBCLK0_L	RAM_FBCLK_A:G:L:S:0 MIL:70 MIL:S:2500 MIL					200	300 MHZ	19C1< 20C6<
	FBD<64..127>	GPU_FBDDATA_B:G:L:S:0:225		L:S:800				300 MHZ	18E5<> 18F5<> 18G5<> 19B5< 19B8< 19C5< 19C8<
	RFBD<64..127>	RAM_FBDDATA_B:G:L:S:0:325		L:S:1000				300 MHZ	19B4< 19B7< 19C4< 19C7< 21B1<> 21B5<> 21C1<> 21C5<>
	FBDQM<8..15>	GPU_FBDQM_B:G:L:S:0:120		L:S:800				300 MHZ	18C3< 18D3< 18D5<
	RFBDQM<8..15>	RAM_FBDQM_B:G:L:S:0:120		L:S:1000				300 MHZ	18C2< 18D2< 21C2< 21C6<
	FBBAA<0..12>	GPU_FBADDR_B:G:L:S:0:220		L:S:600				300 MHZ	18A3< 18B3< 18C3< 18C5<> 18D5<>
	RFBBAA<0..12>	RAM_FBADDR_B:G:L:S:0:370		L:S:2400	50			300 MHZ	18B2<> 18C2<> 21C2< 21C6< 21D2< 21D6<
	FBBBA<0..1>	GPU_FBADDR_B:G:L:S:0:220		L:S:600				300 MHZ	18A3< 18C5<>
	RFBBBA<0..1>	RAM_FBADDR_B:G:L:S:0:370		L:S:2400	50			300 MHZ	18A2<> 21C2< 21C6<
	FBBRAS_L	GPU_FBCNTL_B:G:L:S:0 MIL:120 MTS:400 MIL						300 MHZ	18C3< 18D4<>
	FBBCAS_L	GPU_FBCNTL_B:G:L:S:0 MIL:120 MTS:400 MIL						300 MHZ	18C3< 18D4<>
	FBBWE_L	GPU_FBCNTL_B:G:L:S:0 MIL:120 MTS:400 MIL						300 MHZ	18C3< 18D4<>
	FBBCSO_L	GPU_FBCNTL_B:G:L:S:0 MIL:120 MTS:400 MIL						300 MHZ	18C3< 18C4<>
	FBBCKE	GPU_FBCNTL_B:G:L:S:0 MIL:120 MTS:400 MIL			100			300 MHZ	18A3< 18C4<>
	RFBBRAS_L	RAM_FBCNTL_B:G:L:S:0 MIL:2000 MTS:3500 MIL			3550			300 MHZ	18C2<> 21B2< 21B6<
	RFBBCAS_L	RAM_FBCNTL_B:G:L:S:0 MIL:2000 MTS:3500 MIL			3550			300 MHZ	18C2<> 21B2< 21B6<
	RFBBWE_L	RAM_FBCNTL_B:G:L:S:0 MIL:2000 MTS:3500 MIL			3550			300 MHZ	18C2<> 21B2< 21B6<
	RFBBCSO_L	RAM_FBCNTL_B:G:L:S:0 MIL:2000 MTS:3500 MIL			3550			300 MHZ	18C2<> 21B2< 21B6<
	RFBBCKE	RAM_FBCNTL_B:G:L:S:0 MIL:2000 MTS:3500 MIL			3550			300 MHZ	18A2<> 21C2< 21C6<
	FBDQS<8..15>	GPU_FBDQS_B:G:L:S:0:190		L:S:350				300 MHZ	18D4<> 19A5<
	FBDOSTERM<8..15>	FB_FBDQSTERM_B:G:L:S:0:60		L:S:1500		10 MIL SPACING		300 MHZ	19A4<
	RFBDQS<8..15>	RAM_FBDQS_B:G:L:S:0:59		L:S:150		10 MIL SPACING		300 MHZ	19A3< 21C2<> 21C6<>
	FBBCLK0	GPU_FBCLK_B:G:L:S:0 MIL:50 MIL L:S:150 MIL					200	300 MHZ	18C5<> 19B3<
	FBBCLK0_L	GPU_FBCLK_B:G:L:S:0 MIL:50 MIL L:S:150 MIL					200	300 MHZ	18C5<> 19B3<
	FBBCLK1	GPU_FBCLK_B:G:L:S:0 MIL:50 MIL L:S:150 MIL					200	300 MHZ	18C5<> 19C3<
	FBBCLK1_L	GPU_FBCLK_B:G:L:S:0 MIL:50 MIL L:S:150 MIL					200	300 MHZ	18C5<> 19B3<
	RFBBCLK1	RAM_FBCLK_B:G:L:S:0 MIL:90 MIL L:S:2500 MIL					200	300 MHZ	19C1< 21C2<
	RFBBCLK1_L	RAM_FBCLK_B:G:L:S:0 MIL:90 MIL L:S:2500 MIL					200	300 MHZ	19B1< 21C2<
	RFBBCLK0	RAM_FBCLK_B:G:L:S:0 MIL:90 MIL L:S:2500 MIL					200	300 MHZ	19B1< 21C6<
	RFBBCLK0_L	RAM_FBCLK_B:G:L:S:0 MIL:90 MIL L:S:2500 MIL					200	300 MHZ	19B1< 21C6<

SIGNAL CONSTRAINTS

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6497	13
SCALE	SHEET		OF
NONE	55		69

DIGITAL SIGNALS

GROUP	SIG_NAME	RELATIVE_PROPAGATION_DELAY	MIN_LENGTH	PROPAGATION_DELAY	MIN_LENGTH	NET_SPACING_TYPE	NO_TEST	PULSE_PARAM	MAX_EXPOSED_LENGTH	
MAXBUS	CPU_ADDR<0..31>	CPU_ADDR_GROUP:G:L:S:0	1550	250				166 MHZ		4B7<> 4C7<> 8B4<> 8B5<> 8B7<> 8B8<> 8C4<> 8C5<> 8C7<> 8C8<>
	CPU_DATA<0..63>	CPU_DATA_GROUP:G:L:S:0	1550	250				166 MHZ		8C3<> 9D3<> 9D5<> 9D8<>
	CPU BR L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		5B4<> 5B4<> 5D4<> 5D4<> 6C4<> 8C4<> 8C4<> 8C5<> 8C7<> 8C7<> 8C8<>
	CPU BG L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		8D4<> 8D5<> 8D7<> 8D8<> 9A7<> 9B1<> 9B7<> 9C1<> 9C5<> 9C8<> 9D1<>
	CPU TS L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4D7<> 7C7<> 8B4<> 9D3<>
	CPU TT<0..4>	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4D7<> 7C7<> 8B7<> 9D3<>
	CPU TBST L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4B7<> 7A7<> 8B4<> 8B5<> 9B3<>
	CPU TSIZ<0..2>	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4B7<> 7B7<> 8B4<> 9B3<>
	CPU ARTRY L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4B7<> 8B5<> 8B7<> 9B3<>
	CPU AACK L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4A7<> 7C7<> 8B8<> 9B3<>
	CPU GBL L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4A7<> 7B7<> 8B5<> 9B3<>
	CPU INT_GBL L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4B8<> 8B5<>
	CPU CI L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4B8<> 7B7<> 9C3<>
	CPU HIT L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4A7<> 7A7<> 8C5<> 9C3<>
	CPU DBG L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4A7<> 7C7<> 8B8<> 9B3<>
	CPU DRDY L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4C3<> 7B7<> 8B8<> 9B1<>
	CPU WT L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4C2<> 7B7<> 8B5<> 9B1<>
	CPU DRDY L UP	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4B7<> 7A7<> 8B5<> 9B3<>
	CPU DTI<0..2>	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4C3<>
	CPU TA L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4C3<> 8B4<> 8B7<> 9A1<>
	CPU TEA L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4C3<> 7C7<> 8C4<> 9A1<>
	CPU QREQ L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4C3<> 7B7<> 8B5<> 9A1<>
	CPU QACK L	CPU_CNTL_GROUP:G:L:S:0	1550	250	10 MIL SPACING			166 MHZ		4C3<> 7D5<> 8B7<> 9B3<>
	SYSCLK_CPU UP		2	150	10 MIL SPACING			166 MHZ	315	4C3<> 8B4<> 9B3<>
	SYSCLK_CPU		4	2200	200	10 MIL SPACING		166 MHZ	315	9A3<>
	INT_CPU_FB_OUT		3	1000	200			166 MHZ	315	4D2<> 9A4<>
	CPU_FBO_PLUS1		3	200	200			166 MHZ	315	9B3<>
	CPU_FBI_PLUS1		3	1400	1500	200		166 MHZ	315	9A5<>
	CPU_FB_MINUS3		4	900	1000	200		166 MHZ	315	9A5<>
	INT_CPU_FB_IN		4	1000	200			166 MHZ	315	9A4<>
	CPU_FB_PLUS2		3	900	1000			166 MHZ	315	9B3<>
	CPU_FB_PLUS3		3	2900	3000			166 MHZ	315	9A5<>
	INT_ANALYZER_CLK		3	300				166 MHZ		9A4<>
	SYSCLK_LA		2	2000				166 MHZ		8A2<> 9B4<> 16C7<> 54A7<> 59A8<>
	INT_CLOCK_OUT		3	3000				166 MHZ		8A2<> 8D8<>
	USB2_XT1		3	1000	100	10 MIL SPACING		30 MHZ		MIN LINE WIDTH DIFFERENTIAL PAIR
	USB2_XT2_B		3	1000	100	10 MIL SPACING		30 MHZ		32C4<>
	USB2_XT2		3	100	100	10 MIL SPACING		30 MHZ		32C4<>
	USB2_RREF		2	100						32B4<>
	USB2_RSDAM	USB2_RSDA:G:L:S:0	2	100	8 MIL SPACING	3.5	480 MHZ			32C4<>
	USB2_RSDAP	USB2_RSDA:G:L:S:0	2	100	8 MIL SPACING	3.5	480 MHZ			32C4<>
	USB2_RSDBM	USB2_RSDB:G:L:S:0	2	100	8 MIL SPACING	3.5	480 MHZ			32C4<>
	USB2_RSDBP	USB2_RSDB:G:L:S:0	2	100	8 MIL SPACING	3.5	480 MHZ			32C4<>
	USB2_RSDCM	USB2_RSDB:G:L:S:0	2	100	8 MIL SPACING	3.5	480 MHZ			32C4<>
	USB2_RSDCP	USB2_RSDB:G:L:S:0	2	100	8 MIL SPACING	3.5	480 MHZ			32C4<>
	USB2_DAN_F	USB2_DMA:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			32C4<> RATSNEST_SCHEDULE
	USB2_DAP_F	USB2_DMA:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_DMA_DP MIN DAISY_CHAIN 32C1<> 33B7<>
	USB2_DBN_F	USB2_DMB:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_DMA_DP MIN DAISY_CHAIN 32C1<> 33B7<>
	USB2_DBP_F	USB2_DMB:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_DMB_DP MIN DAISY_CHAIN 32C1<> 33C7<>
	USB2_DCN_F	USB2_DMC:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_DMB_DP MIN DAISY_CHAIN 32C1<> 33C7<>
	USB2_DCP_F	USB2_DMC:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_DMC_DP MIN DAISY_CHAIN 32C1<> 33D7<>
	USBT_DAN_F	USB2_DMAT:G:L:S:0	2000	2000	8 MIL SPACING	3.5	480 MHZ			USB2_DMC_DP MIN DAISY_CHAIN 32C1<> 33D7<>
	USBT_DAP_F	USB2_DMAT:G:L:S:0	2000	2000	8 MIL SPACING	3.5	480 MHZ			USB2_DMAT_DP MIN DAISY_CHAIN 33B6<>
	USBT_DBN_F	USB2_DMBT:G:L:S:0	2000	2000	8 MIL SPACING	3.5	480 MHZ			USB2_DMAT_DP MIN DAISY_CHAIN 33B6<>
	USBT_DBP_F	USB2_DMBT:G:L:S:0	2000	2000	8 MIL SPACING	3.5	480 MHZ			USB2_DMBT_DP MIN DAISY_CHAIN 33C6<>
	USBT_DCN_F	USB2_DMCT:G:L:S:0	2000	2000	8 MIL SPACING	3.5	480 MHZ			USB2_DMBT_DP MIN DAISY_CHAIN 33D6<>
	USBT_DCP_F	USB2_DMCT:G:L:S:0	2000	2000	8 MIL SPACING	3.5	480 MHZ			USB2_DMCT_DP MIN DAISY_CHAIN 33D6<>
	USB_DAN_CON	USB2_CONA:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_DAN_DP MIN DAISY_CHAIN 33C3<> 59C6>
	USB_DAP_CON	USB2_CONA:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_DAP_DP MIN DAISY_CHAIN 33C3<> 59C6>
	USB_DBN_CON	USB2_CONB:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_CONB_DP MIN DAISY_CHAIN 33B3<> 59C6>
	USB_DBP_CON	USB2_CONB:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_CONB_DP MIN DAISY_CHAIN 33B3<> 59C6>
	USB_DCN_CON	USB2_CONC:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_CONC_DP MIN DAISY_CHAIN 33D3<> 59C6>
	USB_DCP_CON	USB2_CONC:G:L:S:0	50	50	8 MIL SPACING	3.5	480 MHZ			USB2_CONC_DP MIN DAISY_CHAIN 33D3<> 59C6>

SIGNAL CONSTRAINTS

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D	051-6497	13
SCALE	SHT	OF
NONE	56	69



APPLE COMPUTER INC.

DIGITAL SIGNALS

DIGITAL SIGNALS

GROUP	SIG_NAME	RELATIVE_PROPAGATION_DELAY	MAX_VIAS	STUB_LENGTH	NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	PULSE_PARAM
NEW	VSYNC*	3		L:S::1000 MIL			22C5<>
STUFF	ANALOG VSYNC*	4		L:S::3500 MIL			22D7< 25C6> 26B5< 59B8>
HERE	HSYNC*	3		L:S::1000 MIL			22C5<>
	ANALOG HSYNC*	5		L:S::3500 MIL			22D7< 25D6> 26B5< 59B8>
	ANALOG BLU	4		L:S::4000 MIL	10 MIL SPACING	5.8	22C7<> 25C6<>
	ANALOG GRN	4		L:S::4000 MIL	10 MIL SPACING	5.8	22C7<> 25C6<>
	ANALOG RED	4		L:S::4000 MIL	10 MIL SPACING	5.8	22C7<> 25C6<>
	FILT ANALOG RED	2		L:S::500 MIL	10 MIL SPACING	5.8	25C5< 59B8>
	FILT ANALOG GRN	2		L:S::500 MIL	10 MIL SPACING	5.8	25C5< 59B8>
	FILT ANALOG BLU	2		L:S::500 MIL	10 MIL SPACING	5.8	25C5< 59B8>
	DAC2RSET			L:S::1000 MIL	10 MIL SPACING		22C5<>
	DAC2VREF			L:S::1000 MIL	10 MIL SPACING		22C5<>
	NV11 XTALIN	4		L:S::1000 MIL	8 MIL SPACING		22B4<>
	NV11 XTALOUT	4		L:S::1000 MIL	8 MIL SPACING		22B4<>
	TCKP	TMDSFILT:G:L:S:02MIL:110 MIL		110	8 MIL SPACING	TMDSFILT_CLK	24C4<>
	TCKM	TMDSFILT:G:L:S:02MIL:110 MIL		110	8 MIL SPACING	TMDSFILT_CLK	24C3<>
	TD0P	TMDSFILT:G:L:S:02MIL:110 MIL		110	8 MIL SPACING	TMDSFILT_D0	24C3<>
	TD0M	TMDSFILT:G:L:S:02MIL:110 MIL		110	8 MIL SPACING	TMDSFILT_D0	24C4<>
	TD1P	TMDSFILT:G:L:S:02MIL:110 MIL		110	8 MIL SPACING	TMDSFILT_D1	24C4<>
	TD1M	TMDSFILT:G:L:S:02MIL:110 MIL		110	8 MIL SPACING	TMDSFILT_D1	24C3<>
	TD2P	TMDSFILT:G:L:S:02MIL:110 MIL		110	8 MIL SPACING	TMDSFILT_D2	24C3<>
	TD2M	TMDSFILT:G:L:S:02MIL:110 MIL		110	8 MIL SPACING	TMDSFILT_D2	24C4<>
	ENET LINK TX EN	4		L:S::1000 MIL		25 MHZ	34D6<>
	ENET LINK TX ER	4		L:S::1000 MIL		25 MHZ	34D6<>
	ENET LINK TXD<0..3>	4		L:S::1000 MIL		25 MHZ	34C6<>
	ENET PHY TX EN	4		L:S::5600 MIL		25 MHZ	34D7< 35C6<
	ENET PHY TX ER	4		L:S::5600 MIL		25 MHZ	34D7< 35C6<
	ENET PHY TXD<0..3>	4		L:S:4600:5600		25 MHZ	34C7< 35C6<>
	CLKENET LINK TX	4		L:S:4600 MIL:5600 MIL		25 MHZ	34D7< 35C8<
	CLKENET PHY TX	4		L:S::1000 MIL		25 MHZ	35C6<>
	CLKENET LINK RX	4		L:S:4600 MIL:5600 MIL		25 MHZ	34C7< 35C8<
	CLKENET PHY RX	4		L:S::1000 MIL		25 MHZ	35C6<>
	ENET PHY RXD<0..3>	4		L:S::1000 MIL		25 MHZ	35B6<> 35C6<>
	ENET PHY RX DV	4		L:S::1000 MIL		25 MHZ	35B6<>
	ENET PHY RX ER	4		L:S::1000 MIL		25 MHZ	35B6<>
	ENET PHY CRS	4		L:S::1000 MIL		25 MHZ	35B6<>
	ENET PHY COL	4		L:S::1000 MIL		25 MHZ	35B6<>
	ENET LINK RXD<0..3>	4		L:S:4600:5600		25 MHZ	34C7< 35B8< 35C8<
	ENET CRS	4		L:S:4600 MIL:5600 MIL		25 MHZ	34C7< 35B8<
	ENET COL	4		L:S:4600 MIL:5600 MIL		25 MHZ	34B7< 35B8<
	ENET RX DV	4		L:S:4600 MIL:5600 MIL		25 MHZ	34C7< 35B8<
	ENET RX ER	4		L:S:4600 MIL:5600 MIL		25 MHZ	34C7< 35B8<
	CLK25M ENET XIN	3		L:S::1000 MIL	8 MIL SPACING	25 MHZ	35B6<
	CLK25M ENET XOUT	3		L:S::1000 MIL	8 MIL SPACING	25 MHZ	35B6<>
	ENET TDP	ETHTD:G:L:S:0 MBL:70 MIB::4000 MIL:50		10 MIL SPACING	ETH TXD	100 MHZ	35C3<>
	ENET TDN	ETHTD:G:L:S:0 MBL:70 MIB::4000 MIL:50		10 MIL SPACING	ETH TXD	100 MHZ	35C3<>
	ENET RDP	ETHRD:G:L:S:0 MBL:70 MIB::4000 MIL:50		10 MIL SPACING	ETH RXD	100 MHZ	35C3<>
	ENET RDN	ETHRD:G:L:S:0 MBL:70 MIB::4000 MIL:50		10 MIL SPACING	ETH RXD	100 MHZ	35C3<>
	RJ45 TXP	RJTXD:G:L:S:0 MBL:70 MIB::750 MIL		2KV ISO	RJ45_TXD	100 MHZ	35C1<>
	RJ45 TXN	RJTXD:G:L:S:0 MBL:70 MIB::750 MIL		2KV ISO	RJ45_TXD	100 MHZ	35C1<>
	RJ45 RXP	RJRXD:G:L:S:0 MBL:70 MIB::750 MIL		2KV ISO	RJ45_RXD	100 MHZ	35C1<>
	RJ45 RXN	RJRXD:G:L:S:0 MBL:70 MIB::750 MIL		2KV ISO	RJ45_RXD	100 MHZ	35C1<>
	RJ45 TREF			2KV ISO			35C2<>
	RJ45 RREF			2KV ISO			35C2<>
	RJ45 4 5			2KV ISO			35C1<>
	RJ45 7 8			2KV ISO			35C1<>
	RJ45 F TREF			2KV ISO			35B2<
	FW LINK DATA<0..7>	4		L:S::1000 MIL			34C4<>
	FW LINK CNTL<0..1>	4		L:S::1000 MIL			49.152 MHZ 34C4<>
	FW LINK LREQ	4		L:S::1000 MIL			49.152 MHZ 34C4<>
	FW SCLK	4		L:S:3500 MIL:4500 MIL			49.152 MHZ 34C5<> 36C8<
	FW D<0..7>	4		L:S:3700 MIL:4700 MIL			49.152 MHZ 34C3< 36C8<
	FW CNTL0	4		L:S:3700 MIL:4700 MIL			49.152 MHZ 34C3< 36C8<
	FW CNTL1	4		L:S:3700 MIL:4700 MIL			49.152 MHZ 34C3< 36C8<
	FW LREQ	4		L:S:3700 MIL:4700 MIL			49.152 MHZ 34C3< 36C8<
	FW PHY SCLK	4		L:S:500 MIL			49.152 MHZ 36C7<>
	FW PHY CNTL0	4		L:S:1000 MIL			49.152 MHZ 36C7<>
	FW PHY CNTL1	4		L:S:1000 MIL			49.152 MHZ 36C7<>
	FW PHY D<0..7>	4		L:S:1000 MIL			49.152 MHZ 36B7<> 36C7<>
	FW XT	3		L:S:1000 MIL	8 MIL SPACING		24.576 MHZ 36C6<
	FW XO	3		L:S:1000 MIL	8 MIL SPACING		24.576 MHZ 36C6<>
	FW BIAS1						36C5<>
	FW BIAS2						36C5<>
	FW TPA1P	FWTPA1:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPA1	400 MHZ	36C5<>
	FW TPA1N	FWTPA1:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPA1	400 MHZ	36C5<>
	FW TPB1P	FWTPB1:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPB1	400 MHZ	36C5<>
	FW TPB1N	FWTPB1:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPB1	400 MHZ	36C5<>
	FW TPA2P	FWTPA2:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPA2	400 MHZ	36C5<>
	FW TPA2N	FWTPA2:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPA2	400 MHZ	36C5<>
	FW TPB2P	FWTPB2:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPB2	400 MHZ	36C5<>
	FW TPB2N	FWTPB2:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPB2	400 MHZ	36C5<>
	FW TPO1P	FWTPOL:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPO1	400 MHZ	36A8<> 36D1<>
	FW TPO1N	FWTPOL:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPO1	400 MHZ	36A8<> 36D1<>
	FW TPL1P	FWTPLL:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPL1	400 MHZ	36A8<> 36D1<>
	FW TPL1N	FWTPLL:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPL1	400 MHZ	36A8<> 36D1<>
	FW TPO2P	FWTPO2:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPO2	400 MHZ	36A8<> 36C1<>
	FW TPO2N	FWTPO2:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPO2	400 MHZ	36A8<> 36C1<>
	FW TPL2P	FWTPLZ:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPL2	400 MHZ	36A8<> 36C1<>
	FW TPL2N	FWTPLZ:G:L:S:0 MIL:50.358H:1220 MFD00			FW TPL2	400 MHZ	36A8<> 36C1<>

GROUP	SIG_NAME	RELATIVE_PROPAGATION_DELAY	MAX_VIAS	STUB_LENGTH	NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	PULSE_PARAM
NEW	TMDS CKP	TMDS:G:L:S:0 MIL:120 MIL	3	70	8 MIL SPACING	TMDS_CLK	23D1< 24B7<> 27C2< 59A8>
STUFF	TMDS CKM	TMDS:G:L:S:0 MIL:120 MIL	3	70	8 MIL SPACING	TMDS_CLK	23D1< 24A7<> 27C2< 59A8>
HERE	TMDS D0P	TMDS:G:L:S:0 MIL:120 MIL	3	70	8 MIL SPACING	TMDS_D0	23D1< 24B7<> 27C2< 59A8>
	TMDS D0M	TMDS:G:L:S:0 MIL:120 MIL	3	70	8 MIL SPACING	TMDS_D0	23D1< 24B7<> 27C2< 59A8>
	TMDS D1P	TMDS:G:L:S:0 MIL:120 MIL	3	70	8 MIL SPACING	TMDS_D1	23D1< 24C7<> 27C2< 59B8>
	TMDS D1M	TMDS:G:L:S:0 MIL:120 MIL	3	70	8 MIL SPACING	TMDS_D1	23D1< 24C7<> 27C2< 59B8>
	TMDS D2P	TMDS:G:L:S:0 MIL:120 MIL	3	70	8 MIL SPACING	TMDS_D2	23D1< 24D7<> 27C2< 59B8>
	TMDS D2M	TMDS:G:L:S:0 MIL:120 MIL	3	70	8 MIL SPACING	TMDS_D2	23D1< 24D7<> 27C2< 59B8>
	GPU TMDS CKP	GTMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	G_TMDS_CLK	23C2< 23D3<>
	GPU TMDS CKM	GTMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	G_TMDS_CLK	23C2< 23D3<>
	GPU TMDS D0P	GTMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	G_TMDS_D0	23C2< 23D3<>
	GPU TMDS D0M	GTMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	G_TMDS_D0	23C2< 23D3<>
	GPU TMDS D1P	GTMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	G_TMDS_D1	23C2< 23D3<>
	GPU TMDS D1M	GTMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	G_TMDS_D1	23C2< 23D3<>
	GPU TMDS D2P	GTMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	G_TMDS_D2	23C2< 23D3<>
	GPU TMDS D2M	GTMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	G_TMDS_D2	23C2< 23D3<>
	SI TMDS CKP	STMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	S_TMDS_CLK	27C3<>
	SI TMDS CKM	STMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	S_TMDS_CLK	27C3<>
	SI TMDS D0P	STMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	S_TMDS_D0	27C3<>
	SI TMDS D0M	STMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	S_TMDS_D0	27C3<>
	SI TMDS D1P	STMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	S_TMDS_D1	27C3<>
	SI TMDS D1M	STMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	S_TMDS_D1	27C3<>
	SI TMDS D2P	STMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	S_TMDS_D2	27C3<>
	SI TMDS D2M	STMDS:G:L:S:0 MIL:120 MIL	3	50	8 MIL SPACING	S_TMDS_D2	27C3<>
	DVOD0	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22C5<> 27A8< 27C5<
	DVOD1	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22C5<> 27A8< 27C5<
	DVOD2	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22C5<> 26D1< 27C5<
	DVOD3	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 26D1< 27C5<
	DVOD4	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 27A8< 27C5<
	DVOD5	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 27A8< 27C5<
	DVOD6	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 27A8< 27C5<
	DVOD7	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 27A8< 27C5<
	DVOD8	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 26B1< 27C5<
	DVOD9	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 27A8< 27C5<
	DVOD10	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 27A8< 27B5<
	DVOD11	TMDS_XMIT:G:L:S:0 MIL:400 MIL		200			165MHZ 22B5<> 27A8< 27B5<

SIGNAL CONSTRAINTS

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APPLE COMPUTER INC.

SCALE: NONE

DRAWING NUMBER: D 051-6497

SHEET: 57 OF 69

REV: 13

DIGITAL SIGNALS (CONT'D)

GROUP	SIG_NAME	RELATIVE_PROPAGATION_DELAY	MAX_VIAS	PROPAGATION_DELAY	STUB_LENGTH	NET_SPACING_TYPE	MAX_EXPOSED_LENGTH	PULSE_PARAM	
CD DRIVE BUS	EIDE_RST_L			L:S:3500 MIL:5500 MIL				33 MHZ	37A7> 37D5<
	EIDE_DMACK_L			L:S:3500 MIL:5500 MIL				33 MHZ	37A7<> 37D5<
	EIDE_STOP			L:S:5500 MIL				33 MHZ	37A7> 37D5<
	EIDE_HSTB_RDY			L:S:5500 MIL				33 MHZ	37A7> 37C5<
	EIDE_DSTB_RDY			L:S:3500 MIL:5500 MIL				33 MHZ	37A7< 37C5<
	EIDE_DATA<0..15>			L:S:3500:5500				33 MHZ	37A5< 37B5< 37B7<> 37C5<
	CD_RESET_L			L:S:1000 MIL				33 MHZ	37D4< 38C6<>
	CD_DMACK_L			L:S:4000 MIL				33 MHZ	37D4< 38C6<>
	CD_STOP			L:S:5000 MIL				33 MHZ	37D4< 38C6<>
	CD_HSTB_RDY			L:S:5000 MIL				33 MHZ	37C4< 38C6<>
	CD_DSTB_RDY			L:S:1000 MIL				33 MHZ	37C4< 38C6<>
	UATAD<0..15>			L:S:1000				33 MHZ	37A4< 37B4< 37C4< 38C6<>
	CD_DMARQ			L:S:1000 MIL				33 MHZ	38C6<>
	EIDE_DMARQ			L:S:3500 MIL:5500 MIL				33 MHZ	37A7< 38C8<
	UATAOIRO			L:S:1000 MIL				33 MHZ	38C6<>
	EIDE_INTRO			L:S:3500 MIL:5500 MIL				33 MHZ	37A7< 38C8<
	CD_EIDE_ADDR<0..2>			L:S:1000				33 MHZ	38C6<>
	EIDE_ADDR<0..2>			L:S:3500:5500				33 MHZ	37B7> 38A8< 38B8<
	CD_CS1FX_L			L:S:1000 MIL				33 MHZ	38C6<>
	EIDE_CS1FX_L			L:S:3500 MIL:5500 MIL				33 MHZ	37A7> 38B8<
CD_CS3FX_L			L:S:1000 MIL				33 MHZ	38C6<>	
EIDE_CS3FX_L			L:S:3500 MIL:5500 MIL				33 MHZ	37A7> 38B8<	
HD DRIVE BUS	UIDE_RST_L	HD_DATA:G:L:S:0		L:S:100 MIL:6000 MIL				100 MHZ	37C7<> 37D3<
	UIDE_DMACK_L	HD_DATA:G:L:S:0		L:S:100 MIL:6000 MIL				100 MHZ	37C7<> 37D3<
	UIDE_DIOR_L	HD_DATA:G:L:S:0		L:S:100 MIL:6000 MIL				100 MHZ	37C7<> 37D3<
	UIDE_DIOW_L	HD_DATA:G:L:S:0		L:S:100 MIL:6000 MIL				100 MHZ	37C3< 37C7<>
	UIDE_IOCHRDY	HD_DATA:G:L:S:0		L:S:100 MIL:6000 MIL				100 MHZ	37C3< 37C7<>
	UIDE_DATA<0..15>	HD_DATA:G:L:S:0		L:S:100:6000				100 MHZ	37C3< 37C7<
	HD_RESET_L			L:S:1000 MIL				100 MHZ	37A3< 37B3< 37C3< 37C7<> 37D7<>
	HD_DMACK_L			L:S:1000 MIL				100 MHZ	37D1< 38C3<>
	HD_DIOR_L			L:S:5500 MIL				100 MHZ	37D1< 38C3<>
	HD_DIOW_L			L:S:55000 MIL				100 MHZ	37C1< 38C3<>
	HD_IOCHRDY			L:S:1000 MIL				100 MHZ	37C1< 38C3<>
	HD_DMARQ			L:S:1000 MIL				100 MHZ	38C3<>
	UIDE_DMARQ	HD_DATA:G:L:S:0		L:S:500 MIL:6000 MIL				100 MHZ	37C7<> 38C4<
	HD_INTRO			L:S:1000 MIL				100 MHZ	38C3<>
	UIDE_INTRO	HD_DATA:G:L:S:0		L:S:500 MIL:6000 MIL				100 MHZ	37C7< 38C4<
	HD_UIDE_ADDR<0..2>			L:S:1000				100 MHZ	38C2<> 38C3<>
	UIDE_ADDR<0..2>	HD_DATA:G:L:S:0		L:S:100:6000				100 MHZ	37C7<> 38A4< 38B4<
	HD_UIDE_CS1FX_L			L:S:6000 MIL				100 MHZ	38C3<>
	UIDE_CS1FX_L	HD_DATA:G:L:S:0		L:S:6000 MIL				100 MHZ	37C7<> 38B4<
	HD_UIDE_CS3FX_L			L:S:6000 MIL				100 MHZ	38C2<>
UIDE_CS3FX_L	HD_DATA:G:L:S:0		L:S:6000 MIL				100 MHZ	37C7<> 38B4<	
CLK_18M_INT_XOUT		3		L:S:1000 100		8 MIL SPACING		18.432 MHZ	58B5>
CLK_18M_INT_XOUT		3		L:S:1000 100		8 MIL SPACING		18.432 MHZ	58B5>
CLK_18M_INT_XOUT		3		L:S:200 50		8 MIL SPACING		18.432 MHZ	58B5>
USB_DAP	USBA:G:L:S:0		MIL:500 MIL						28A3< 28B3<>
USB_DAN	USBA:G:L:S:0		MIL:500 MIL						28A3< 28B3<>
USB_DAP_F	USBA_F:G:L:S:0		MIL:500 MIL						28B2< 33B7<
USB_DAN_F	USBA_F:G:L:S:0		MIL:500 MIL						28B2< 33B7<
USB_DBP	USBB:G:L:S:0		MIL:500 MIL						28A3< 28B3<>
USB_DBN	USBB:G:L:S:0		MIL:500 MIL						28A3< 28B3<>
USB_DBP_F	USBB_F:G:L:S:0		MIL:500 MIL						28B2< 33C7<
USB_DBN_F	USBB_F:G:L:S:0		MIL:500 MIL						28B2< 33C7<
USB_DCP	USBC:G:L:S:0		MIL:500 MIL						28A3< 28B3<>
USB_DCN	USBC:G:L:S:0		MIL:500 MIL						28A3< 28B3<>
USB_DCP_F	USBC_F:G:L:S:0		MIL:500 MIL						28B2< 33D7<
USB_DCN_F	USBC_F:G:L:S:0		MIL:500 MIL						28B2< 33D7<
USB_DEP	USBE:G:L:S:0		MIL:500 MIL						28B3<>
USB_DEN	USBE:G:L:S:0		MIL:500 MIL						28B3<>
BT_USB_DP	USBE_F:G:L:S:0		MIL:500 MIL						28B2< 29D3<> 59B6>
BT_USB_DM	USBE_F:G:L:S:0		MIL:500 MIL						28B2< 29D3<> 59B6>
USB_DFP	USBF:G:L:S:0		MIL:500 MIL						28B3<>
USB_DFN	USBF:G:L:S:0		MIL:500 MIL						28B3<>
MODEM_USB_DP	USBF_F:G:L:S:0		MIL:500 MIL						28B2< 29C5<> 59B6>
MODEM_USB_DM	USBF_F:G:L:S:0		MIL:500 MIL						28B2< 29C5<> 59B6>
PMU_XO		3		L:S:1000 MDD		8 MIL SPACING		10 MHZ	44B5<
PMU_XI		3		L:S:1000 MDD		8 MIL SPACING		10 MHZ	44B5<
PMU_XT		3		L:S:300 MDD		8 MIL SPACING		10 MHZ	44A6<
PMU_CLKOUT		3		L:S:1000 MDD		8 MIL SPACING		32.768 MHZ	44B4<>
PMU_CLKIN		3		L:S:1000 MDD		8 MIL SPACING		32.768 MHZ	44B4<>
PMU_CLKT		3		L:S:300 MDD		8 MIL SPACING		32.768 MHZ	44B2<>
MICSHLD						10 MIL SPACING			29A5<> 43A8< 59A8>
MICHIGH						10 MIL SPACING			29A5<> 43B8< 59A8>
MICLOW						10 MIL SPACING			29A5<> 43A8< 59A8>
KS_INT_SPKR+						10 MIL SPACING			29A3< 43D7< 59B8>
KS_INT_SPKR-						10 MIL SPACING			29A3< 42B4< 43D7< 59B8>

SIG_NAME	PROPAGATION_DELAY	PULSE_PARAM	
T_UD_IDEDD_0	L:S:1000 MIL	100 MHZ	37C1< 38C3<>
T_UD_IDEDD_1	L:S:1000 MIL	100 MHZ	37C1< 38C3<>
T_UD_IDEDD_2	L:S:1000 MIL	100 MHZ	37C1< 38C3<>
T_UD_IDEDD_3	L:S:1000 MIL	100 MHZ	37C1< 38C3<>
T_UD_IDEDD_4	L:S:1000 MIL	100 MHZ	37B1< 38C3<>
T_UD_IDEDD_5	L:S:1000 MIL	100 MHZ	37B1< 38C3<>
T_UD_IDEDD_6	L:S:1000 MIL	100 MHZ	37B1< 38C3<>
T_UD_IDEDD_7	L:S:1000 MIL	100 MHZ	37B1< 38C3<>
T_UD_IDEDD_8	L:S:1000 MIL	100 MHZ	37B1< 38C2<>
T_UD_IDEDD_9	L:S:1000 MIL	100 MHZ	37B1< 38C2<>
T_UD_IDEDD_10	L:S:1000 MIL	100 MHZ	37B1< 38C2<>
T_UD_IDEDD_11	L:S:1000 MIL	100 MHZ	37B1< 38C2<>
T_UD_IDEDD_12	L:S:1000 MIL	100 MHZ	37B1< 38C2<>
T_UD_IDEDD_13	L:S:1000 MIL	100 MHZ	37A1< 38C2<>
T_UD_IDEDD_14	L:S:1000 MIL	100 MHZ	37A1< 38C2<>
T_UD_IDEDD_15	L:S:1000 MIL	100 MHZ	37A1< 38C2<>

SIGNAL CONSTRAINTS

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APPLE COMPUTER INC.	SIZE	DRAWING NUMBER	REV.
	D	051-6497	13
SCALE	SHT	OF	
NONE	58	69	

FUNC_TEST

FUNC_TEST

FUNC_TEST

D

D

C

C

B

B

A

A

52C1> 51D7<> 51C3<> 52C4	+1_8V_MAIN	STOP
51B7< 51A7< 51A5< 50D5<> 50D4<> 50D5<> 50D5<> 49D3<> 49C4<>	+12V_MAIN	STOP
50C4<> 50B4<> 49D7<> 49D3<> 49C4<>	+12V_SLEEP	STOP
48B4<> 44D8< 42B3< 39C8< 36D8<> 48D6<> 48C8<> 48C4<> 45D7< 45D3<>	+12V_SLEEPA	STOP
51A5< 50D5< 29C5<> 29A8< 29A3<> 52C1> 51D6<> 51C2<>	FUNC_TEST	STOP
52C4 51B4<>	+5V_MAIN	STOP
51C5<> 50D5< 46D6<> 46C7< 38C1<> 51C8<>	+5V_SLEEP	STOP
52C4 49B2<> 30B3<>	+2_5V_MAIN	STOP
42B5< 41A7< 41A5< 40D5< 39D4<> 52C4 43C7< 42C8< 42B7<>	+3V_MAIN	STOP
52C6> 45D2<> 8C1< 8B7< 4D7< 4D3<> 59B6>	CPU_VCORE_SLEEP	STOP
35C4< 34B7< 8A4<>	JTAG_ASIC_TCK	STOP
34B7< 29C6< 8A4<>	JTAG_ASIC_TDI	STOP
35B4<> 8A4<>	JTAG_ASIC_TDO	STOP
35B4<> 35A2< 34B7< 8A4<>	JTAG_ASIC_TMS	STOP
34B7< 8A4<>	JTAG_ASIC_TRST_L	STOP
52A6> 24C3<>	INT_TMDS_3V	STOP
16D7< 16C2< 16A8< 11A6< 10D6<> 52C3> 46B4<> 17D5< 17A4< 17A3<>	+1_5V_AGP	STOP
52B3> 29A5<>	FAN_12V_FILT	STOP
47B2<> 46B3< 11D3< 10D6<>	+INTREPID_CORE_MAIN	STOP
47C6<>	INTREPID_VSENSE	STOP
8D5<> 8A3<> 7B5< 4B3>	OVDD_ADJ	STOP
7B5< 4B3>	CPU_CHKSTP_OUT_L	STOP
44C2< 8A3<> 7B3< 7A5< 7A3< 4B3<> 44D2<>	CPU_CHKSTP_IN_L	STOP
8A3<> 7D5< 4C3<>	CPU_HRESET_L	STOP
8A3<> 7A5< 4C3<>	JTAG_CPU_TCK	STOP
8A3<> 4C3>	JTAG_CPU_TDI	STOP
8A3<> 4C3>	JTAG_CPU_TDO	STOP
8A3<> 7A5< 4C3>	JTAG_CPU_TMS	STOP
8A3<> 7C5< 4C3>	JTAG_CPU_TRST_L	STOP
7C7< 7C5< 52C6> 46D4< 45D2<> 44D2< 44D1<> 7C3< 7B3< 7A3< 6D6< 6C5< 4D5<> 44B7< 3D8< 9B7< 8D4< 8D1< 8A3<>	+MAXBUS_SLEEP	STOP
31B4<> 30C6< 30B4<>	ROM_CS_L	STOP
31B2<> 30C6< 30B2<>	ROM_OE_L	STOP
31B4<> 30B6< 30B2<>	ROM_RW_L	STOP
52B6> 24B3<>	DDC_VCC_3	STOP
52A6> 25C4<>	DDC_VCC_5	STOP
41A5< 28B5<>	SND_HP_SENSE_L	STOP
57D5> 26B5< 25D6<> 22D7<>	ANALOG_HSYNC*	STOP
57D5> 26B5< 25C6<> 22D7<>	ANALOG_VSYNC*	STOP
57D5> 25C5<>	FILT_ANALOG_BLU	STOP
57D5> 25C5<>	FILT_ANALOG_RED	STOP
57D5> 25C5<>	FILT_ANALOG_GRN	STOP
58A5> 43D7< 29A3<>	GND	STOP
58A5> 43D7< 42B4< 29A3<>	KS_INT_SPKR+	STOP
57D2> 27C2< 24D7<> 23D1<>	KS_INT_SPKR-	STOP
57D2> 27C2< 24D7<> 23D1<>	TMDS_D2P	STOP
57D2> 27C2< 24D7<> 23D1<>	TMDS_D2M	STOP
57D2> 27C2< 24C7<> 23D1<>	TMDS_D1P	STOP
57D2> 27C2< 24C7<> 23D1<>	TMDS_D1M	STOP
57D2> 27C2< 24B7<> 23D1<>	TMDS_D0P	STOP
57D2> 27C2< 24B7<> 23D1<>	TMDS_D0M	STOP
57D2> 27C2< 24B7<> 23D1<>	TMDS_CK1	STOP
57D2> 27C2< 24B7<> 23D1<>	TMDS_CK2	STOP
57D2> 27C2< 24A7<> 23D1<>	TMDS_CKM	STOP
29A5<>	INV_CUR_HI_FILT	STOP
44D3< 44B8<> 35B8< 32A6<>	IO_RESET_L	STOP
52B3> 29A5<>	KS5VSD	STOP
39B1<> 34B5< 29C7<> 28D1< 28A3<>	INT_I2C_CLK2	STOP
39B1<> 34B5< 29C7<> 28D1< 28A3<>	INT_I2C_DATA2	STOP
56B3> 54A7< 16C7< 9B4< 8A2<>	INT_ANALYZER_CLK	STOP
29A5<>	LAMP_STS_FILT	STOP
29A5<>	LCD_PWM_FILT	STOP
52B3> 29A5<>	LED_5V_FILT	STOP
52A3> 29A5<>	LED_RET_FILT	STOP
58A5> 43A8< 29A5<>	MICSHLD	STOP
58A5> 43B8< 29A5<>	MICHIGH	STOP
58A5> 43A8< 29A5<>	MICLOW	STOP
29D5<> 28C5<>	COMM_RESET_L	STOP
29C6<>	IIC_ADD	STOP
30B2<>	ROM_WP_L	STOP

29D7<> 28D1< 28C5<>	COMM_SHUTDOWN	STOP
25C6< 23D7<>	MON_DETECT	STOP
45C8<> 45C7<>	FLO_KNOWS_BEST	STOP
44C4<> 29B2<>	NMI_BUTTON*	STOP
59D6> 44C5<> 44B1< 8A8<>	PWR_SWITCH*	STOP
44B5<> 44A5<> 29B3<> 8A8<>	PMU_RST*	STOP
44A4<>	PMURESETBUTTON*	STOP
59D6> 44C5<> 44B1< 8A8<>	PWR_SWITCH*	STOP
51C6< 50C8< 50C3< 42D8< 39C8<>	PMU_RST*	STOP
51A8< 44C7<>	PWR_UP	STOP
44C4<> 29B2<>	POWER_UP*	STOP
44C5<> 29C5<> 28B8< 28B5<>	RESET_BUTTON*	STOP
31B4<> 30B2<>	COMM_RING_DET_L	STOP
29C7<> 28C3>	ROM_ONBOARD_CS_L	STOP
29C7<> 28C3>	COMM_DTR_L	STOP
29C7<> 28C3>	COMM_TXD_L	STOP
29C7<> 28D3< 28C3>	COMM_TRXC	STOP
29C5<> 28D3< 28C3>	COMM_RTS_L	STOP
29C5<> 28D3< 28C3>	COMM_RXD	STOP
50C2< 44B5<>	COMM_GPIO_L	STOP
8A3<> 7A5< 4B3>	SLEEP	STOP
52B3> 44D4<> 44B5<>	CPU_SRESET_L	STOP
24B3<>	PMU_AVCC	STOP
24B4<>	TMDS_DDC_CLK	STOP
56A3> 33D3<>	TMDS_DDC_DAT	STOP
56A3> 33D3<>	USB_DCN_CON	STOP
56A3> 33B3<>	USB_DCP_CON	STOP
56A3> 33B3<>	USB_DBN_CON	STOP
56A3> 33B3<>	USB_DBP_CON	STOP
56A3> 33C3<>	USB_DAN_CON	STOP
56A3> 33C3<>	USB_DAP_CON	STOP
58B5> 29D3<> 28B2<>	BT_USB_DP	STOP
58A5> 29D3<> 28B2<>	BT_USB_DM	STOP
58A5> 29C5<> 28B2<>	MODEM_USB_DP	STOP
58A5> 29C5<> 28B2<>	MODEM_USB_DM	STOP
52A3> 33D3<> 33C3<> 33B3<> 33A4<>	USB_PORT_PWR	STOP
25C4<>	VGA_IIC_CLK	STOP
25C4<>	VGA_IIC_DAT	STOP
59D8> 52C6> 45D2<> 8C1< 8B7< 4D7< 4D3<>	CPU_VCORE_SLEEP	STOP
40B7<>	LINE_IN_COM	STOP
40B7<>	LINE_IN_R	STOP
40C7<> 40B7<>	LINE_IN_SENSE	STOP
40C7<> 40B7<>	LINE_IN_L	STOP
40D4< 28B5<>	SND_LIN_SENSE_L	STOP
54D7< 32B6<> 31B7< 30C5<> 30B7<>	OUT_R	STOP
54D7< 32B6<> 31B7< 30C5<> 30B7<>	LINEOUT_COMM2	STOP
54D7< 32B6<> 31B7< 30C5<> 30B7<>	LINE_OUT_L	STOP
53A6< 32B6<> 31B7< 30C5<> 30B7<>	PCIT_IRDY_L	STOP
54D7< 32B6<> 31B7< 30C5<>	RF_CLKRUN_L	STOP
31C3<>	NC_RF_DISABLE_L	STOP
54D7< 32B6<> 31B7< 30C5<>	PCI_DEVSEL_L	STOP
54D7< 32B6<> 31B7< 30C5<> 30B7<>	PCI_STOP_L	STOP
54D7< 32B6<> 31B7< 30C5<> 30B7<>	PCI_TRDY_L	STOP
54D7< 32B6<> 31B7< 30C5<> 30B7<>	PCI_FRAME_L	STOP
54D7< 32B6<> 31B7< 30C5<> 30B7<>	PCI_PAR	STOP
54D7< 32B6<> 31B7< 30C5<>	WL_PCI_IDSEL	STOP
31C2<>	33SLOTB_INT_L	STOP
44B2<> 32A8< 31C2< 28B5<>	PMU_PME_L	STOP
31C2<> 30D5<> 30B5<>	PCI_SLOTB_GNT_L	STOP
54D7< 31C2<> 30D7<>	CLK33M_PCI_SLOTB	STOP
31C3<> 30D5<> 30B7<>	PCI_SLOTB_REQ_L	STOP
44C4<> 32A8< 31D4< 30B2< 17C8<>	MAIN_RESET_L	STOP
53A6< 32B6<> 31B7< 30C5<>	PCI_CBE<0>	STOP
54C7< 31C3<> 31B6<>	PCI_CBE<1>	STOP
54C7< 31C3<> 31B6<>	PCI_CBE<2>	STOP
54C7< 31C3<> 31B6<>	PCI_CBE<3>	STOP
28C1< 28B5<>	UNUSED_GPIO15	STOP

53A6< 32C6<> 31C7< 30D4<> 30C2<>	PCI_AD<0>	STOP
54C7< 31C6< 31B3<>	PCIT_AD<1>	STOP
54C7< 31C6< 31B2<>	PCIT_AD<2>	STOP
54C7< 31C6< 31B3<>	PCIT_AD<3>	STOP
53A6< 32C6<> 31C7< 30D4<> 30C2<>	PCI_AD<4>	STOP
54C7< 31C6< 31B3<>	PCIT_AD<5>	STOP
53A6< 32C6<> 31C7< 30D4<> 30C2<>	PCI_AD<6>	STOP
54C7< 31C6< 31B3<>	PCIT_AD<7>	STOP
54C7< 31C6< 31B3<>	PCIT_AD<8>	STOP
53A6< 32C6<> 31C7< 30D4<> 30C2<>	PCI_AD<9>	STOP
54C7< 31C6< 31B3<>	PCIT_AD<10>	STOP
53A6< 32C6<> 31C7< 30C4<> 30B2<>	PCI_AD<11>	STOP
54C7< 31C6< 31B3<>	PCIT_AD<12>	STOP
53A6< 32C6<> 31C7< 30C4<> 30B2<>	PCI_AD<13>	STOP
54C7< 31C6< 31C3<>	PCIT_AD<14>	STOP
53A6< 32C6<> 31C7< 30C4<> 30B2<>	PCI_AD<15>	STOP
53A6< 32C6<> 31C7< 30C4<> 30B2<>	PCI_AD<16>	STOP
54C7< 31C7< 31C3<>	PCIT_AD<17>	STOP
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54C7< 31C7< 31C3<>	PCIT_AD<19>	STOP
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54C7< 31C6< 31C3<>	PCIT_AD<21>	STOP
53A6< 32C6<> 31C6< 30C4<> 30B2<>	PCI_AD<22>	STOP
54C7< 31C6< 31C3<>	PCIT_AD<23>	STOP
53A6< 32C6<> 31B6< 30C4<> 30C1<>	PCI_AD<24>	STOP
54C7< 31C3<> 31B6<>	PCIT_AD<25>	STOP
53A6< 32B6<> 31B6< 30C4<> 30C1<>	PCI_AD<26>	STOP
54C7< 31C3<> 31B6<>	PCIT_AD<27>	STOP
54C7< 31C2<> 31B7<>	PCIT_AD<28>	STOP
54C7< 31C3<> 31B6<>	PCIT_AD<29>	STOP
53A6< 32B6<> 31B6< 30C4<> 30C1<>	PCI_AD<30>	STOP
54C7< 31C3<> 31B6<>	PCIT_AD<31>	STOP

CONSTRAINT TABLES

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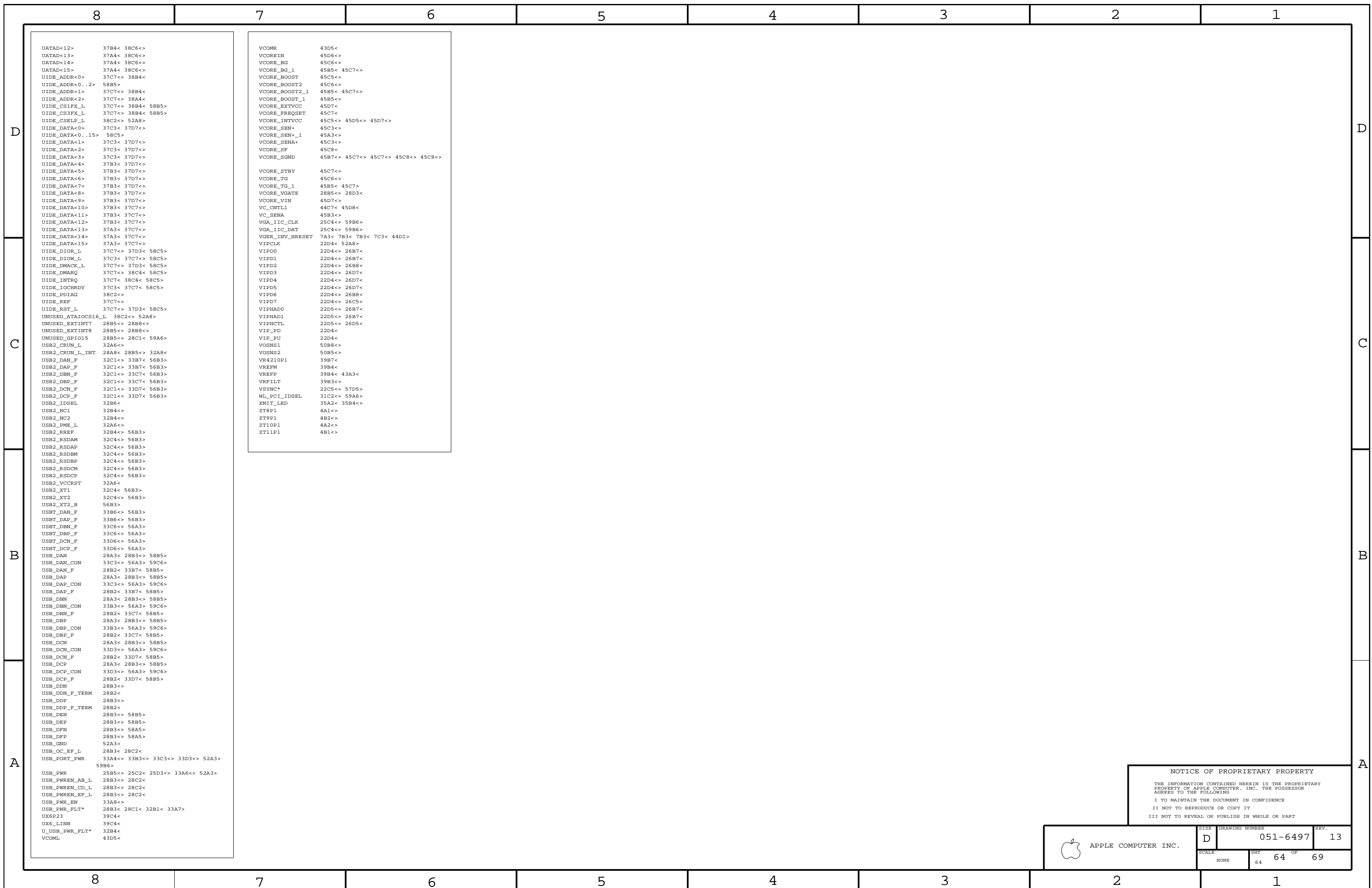
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	D	051-6497	13
	SCALE	SHT	OF
	NONE	59	69

	8	7	6	5	4	3	2	1
	<pre> *** Signal Cross-Reference for the entire design *** +1_5V_AGP 10D6< 11A6< 16A8< 16C2< 16D7< 17A3< 17A4< 17D5< 46B4< 52C3> 59C8> +1_5V_INTREPID_PLL 9D4< 16D6< 28D6> 30D5< 52D3> +1_5V_INTREPID_PLL1 28C4< 52D3> +1_5V_INTREPID_PLL2 28D4< 52D3> +1_5V_INTREPID_PLL3 28D4< 52D3> +1_5V_INTREPID_PLL4 28D4< 52D3> +1_5V_INTREPID_PLL5 16D5< 52D3> +1_5V_INTREPID_PLL6 30D4< 52D3> +1_5V_INTREPID_PLL7 9D2< 52D3> +1_5V_INTREPID_PLL8 28D4< 52D3> +1_8V_MAIN 52C4 59D8> +2_5V_MAIN 30B3< 49B2< 52C4 59D8> +3_3VFFD 24D7< 51C1< 52B6> +3V_AUDIO 39D2< 39D6< 39D7< 40C3< 43B2< 43C6< +3V_GPIU_SS 22A6< 22A8< 22B7< +3V_INTREPID_USB 28C4< 52A3> +3V_MAIN 39D4< 40D5< 41A5< 41A7< 42B5< 42B7< 42C8< 43C7< 52C4 59D8> +5VSD_T 50D6<< +5V_AUDIO 39D6< 39D7< 41B8<> +5V_HP 41A8< 41B7<> 41D5< +5V_MAIN 51B4< 51B4< 52C4 59D8> +5V_SLEEP 38D1< 46C7< 46D6> 50D5< 51C5<> 51C8< 59D8> +12VSD_FILT 29A5< 52B3> +12VSD_T 50D6<< +12V_DROPPED 44D8< +12V_MAIN 36D8< 39C8< 42B3< 44D8< 45B4<> 45D3< 45D7< 48C4< 48C8> 48D6<> 49C4< 49D3< 49D7> 50B4< 50C4> 50D2< 50D5< 51A5< 51A7< 51B7< 51C3< 51D7< 52C1> 52C1> 59D8> +12V_SLEEP 29A3< 29A8< 29C5< 50D5< 51A5< 51C2< 51D6< 52C1> 52C1> 59D8> +12V_SLEEPA 59D8> +12V_TPA 42B2< 42B7< 42C4< 42C5< 42D5< +INTREPID_CORE_MAIN 10D6< 11D3< 46B3< 47B2< 59C8> +MAXBUS_SLEEP 4D5< 6C5< 6D6< 7A3< 7A3< 7B3< 7C3< 7C3< 7C5< 7C7< 8A3> 8D1< 8D4< 9B7< 9D8< 44B7< 44D1< 44D2< 45D2< 46D4< 52C6> 59C8> 3_8VH_TRICKLE 44C1< 44D7> 52B3> 3_8V_TRICKLE 36D8< 44C2< 44C6< 44D7> 50D5< 52C1> 52C2> 3V_SI_AVCC 27D4< 3V_SI_PLLVCC 27D4< 3V_SI_VCC 27B2< 27D3< 3_5_HONKER 51C4< 3_6V_SLEEP 37D1< 51C1< 5V_XRA 54B7<< 15_I282 15B2< 15_I286 15B3< 18P_GND 29B5<> 25V_BSTH 49C5<> 25V_COMP 49B6< 25V_COMP_DWN 49B6< 25V_DH 49B5<> 25V_DHT 49B5<> 25V_DL 49B5<> 25V_DLT 49B4<> 25V_GND 49B6<> 25V_OCSSET 49C5<> 25V_OVP 49B6<> 25V_VCC 49C6< 25V_VFWR 49B5< 49C4<> 25V_VFWR_A 49B4<> 25V_VSSENSE 49C4<> 25_CORE_1 49C3< 33PCL_SLOTD_SERR_L 32B6<> 33SLOTH_INTN_L 28B7<> 28B7> 28B7> 31C2<> 59A6> 42_I291 42B6< 42_I295 42B5< 45_I408 45B6< 45_I525 45C4< 45_I526 45B3< 47_I66 47B4< 48_I10 48C3< 48_I99 48B4< 49_I70 49B4< 50_I408 50B3< 50_I410 50A3< AGND 39B7< 52C4 AGP_AD<0> 16C4< 17D8< AGP_AD<0..15> 54C7< AGP_AD<1> 16C4< 17D8< AGP_AD<2> 16C4< 17D8< AGP_AD<3> 16C4< 17D8< AGP_AD<4> 16C4< 17D8< AGP_AD<5> 16C4< 17D8< AGP_AD<6> 16C4< 17D8< AGP_AD<7> 16C4< 17D8< AGP_AD<8> 16C4< 17D8< AGP_AD<9> 16C4< 17D8< AGP_AD<10> 16C4< 17D8< AGP_AD<11> 16C4< 17D8< AGP_AD<12> 16C4< 17D8< AGP_AD<13> 16C4< 17D8< AGP_AD<14> 16C4< 17D8< AGP_AD<15> 16C4< 17C8< AGP_AD<16> 16C4< 17C8< AGP_AD<16..31> 54C7< AGP_AD<17> 16C4< 17C8< AGP_AD<18> 16C4< 17C8< </pre>	<pre> AGP_AD<19> 16C4< 17C8< AGP_AD<20> 16C4< 17C8< AGP_AD<21> 16C4< 17C8< AGP_AD<22> 16C4< 17C8< AGP_AD<23> 16C4< 17C8< AGP_AD<24> 16B4< 17C8< AGP_AD<25> 16B4< 17C8< AGP_AD<26> 16B4< 17C8< AGP_AD<27> 16B4< 17C8< AGP_AD<28> 16B4< 17C8< AGP_AD<29> 16B4< 17C8< AGP_AD<30> 16B4< 17C8< AGP_AD<31> 16B4< 17C8< AGP_AD_STB<0> 16A4< 16B3< 17B8< 54C7< AGP_AD_STB<1> 16A4< 16B3< 17B8< 54C7< AGP_AD_STB<2> 16A4< 16D1< 17B8< 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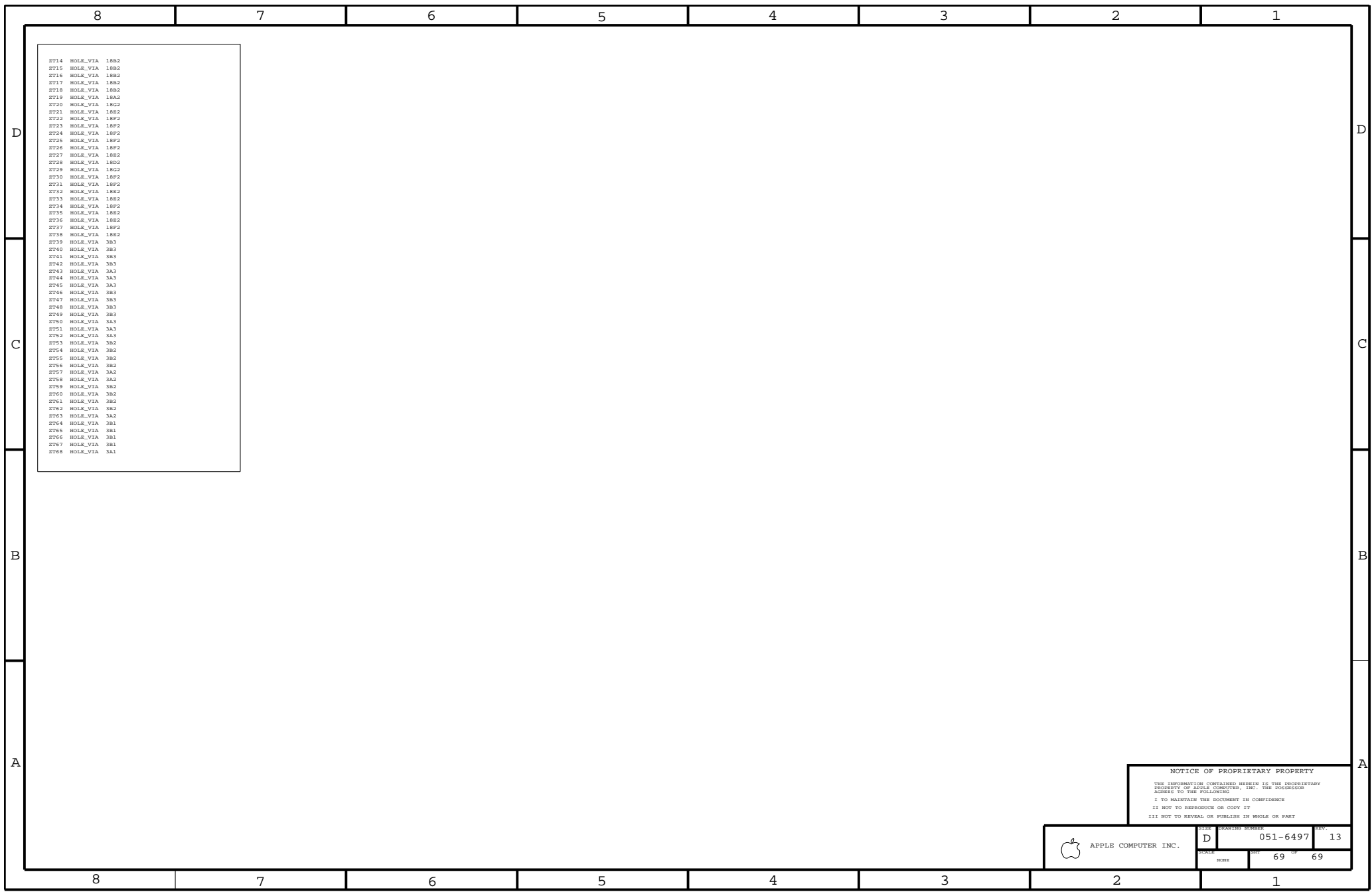
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	*** Part Cross-Reference for the entire design ***							
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	BS2 PCR_STANDOFF 29B6	C105 CAP 17B2	C222 CAP 16A8	C338 CAP_P 50D6	C456 CAP 36B3	C579 CAP 39C5		
	BS3 PCR_STANDOFF 29B6	C106 CAP_P 48B2	C223 CAP 17C2	C339 CAP_P 50D6	C457 CAP 36B4	C580 CAP_P 39B2		
	BS4 PCR_STANDOFF 29D1	C107 CAP 24B2	C224 CAP 22C2	C340 CAP_P 45C2	C458 CAP 36B3	C581 CAP 39B2		
	BT1 BATTERY 44D6	C108 CAP 42B7	C225 CAP 30C2	C341 CAP 50B7	C460 CAP 21D2	C582 CAP 42B7		
	C1 CAP 33C4	C109 CAP 17D3	C226 CAP 22B6	C342 CAP_P 49C3	C461 CAP 21B7	C583 CAP 27D3		
	C2 CAP 33D4	C110 CAP 17D2	C227 CAP 22C3	C343 CAP_P 45D3	C462 CAP 21B8	C584 CAP 39D3		
	C3 CAP 35B1	C111 CAP 17B3	C228 CAP 22C3	C344 CAP 50B4	C463 CAP 36B4	C585 CAP 48B5		
	C4 CAP 36B6	C112 CAP 17B2	C229 CAP 28C6	C345 CAP 48B1	C464 CAP 36B4	C586 CAP 48B4		
	C5 CAP_P 33A5	C113 CAP 27D5	C230 CAP 13D7	C346 CAP_P 45B1	C466 CAP 21B7	C587 CAP 39B2		
	C6 CAP 33B4	C114 CAP 32D3	C231 CAP 11A6	C347 CAP_P 45B2	C467 CAP 21D8	C588 CAP 39B5		
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	C713 CAP 28D5	C835 CAP 11B3	C955 CAP 50D3	C1065 CAP 42A7	C4702 CAP 47B4	L8 FILTER_4P 36D2		
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	C719 CAP 11D2	C841 CAP 11D4	C961 CAP 8C2	C1071 CAP 42C4	D2 DIODE_DUAL_6P 36A7	L20 IND 45C3		
	C720 CAP 11C3	C842 CAP 11D6	C962 CAP 49C3	C1072 CAP 42C3	D3 DIODE_DUAL_6P 36A7	L21 IND_3P 49B3		
	C721 CAP 11D3	C843 CAP 11C7	C963 CAP 45D4	C1073 CAP 42C3	D4 DIODE_DUAL_6P 36B7	L22 IND_3P 50A3		
	C722 CAP 11B7	C844 CAP 11D6	C964 CAP 45D4	C1074 CAP 42C3	D5 ZENER_MMBZ15VDLT1 40B6	L23 IND 50C3		
	C723 CAP 11B6	C845 CAP 11D6	C965 CAP 45D4	C1075 CAP 42C2	D6 DIODE_DUAL_6P 25D4	L24 IND 45B3		
	C724 CAP 11B5	C846 CAP 11D7	C966 CAP 45D4	C1076 CAP 43D5	D7 DIODE_DUAL_6P 25D4	L26 IND 42A6		
	C725 CAP 11C7	C848 CAP 30D5	C967 CAP 8C1	C1077 CAP 43D5	D8 DIODE 36D6	L27 IND 41C3		
	C726 CAP 15D3	C849 CAP 11C6	C968 CAP 49C4	C1078 CAP 43D5	D9 DIODE 35B8	L32 IND 36B6		
	C727 CAP 11D4	C850 CAP 11B3	C969 CAP 8D4	C1079 CAP 43D5	D10 DIODE_SCHOT 23B8	L33 IND 22B7		
	C728 CAP 11C3	C851 CAP 11B3	C970 CAP 50D4	C1080 CAP_P 42B3	D11 DIODE_SCHOT 44D7	L34 IND 41D3		
	C729 CAP 11B3	C852 CAP 11B2	C971 CAP 50D4	C1081 CAP_P 42A3	D12 DIODE_SCHOT 44D7	L35 IND 41B3		
	C730 CAP 11D3	C854 CAP 9D3	C972 CAP 8D2	C1082 CAP 42B2	D13 DIODE_SCHOT 47C7	L36 IND 40B6		
	C731 CAP 11D1	C855 CAP 11C5	C973 CAP 8D1	C1083 CAP 42B2	D14 DIODE_SCHOT 47B5	L39 IND 41A3		
	C732 CAP 11D1	C856 CAP 11B7	C974 CAP 8D1	C1084 CAP 42B2	D15 DIODE_SCHOT 49B3	L40 IND 42A5		
	C733 CAP 11A6	C857 CAP 11C6	C975 CAP 49C4	C1085 CAP 42B2	D16 DIODE_SCHOT 50C5	L41 IND 42B7		
	C734 CAP 11D2	C858 CAP 13D4	C976 CAP 8C1	C1086 CAP 42A2	D17 DIODE_SCHOT 50B5	L42 IND 41B3		
	C735 CAP 11B7	C859 CAP 13D3	C977 CAP 8D1	C1087 CAP 43D2	D18 DIODE_SCHOT 49C6	L43 IND 41A4		
	C736 CAP 11B5	C860 CAP 13D4	C978 CAP 50D4	C1088 CAP 43D2	D19 DIODE_SCHOT 46D6	L44 IND 41C3		
	C741 CAP 15D3	C861 CAP 44A1	C979 CAP 50D4	C1089 CAP 42B2	D20 DIODE_SCHOT 45D5	L45 IND 41D3		
	C742 CAP 51C6	C862 CAP 44B7	C980 CAP 8D2	C1090 CAP 42B2	D21 DIODE_SCHOT 45B5	L46 IND 40C6		
	C743 CAP 11B3	C863 CAP 11C2	C981 CAP 8D2	C1091 CAP 42B2	D22 DIODE_SCHOT_3P 42A6	L47 IND 41B3		
	C744 CAP 11B1	C864 CAP 15D2	C982 CAP 8D2	C1092 CAP 42B2	D24 ZENER_MMBZ15VDLT1 41B2	L48 IND 40C6		
	C745 CAP 11C3	C865 CAP 11B1	C983 CAP 8D3	C1093 CAP 42A2	D25 ZENER 36B6	L49 IND 40B5		
	C746 CAP 11D3	C866 CAP 11C3	C984 CAP 27D3	C1094 CAP 43C2	D26 ZENER_MMBZ15VDLT1 41B2	L50 IND 40C5		
	C747 CAP 11D1	C867 CAP 11C3	C985 CAP 50D3	C1095 CAP 43D2	D27 DIODE_DUAL_6P 25B4	L51 IND 40D6		
	C748 CAP 11D2	C868 CAP 11C2	C986 CAP 50D4	C1096 CAP 8A7	D28 DIODE_DUAL_6P 25B4	L52 IND 40C5		
	C749 CAP 11D3	C869 CAP 11D5	C987 CAP 8D2	C1097 CAP 8A7	D29 ZENER_MMBZ15VDLT1 40B5	L53 IND 40D5		
	C750 CAP 11B6	C870 CAP 11D6	C988 CAP 8D3	C1098 CAP 8B5	D30 DIODE_SCHOT 48C5	L56 IND 44A1		
	C751 CAP 11B7	C871 CAP 49C3	C989 CAP 49C3	C1099 CAP 42C8	D31 DIODE_SCHOT 48B3	L57 IND 44A1		
	C752 CAP 11B6	C872 CAP 11D5	C990 CAP 8D1	C1100 CAP 42C2	D32 DIODE_SCHOT 48B5	L58 IND 32D6		
	C753 CAP 11B6	C873 CAP 11D5	C991 CAP 8D1	C1101 CAP 9D3	D33 DIODE_SCHOT 46B6	L59 IND 43A5		
	C754 CAP 11B5	C874 CAP 11D5	C992 CAP 8D3	C1102 CAP_P 42B3	D34 DIODE_SCHOT 48B2	L60 IND 43B5		
	C755 CAP 11B6	C875 CAP 11B3	C993 CAP 8D1	C1103 CAP 22B5	D35 DIODE_SCHOT 44D6	L63 IND 39D4		
	C759 CAP 44D5	C876 CAP 11C5	C994 CAP 8C2	C1401 CAP 14D7	D36 DIODE_SCHOT 44D6	L64 IND 24A5		
	C760 CAP 16D6	C877 CAP 11C7	C995 CAP 8D2	C1402 CAP 14D2	D37 DIODE_SCHOT 50B2	L65 IND 29A7		
	C761 CAP 11B3	C878 CAP 8A7	C996 CAP 8A7	C1403 CAP 14C1	D38 DIODE_SCHOT 50C6	L66 IND 29A3		
	C762 CAP 11C1	C879 CAP 11B2	C997 CAP 8A6	C1404 CAP 14C1	D39 DIODE_SCHOT 50B4	L67 FILTER_4P 24B5		
	C763 CAP 11D2	C880 CAP 11C5	C998 CAP 8D2	C1405 CAP 14C1	D40 DIODE_SCHOT 50A4	L68 IND 22D2		
	C764 CAP 11D3	C881 CAP 11B3	C999 CAP 8D1	C1406 CAP 14C3	D41 DIODE_SCHOT 45C4	L69 IND 29A3		
	C765 CAP 11D3	C882 CAP 11C0	C1000 CAP 49B6	C1407 CAP 14C2	D42 DIODE_SCHOT 49B5	L70 IND 29A7		
	C766 CAP 11D1	C883 CAP 11B4	C1001 CAP 8A2	C1408 CAP 14C2	D43 DIODE_SCHOT 45B4	L71 FILTER_4P 24B5		
	C767 CAP 11C6	C884 CAP 11D6	C1002 CAP 8A7	C1409 CAP 14C2	D44 DIODE_SCHOT 45D2	L72 IND 29A7		
	C768 CAP 11B6	C885 CAP 11D7	C1003 CAP 8D1	C1410 CAP 14C2	D45 DIODE_SCHOT 45D6	L73 IND 29A3		
	C773 CAP 51C6	C886 CAP 11D5	C1004 CAP 49B6	C1411 CAP 14C1	D46 DIODE_SCHOT 49B1	L74 FILTER_4P 24C5		
	C774 CAP 11C1	C887 CAP 11D7	C1005 CAP 49C4	C1412 CAP 14C1	D47 DIODE 36D6	L75 IND 29A7		
	C775 CAP 11C3	C888 CAP 11D6	C1006 CAP 45C2	C1413 CAP 14C1	D48 DIODE_DUAL_SWI 42B6	L76 FILTER_4P 24D5		
	C776 CAP 11B2	C889 CAP 11D6	C1007 CAP 8C2	C1414 CAP 14B1	D4901 DIODE_SCHOT 47B2	L77 IND 29B7		
	C777 CAP 11C1	C890 CAP 11D7	C1008 CAP 8B6	C1415 CAP 14C3	DS1 LED 35A2	L78 IND 29A3		
	C778 CAP 22B6	C891 CAP 11D6	C1009 CAP 8A7	C1416 CAP 14C2	DS2 LED 35A1	L79 IND 43B7		
	C779 CAP 11D1	C892 CAP 11C4	C1010 CAP 8B6	C1417 CAP 14C2	DS3 LED 35A1	L80 IND 29B3		
	C780 CAP 11D2	C893 CAP 11C1	C1011 CAP 8B6	C1418 CAP 14C2	DS4 LED 30A3	L81 IND 43A7		
	C781 CAP 11B5	C894 CAP 11C2	C1012 CAP 8A7	C1419 CAP 14C2	DS5 LED 51C8	L82 IND 24D5		
	C782 CAP 11B7	C895 CAP 11D5	C1013 CAP 8D2	C1420 CAP 14C1	DS6 LED 51A6	L83 IND 43A7		
	C783 CAP 44D5	C896 CAP 11D5	C1014 CAP 8D2	C1421 CAP 14C1	DS7 LED 38B2	L84 IND 50D6		
	C784 CAP 28A6	C897 CAP 11D6	C1015 CAP 49B7	C1501 CAP 15D7	DS8 LED 38B6	L85 IND 50D6		
	C785 CAP 28C3	C898 CAP 11D5	C1016 CAP 8C1	C1502 CAP 15D1	DS9 LED 50D5	L86 IND 50D6		
	C786 CAP 27D3	C899 CAP 11D5	C1017 CAP 8A6	C1503 CAP 15D1	DS10 LED 51A4	L87 IND 27D5		
	C787 CAP 11D3	C900 CAP 11D7	C1018 CAP 8A6	C1504 CAP 15D3	D21 ZENER 51B1	L88 IND 27D2		
	C788 CAP 11D3	C901 CAP 11D7	C1019 CAP 49C6	C1505 CAP 15D3	F2 FUSE 36D5	L89 IND 42C7		
	C789 CAP 11D3	C902 CAP 11C7	C1020 CAP 8C1	C1506 CAP 15D3	F3 FUSE 36D5	L90 IND 42C6		
	C790 CAP 11D1	C903 CAP 11B2	C1021 CAP 8C1	C1507 CAP 15D2	FL2 FILTER_LC 25C6	L91 IND 42C6		
	C791 CAP 11B6	C904 CAP 8B7	C1022 CAP 8B7	C1508 CAP 15D2	FL3 FILTER_LC 25C6	L92 IND 42C7		
	C792 CAP 11B6	C908 CAP 13C2	C1023 CAP 8A6	C1509 CAP 15D2	FL4 FILTER_LC 25B6	L93 IND 42C3		
	C793 CAP 11C6	C909 CAP 13C2	C1024 CAP 8A6	C1510 CAP 15D1	C1510 CAP 15D1	L94 IND 42C2		
	C794 CAP 11C5	C910 CAP 8B6	C1025 CAP 8B6	C1511 CAP 15D1	J2 CON_FWVERT_SKT 36C1	L95 IND 42C2		
	C795 CAP 28C3	C911 CAP 37C2	C1026 CAP 8D2	C1512 CAP 15D1	J3 CON_F8RT_S_TH1 42B5	L96 IND 42C3		
	C796 CAP 11C3	C914 CAP 15D2	C1027 CAP 8B7	C1513 CAP 15D1	J4 CON_F4RT_USB_UPRIGHT 33C3	L97 IND 43D7		
	C797 CAP 11D3	C915 CAP 51D7	C1028 CAP 8A6	C1514 CAP 15C3	J5 CON_FWVERT_SKT 36D1	L98 IND 43D7		
	C798 CAP 11D1	C917 CAP 37C5	C1029 CAP 45B4	C1515 CAP 15C3	J6 CON_F4RT_USB_UPRIGHT 33B3	L99 IND 42B3		
	C799 CAP 11D1	C918 CAP 51D6	C1030 CAP 45B2	C1601 CAP 14D7	J7 CON_F4RT_USB_UPRIGHT 33D3	L100 FILTER_4P 33D5		
	C800 CAP 11D2	C919 CAP 8A6	C1031 CAP 8A6	C1602 CAP 14D3	J8 CON_F4RT_S4MT_TH1 41C1	L101 FILTER_4P 33C5		
	C801 CAP 11B7	C920 CAP 38C1	C1032 CAP 8C2	C1702 CAP 15D7	J9 CON_F14RT_D4MT_TH1 25C5	L102 FILTER_4P 33B5		
	C802 CAP 11B5	C921 CAP 38C5	C1033 CAP 8C1	C1801 CAP 16D5	J10 CON_F4RT_S4MT_TH1 40C7	L103 IND 29B3		
	C803 CAP 11B6	C922 CAP 15D2	C1034 CAP 45B2	C1802 CAP 16A7	J11 CON_F184ST_DDRDIMM 15D5	L2401 IND 22C7		
	C805 CAP 11B2	C923 CAP 38B5	C1035 CAP 8A7	C1901 CAP 8A7	J12 CON_F21ST_D2MT_SM 24C4	L2501 IND 23A6		
	C806 CAP 11C2	C924 CAP 38B7	C1036 CAP 4D3	C1902 CAP 8A6	J13 CON_M40SM_635 29D6	LP1 LPAK4P 42A8		
	C807 CAP 11B2	C9						

	8	7	6	5	4	3	2	1
D	Q5 TRA_2N7002 41A5 Q6 TRA_2N7002 42B6 Q7 TRA_SUD70N03 48B4 Q8 TRA_SUD50N03 48B4 Q9 TRA_SUD50N03 48C4 Q10 TRA_2N7002 22B7 Q11 TRA_FDC602P 51C2 Q12 TRA_2N3904 44D7 Q13 TRA_2N7002 51B2 Q14 TRA_2N7002 51C7 Q15 TRA_2N7002 51B7 Q16 TRA_2N7002 51A7 Q17 TRA_2N7002 51B6 Q18 TRA_2N7002 51A7 Q19 TRA_2N7002 51A6 Q20 TRA_FDC602P 51D7 Q21 TRA_2N7002 50C1 Q22 TRA_2N7002 50D1 Q23 TRA_2N7002 50C2 Q24 TRA_2N7002 50C2 Q25 TRA_IRF7807Z 47B5 Q26 TRA_IRF7807Z 47B5 Q27 TRA_SUD70N03 49B4 Q28 TRA_SUD50N03 49B4 Q29 TRA_2N7002 45D8 Q30 TRA_2N3904 36C7 Q31 TRA_2N7002 41A8 Q32 TRA_2N7002 39C7 Q33 TRA_2N3904 43A2 Q34 TRA_SUD70N03 48B4 Q35 TRA_2N7002 40D4 Q36 TRA_2N7002 42D3 Q37 TRA_FDC602P 51C6 Q38 TRA_2N7002 51B6 Q39 TRA_2N3904 50C7 Q40 TRA_SUD70N03 45C4 Q41 TRA_2N7002 15B2 Q42 TRA_2N7002 15C2 Q43 TRA_SUD70N03 45C4 Q44 TRA_SUD50N03 45C4 Q45 TRA_IRF7807Z 50B4 Q46 TRA_IRF7807Z 50C4 Q47 TRA_SUD50N03 50A4 Q48 TRA_SUD50N03 50A4 Q49 TRA_SUD70N03 45B4 Q50 TRA_SUD50N03 45B4 Q51 TRA_SUD70N03 45B4 Q52 TRA_2N7002 15B2 Q53 TRA_2N7002 15A2 Q54 TRA_2N7002 15B2	R61 RES 18A3 R62 RES 18A2 R63 RES 19A4 R64 RES 18D3 R65 RES 19A4 R66 RES 18D2 R67 RES 18D2 R68 RES 19A4 R69 RES 19A4 R70 RES 18D3 R71 RES 18D6 R72 RES 17A5 R73 RES 26C2 R74 RES 26D2 R75 RES 35B8 R76 RES 35B8 R77 RES 26D2 R78 RES 32A7 R79 RES 26C2 R80 RES 17A5 R81 RES 19A7 R82 RES 12A8 R83 RES 18A5 R84 RES 18D2 R85 RES 23D6 R86 RES 23D6 R87 RES 26B3 R88 RES 18A5 R89 RES 32D3 R90 RES 18D3 R91 RES 23D6 R92 RES 23D6 R93 RES 12A8 R94 RES 26A3 R95 RES 18A5 R96 RES 35B1 R97 RES 19A7 R98 RES 23D6 R99 RES 42D8 R100 RES 26A8 R101 RES 19A7 R102 RES 23C5 R103 RES 19B2 R104 RES 30D6 R105 RES 19C2 R106 RES 19C2 R107 RES 23D5 R108 RES 42D7 R109 RES 42D6 R110 RES 42C8 R111 RES 18D2 R112 RES 23D6 R113 RES 23D5 R114 RES 23C5 R115 RES 23C5 R116 RES 26C6 R117 RES 18D3 R118 RES 22D3 R119 RES 23C5 R120 RES 42B8 R121 RES 42C8 R122 RES 19A7 R123 RES 19D2 R124 RES 23C5 R125 RES 26D5 R126 RES 26C6 R127 RES 32B8 R128 RES 32B8 R129 RES 32D3 R130 RES 18D2 R131 RES 18D2 R132 RES 19D2 R133 RES 22D3 R134 RES 26B8 R135 RES 42D7 R136 RES 26C5 R137 RES 12B1 R138 RES 30B3 R139 RES 30B4 R140 RES 19A7 R141 RES 23B5 R142 RES 26C5 R143 RES 18D2 R144 RES 23B2 R145 RES 22D3 R146 RES 19A7 R147 RES 28D7 R148 RES 19C2 R149 RES 23B5 R150 RES 48C5 R151 RES 20A4 R152 RES 20A5 R153 RES 37D7 R154 RES 18D3 R155 RES 18G2 R156 RES 19C2 R157 RES 23B3 R158 RES 19D2 R159 RES 18B7 R160 RES 17A5 R161 RES 16D1 R162 RES 41A5 R163 RES 20A4 R164 RES 18D3 R165 RES 35C4 R166 RES 20A4 R167 RES 19A4 R168 RES 17A7 R169 RES 18C2 R170 RES 22B5	R171 RES 22B6 R172 RES 12A1 R173 RES 22C7 R174 RES 22C6 R175 RES 40D4 R176 RES 20A4 R177 RES 17A5 R178 RES 22C7 R179 RES 22C6 R180 RES 40D5 R181 RES 41A4 R182 RES 41A4 R183 RES 22C3 R184 RES 23C6 R185 RES 22B6 R186 RES 19A6 R187 RES 19A6 R188 RES 22B2 R191 RES 22B6 R192 RES 17A2 R193 RES 17A2 R194 RES 22B6 R195 RES 17A3 R196 RES 17A3 R197 RES 16A7 R198 RES 17C1 R199 RES 22D6 R200 RES 22D6 R203 RES 16C8 R204 RES 16B8 R205 RES 16A8 R206 RES 22D6 R207 RES 23D2 R208 RES 23D2 R210 RES 17B7 R211 RES 16C7 R212 RES 30B3 R213 RES 23C5 R214 RES 12B1 R215 RES 26A8 R216 RES 16B7 R217 RES 16D3 R218 RES 30D6 R219 RES 30D6 R220 RES 16C7 R221 RES 16C7 R222 RES 16D3 R223 RES 50B5 R224 RES 16C7 R225 RES 16D7 R226 RES 16C7 R227 RES 30C6 R228 RES 28C7 R230 RES 30C6 R231 RES 28C8 R232 RES 28C6 R233 RES 16B3 R234 RES 16D1 R235 RES 12A1 R236 RES 26D2 R237 RES 23C5 R238 RES 27A6 R239 RES 42B7 R240 RES 42B6 R241 RES 27A5 R242 RES 42B5 R243 RES 27A6 R244 RES 27A5 R245 RES 42B5 R246 RES 23B7 R247 RES 9C5 R248 RES 45B3 R249 RES 42B5 R250 RES 27A7 R251 RES 51B3 R252 RES 30C7 R253 RES 30C8 R254 RES 34B4 R255 RES 30C7 R256 RES 30C8 R257 RES 34B3 R258 RES 37D5 R259 RES 30C7 R261 RES 12D5 R262 RES 30C8 R263 RES 37D4 R264 RES 28D1 R265 RES 38B4 R266 RES 37C6 R267 RES 28B1 R268 RES 38B4 R269 RES 34C4 R270 RES 37D2 R271 RES 28B3 R272 RES 28B3 R273 RES 37D2 R274 RES 28C7 R275 RES 34C4 R276 RES 45B8 R277 RES 51B2 R278 RES 35C1 R279 RES 51B2 R280 RES 44D8 R281 RES 44D7 R282 RES 35C2 R283 RES 51B3 R284 RES 51A3 R285 RES 31C1 R286 RES 12A3 R287 RES 44D5	R288 RES 12D5 R289 RES 28C8 R290 RES 44D8 R291 RES 28C7 R292 RES 28C7 R293 RES 31C5 R294 RES 28C1 R295 RES 47B3 R296 RES 47B7 R297 RES 44D6 R298 RES 44B1 R299 RES 47B3 R300 RES 51C8 R301 RES 47B5 R302 RES 51A6 R303 RES 34D7 R304 RES 47B3 R305 RES 28A2 R306 RES 28B8 R307 RES 34D7 R308 RES 8B1 R309 RES 47B7 R310 RES 9A4 R311 RES 4B8 R312 RES 4A8 R313 RES 8A2 R314 RES 8B2 R315 RES 47B3 R316 RES 47B7 R317 RES 47C5 R318 RES 47C6 R319 RES 9A5 R320 RES 47B5 R321 RES 9A5 R322 RES 50C2 R323 RES 50D2 R324 RES 42B5 R326 RES 35B3 R328 RES 29D3 R329 RES 29D3 R330 RES 50A8 R331 RES 50B8 R332 RES 50A7 R333 RES 28A2 R334 RES 49B1 R335 RES 50A5 R336 RES 50C5 R337 RES 50B5 R338 RES 9C5 R339 RES 9D5 R340 RES 9D5 R341 RES 9C5 R342 RES 9A7 R343 RES 9C6 R344 RES 9A7 R345 RES 9A7 R346 RES 7C7 R347 RES 7C7 R348 RES 7C7 R349 RES 7B7 R350 RES 7A7 R351 RES 9C5 R352 RES 9C6 R353 RES 9A7 R354 RES 9C6 R355 RES 9C7 R356 RES 6C4 R357 RES 6C4 R358 RES 9C6 R359 RES 9C7 R360 RES 32B3 R361 RES 45B3 R362 RES 45B3 R363 RES 6C5 R364 RES 6C5 R365 RES 6C4 R366 RES 9C7 R367 RES 6C4 R368 RES 6C4 R369 RES 49C6 R370 RES 50D5 R371 RES 49B6 R372 RES 46D4 R373 RES 32A7 R374 RES 6C6 R375 RES 6C6 R376 RES 6C7 R377 RES 6C7 R378 RES 6C7 R379 RES 6C8 R380 RES 6C7 R381 RES 6C8 R382 RES 6C8 R383 RES 6C8 R384 RES 45C5 R385 RES 46D6 R386 RES 45B5 R387 RES 45B5 R388 RES 45B8 R389 RES 45B6 R390 RES 35C1 R391 RES 35C2 R392 RES 41B3 R393 RES 35C2 R394 RES 35C2 R395 RES 36B7 R396 RES 28A2 R397 RES 26A7 R398 RES 28A2 R399 RES 26B7	R400 RES 41C4 R401 RES 26B6 R402 RES 36B4 R403 RES 19B2 R404 RES 36B8 R405 RES 15C2 R406 RES 41D4 R407 RES 40C5 R408 RES 26A6 R409 RES 36D7 R410 RES 40B5 R411 RES 36C5 R412 RES 41C4 R413 RES 47B5 R414 RES 43B5 R415 RES 19A3 R416 RES 19A3 R417 RES 36C7 R418 RES 36C6 R419 RES 41D4 R420 RES 40B4 R421 RES 43B5 R422 RES 36C6 R423 RES 48B8 R424 RES 36C7 R425 RES 36C7 R426 RES 26A5 R427 RES 26B5 R428 RES 19A3 R429 RES 19A3 R430 RES 36B6 R431 RES 36C7 R432 RES 36C7 R433 RES 36B6 R434 RES 40C4 R435 RES 40C4 R436 RES 40B4 R437 RES 40B4 R438 RES 36C8 R439 RES 26B4 R440 RES 41A8 R441 RES 41D6 R442 RES 40C4 R443 RES 26A4 R444 RES 41C6 R445 RES 26B6 R446 RES 41C6 R447 RES 41D6 R448 RES 40B3 R449 RES 26A6 R450 RES 33B5 R451 RES 33B6 R452 RES 33C5 R453 RES 33C6 R454 RES 33D5 R455 RES 33D6 R456 RES 33B7 R457 RES 33B7 R458 RES 33C7 R459 RES 33C7 R460 RES 33D7 R461 RES 33D7 R462 RES 40B3 R463 RES 18B8 R464 RES 32C3 R465 RES 32C3 R466 RES 32C2 R467 RES 32C2 R468 RES 32C3 R469 RES 32B3 R470 RES 28D6 R471 RES 15C1 R472 RES 41C7 R473 RES 41D7 R474 RES 40C3 R475 RES 48B1 R476 RES 32B3 R477 RES 41B4 R478 RES 41B5 R479 RES 19A6 R480 RES 32B2 R481 RES 39B6 R482 RES 39C6 R483 RES 19A6 R484 RES 32A7 R485 RES 35B2 R486 RES 35B2 R487 RES 43A6 R488 RES 43B4 R489 RES 43B4 R490 RES 32B3 R491 RES 35B2 R492 RES 35B1 R493 RES 28A2 R494 RES 32B7 R495 RES 15B1 R496 RES 48C2 R497 RES 15C1 R498 RES 43A2 R499 RES 48C3 R500 RES 19C2 R501 RES 39C7 R502 RES 39D7 R503 RES 48B6 R504 RES 48C3 R505 RES 48C5 R506 RES 19A7 R507 RES 15B1 R508 RES 15B3 R509 RES 19A7	R510 RES 30B4 R511 RES 30A3 R512 RES 15C3 R515 RES 48B5 R517 RES 39C5 R518 RES 46B5 R519 RES 46A5 R521 RES 39C5 R522 RES 42B8 R523 RES 48C6 R524 RES 39B4 R525 RES 42B7 R526 RES 42B7 R527 RES 48B7 R528 RES 39C4 R529 RES 48C6 R531 RES 39C2 R532 RES 39C4 R533 RES 26D6 R535 RES 26D1 R537 RES 27A5 R540 RES 24A6 R541 RES 26A6 R542 RES 26B6 R543 RES 26B1 R545 RES 16A7 R546 RES 24A5 R547 RES 24A6 R548 RES 27A7 R549 RES 16A8 R550 RES 24A5 R551 RES 24A5 R552 RES 26C8 R553 RES 26D8 R554 RES 22C7 R555 RES 22C3 R556 RES 22C2 R557 RES 22C2 R558 RES 17B7 R559 RES 24B6 R560 RES 26C7 R561 RES 26D7 R562 RES 22C6 R563 RES 27A6 R564 RES 23C5 R565 RES 28D5 R566 RES 30D6 R567 RES 24B5 R568 RES 24B5 R569 RES 22B3 R570 RES 24B6 R571 RES 28D8 R572 RES 28D8 R573 RES 28C6 R574 RES 28C8 R575 RES 34C1 R576 RES 34C1 R577 RES 28C7 R578 RES 30B6 R579 RES 30A8 R580 RES 16B3 R581 RES 24B5 R582 RES 24C5 R583 RES 26D3 R584 RES 29D7 R585 RES 29D6 R586 RES 28D6 R587 RES 16B3 R588 RES 26C3 R589 RES 28A8 R590 RES 28D6 R591 RES 24C6 R592 RES 23A7 R593 RES 24C5 R594 RES 24D5 R595 RES 26D5 R596 RES 26D5 R597 RES 26C3 R598 RES 26D3 R599 RES 22A8 R600 RES 24D6 R601 RES 51B3 R602 RES 23A7 R603 RES 24D5 R604 RES 44A3 R605 RES 44A5 R606 RES 44A5 R607 RES 51A5 R608 RES 51A5 R609 RES 51A5 R610 RES 46B4 R611 RES 28A6 R612 RES 51A5 R613 RES 29A8 R614 RES 51A5 R615 RES 51A6 R616 RES 30D8 R617 RES 30D8 R618 RES 30C7 R619 RES 16D6 R620 RES 28D5 R621 RES 30C6 R622 RES 16C6 R623 RES 37D5 R624 RES 37A7 R625 RES 12B6 R626 RES 37D2 R627 RES 37D5 R628 RES 28D5 R629 RES 28D5	A	
B								
C								
D								

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RES 50C8 R722 RES 44C6 R723 RES 28B2 R724 RES 28C2 R725 RES 44B6 R726 RES 44B7 R727 RES 28C7 R728 RES 28B2 R729 RES 44C6 R730 RES 44C7 R731 RES 44B6 R732 RES 44C7 R733 RES 44B7 R734 RES 44B7 R735 RES 28C7 R736 RES 28B2 R737 RES 38C7 R738 RES 51B7 R739 RES 51C7 R740 RES 51A7 R741 RES 51A7 R742 RES 28B2 R743 RES 38C4 R744 RES 38C7 R745 RES 38C3 R746 RES 28B8 R747 RES 28B6 R749 RES 28B8 R750 RES 28B8 R751 RES 51A8 R752 RES 9A4 R753 RES 37C2 R754 RES 37D4 R755 RES 28D2 R756 RES 28D2 R757 RES 9A4 R758 RES 38C1 R759 RES 37D2 R760 RES 38C5	R761 RES 37C5 R762 RES 28B3 R763 RES 28B3 R764 RES 9B4 R765 RES 9A4 R766 RES 9A4 R767 RES 37D2 R768 RES 37D5 R769 RES 51D7 R770 RES 51D7 R772 RES 9A4 R773 RES 37D2 R774 RES 37D4 R775 RES 38C7 R776 RES 9A5 R777 RES 9A4 R779 RES 38C1 R780 RES 38C3 R781 RES 38C5 R782 RES 38C7 R783 RES 38C4 R784 RES 38C7 R785 RES 38C4 R786 RES 38A4 R787 RES 38B4 R788 RES 16D6 R789 RES 38B7 R790 RES 29C2 R791 RES 38B7 R792 RES 38B2 R793 RES 38B6 R794 RES 34B4 R795 RES 50A6 R796 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R894 RES 9C7 R895 RES 4D2	R896 RES 45C5 R897 RES 45B5 R898 RES 45B5 R899 RES 45D6 R900 RES 45C5 R901 RES 4D3 R902 RES 7A3 R903 RES 7A3 R904 RES 7A3 R905 RES 7A3 R906 RES 7B3 R907 RES 7B3 R908 RES 45D6 R909 RES 7B5 R910 RES 7B5 R911 RES 7B5 R912 RES 7A5 R913 RES 7C3 R914 RES 7C3 R915 RES 7C3 R916 RES 7B3 R917 RES 38C7 R918 RES 7C3 R919 RES 7B3 R920 RES 7B3 R921 RES 7B5 R922 RES 7A5 R923 RES 7A5 R924 RES 7D5 R925 RES 7C5 R926 RES 7C3 R927 RES 46C5 R928 RES 45C5 R929 RES 15A8 R930 RES 46C5 R931 RES 45D7 R932 RES 45C7 R933 RES 45D7 R934 RES 45C8 R935 RES 45A5 R936 RES 45A5 R937 RES 8A4 R939 RES 8A4 R940 RES 8A4 R941 RES 37D1 R942 RES 22A7 R943 RES 37D2 R944 RES 37D2 R945 RES 46B7 R946 RES 23D2 R947 RES 23D2 R948 RES 31D4 R949 RES 31D4 R950 RES 31D3 R951 RES 31C2 R952 RES 22A7 R953 RES 22A7 R954 RES 22A6 R955 RES 22A6 R956 RES 33A8 R957 RES 22A6 R958 RES 46B5 R959 RES 46C5 R960 RES 23D2 R961 RES 23D1 R962 RES 23D1 R963 RES 23D1 R964 RES 23D1 R965 RES 27B5 R966 RES 27B4 R967 RES 27C5 R968 RES 27B4 R969 RES 27C4 R970 RES 27B4 R971 RES 27C4 R972 RES 27C3 R973 RES 27C2 R974 RES 27C2 R975 RES 45B3 R976 RES 27C2 R977 RES 27C2 R978 RES 27C2 R979 RES 27C2 R980 RES 27C2 R981 RES 46D6 R982 RES 42D6 R983 RES 42C4 R984 RES 42D4 R985 RES 50B3 R986 RES 43D5 R987 RES 50A1 R988 RES 43C2 R989 RES 43D2 R990 RES 23D2 R991 RES 23D2 R992 RES 23C1 R993 RES 23C1 R994 RES 23C1 R995 RES 23C1 R996 RES 23C1 R997 RES 23C1 R998 RES 23C1 R999 RES 23C1 R1000 RES 27B5 R1001 RES 19B7 R1002 RES 27B4 R1003 RES 27B3 R1004 RES 27B3 R1005 RES 28C1 R1006 RES 33D5	R1007 RES 33D5 R1008 RES 33C5 R1009 RES 33C5 R1010 RES 33B5 R1011 RES 33B5 R1012 RES 51B2 R1013 RES 50D7 R1014 RES 50C8 R1015 RES 29B4 R1016 RES 26D7 R1017 RES 51C3 R1018 RES 51C2 R1019 RES 23C6 R1020 RES 23C6 R1021 RES 26A1 R1022 RES 26C2 R1023 RES 26C1 R1024 RES 17A4 R1025 RES 17A4 R1026 RES 17C7 R1027 RES 17C7 R1028 RES 32A7 R1029 RES 32A7 R1401 RES 14B2 R1402 RES 14A2 R1403 RES 14D7 R1404 RES 14C7 R1405 RES 14C7 R1406 RES 14C7 R1407 RES 14C7 R1408 RES 14C7 R1409 RES 14C7 R1410 RES 14C7 R1411 RES 14B7 R1412 RES 14B7 R1413 RES 14B7 R1414 RES 14B7 R1415 RES 14B7 R1416 RES 14B7 R1417 RES 14B7 R1418 RES 14A7 R1503 RES 15C7 R1504 RES 15C7 R1505 RES 15C7 R1506 RES 15C7 R1507 RES 15C7 R1508 RES 15C7 R1509 RES 15C7 R1510 RES 15B7 R1511 RES 15B7 R1512 RES 15B7 R1513 RES 15B7 R1514 RES 15B7 R1515 RES 15B7 R1516 RES 15B7 R1517 RES 15A7 R1518 RES 15A7 R3001 RES 28A2 R3002 RES 28A2 R3501 RES 33A8 R4201 RES 42B6 R4202 RES 42C5 R4203 RES 42B5 R4301 RES 41A7 R4401 RES 43C7 R4501 RES 43B6 R4502 RES 43A6 R4503 RES 43A6 R4504 RES 43B6 R4505 RES 43A6 R4506 RES 43A6 R4507 RES 43C5 R4508 RES 45B3 R4509 RES 45C4 R4701 RES 45B6 R4702 RES 45B6 R4703 RES 45B3 R4704 RES 45B3 R4705 RES 45B3 R4706 RES 45D3 R4707 RES 45D3 R4708 RES 45C5 R4709 RES 45B6 R4710 RES 47B4 R4801 RES 46C7 R4802 RES 48B4 R4803 RES 48C3 R4901 RES 49B4 R5001 RES 50B3 R5002 RES 50A3 R5003 RES 50A1 R5301 RES 51C1 RP1 RPAK4P 36C7 RP2 RPAK4P 36C7 RP3 RPAK4P 36B7 RP4 RPAK4P 19B5 19C5 RP5 RPAK4P 19B5 RP6 RPAK4P 19B5 RP7 RPAK4P 19B5 RP8 RPAK4P 18A2 18A3 18B2 RP9 RPAK4P 18C2 18C3 18C3 RP10 RPAK4P 19C7 RP11 RPAK4P 19B7 RP12 RPAK4P 19B7 RP13 RPAK4P 19B7 RP14 RPAK4P 19C7 RP15 RPAK4P 35B7 35B8 RP16 RPAK4P 35A4 RP17 RPAK4P 19C7 RP18 RPAK4P 19D7	RP19 RPAK4P 19D7 RP20 RPAK4P 19D7 RP21 RPAK4P 19D7 RP22 RPAK4P 18F2 18F3 18G2 18G3 RP23 RPAK4P 18E2 18E3 RP24 RPAK4P 19C5 RP25 RPAK4P 19C5 19D5 RP26 RPAK4P 19C5 RP27 RPAK4P 19D5 RP28 RPAK4P 19D5 RP29 RPAK4P 19D5 RP30 RPAK4P 17D7 RP31 RPAK4P 17C7 RP32 RPAK4P 17D7 RP33 RPAK4P 17B7 RP34 RPAK4P 17C7 RP35 RPAK4P 17B7 RP36 RPAK4P 17A7 RP37 RPAK4P 16B3 16C3 16C3 RP38 RPAK4P 30B6 RP39 RPAK4P 28D2 RP40 RPAK4P 17D7 RP41 RPAK4P 17C7 RP42 RPAK4P 17D7 RP43 RPAK4P 17C7 RP44 RPAK4P 17C7 RP45 RPAK4P 17B7 RP46 RPAK4P 17B7 RP47 RPAK4P 17A7 RP48 RPAK4P 16B3 RP49 RPAK4P 16B1 16B1 16C1 RP50 RPAK4P 16B1 RP51 RPAK4P 12C2 RP52 RPAK4P 12B2 RP54 RPAK4P 31B7 RP56 RPAK4P 31B7 RP58 RPAK4P 31C7 RP59 RPAK4P 31C7 RP60 RPAK4P 28C2 RP61 RPAK4P 31B7 RP62 RPAK4P 12C3 RP63 RPAK4P 30B8 RP64 RPAK4P 31B7 RP65 RPAK4P 34C4 RP66 RPAK4P 28A8 RP67 RPAK4P 31B7 RP68 RPAK4P 12D3 RP69 RPAK4P 30B8 RP70 RPAK4P 34C7 RP71 RPAK4P 12C2 12D2 RP72 RPAK4P 31C7 RP73 RPAK4P 31C7 RP74 RPAK4P 28A8 RP75 RPAK4P 31C7 RP76 RPAK4P 34C7 RP77 RPAK4P 31C7 RP78 RPAK4P 7A7 RP79 RPAK4P 7A5 7C5 RP80 RPAK4P 19B7 RP81 RPAK4P 19B7 RP82 RPAK4P 19B7 19C7 19C7 RP83 RPAK4P 18B2 18B3 18B3 RP84 RPAK4P 18B2 18B2 18B3 RP85 RPAK4P 18B3 18C2 18C3 RP86 RPAK4P 19B5 RP87 RPAK4P 19B5 RP88 RPAK4P 19C5 RP89 RPAK4P 19C5 RP90 RPAK4P 19C7 RP91 RPAK4P 19C7 19D7 RP92 RPAK4P 19C7 39B7 RP93 RPAK4P 18E2 18E3 RP94 RPAK4P 18F2 18F3 RP95 RPAK4P 18F2 18F2 18F3 RP96 RPAK4P 19C5 RP97 RPAK4P 19D5 RP98 RPAK4P 16B3 16C3 16C3 RP99 RPAK4P 16C1 RP100 RPAK4P 28A8 RP101 RPAK4P 34B1 RP102 RPAK4P 12C5 RP103 RPAK4P 37B2 37C2 RP104 RPAK4P 37B4 37B5 37C5 RP105 RPAK4P 12B5 RP106 RPAK4P 37B2 RP107 RPAK4P 37B4 37B4 37B5 RP108 RPAK4P 28A3 28B3 RP109 RPAK4P 12A5 RP110 RPAK4P 28A8 RP111 RPAK4P 37C4 37C4 37C5 37C5 RP112 RPAK4P 34C4 RP113 RPAK4P 28B8 RP114 RPAK4P 37A2 37B2 RP115 RPAK4P 37A4 37A5 37B4 37B5 RP116 RPAK4P 12B3 RP117 RPAK4P 28C1 28C1 28D1 28D1 RP118 RPAK4P 37C2 RP119 RPAK4P 34C1 S1 SWI_TACT_2P1 44B2 S2 SWI_TACT 44A3 SP1 SPRING_CLIP_1P_EMI 10D2 SP2 SPRING_CLIP_1P_EMI 10D2 SP3 SPRING_CLIP_1P_EMI 10D1 SP4 SPRING_CLIP_1P_EMI 10D1 T1 XFR_100B7_MDIX 35C3 U1 S1L1162 27C3 U2 SHNTRREG_TLV431A 21A4 U3 VREG_LP2951 36D7 U4 FW802A 36C5 U5 SDRAM_DDR_4MX32 21D6 21D7	U6 OPAMP_TLV2362 40B3 40C3 U7 SHNTRREG_TLV431A 18B8 U8 AMP_TPA6112A2 41D5 U9 CLK_GEN_CY25811 22A7 U10 NC7WZ08 42C7 U11 UPD720101_FPGA 32C5 U12 SDRAM_DDR_4MX32 20D6 20D7 U13 OPAMP_TLV2362 43A3 43A4 U14 VREG_LP2951 39D6 U15 TAS3004 39C3 U16 SHNTRREG_TLV431A 20A5 U18 CBTV4020 13D7 U19 VREG_LT1962 28D7 U20 VREG_LM1117 23A8 U21 VDET_MC33465N_22ATR 44A4 U22 CBTV4020 13B6 U23 MAX6328 44A5 U25 INTREPID 9D2 10D5 10D7 12D7 16D5 28C4 30D5 U26 M16C62 44C5 U27 CBTV4020 13D4 U28 VREG_TL431 44D8 U29 CBTV4020 13C2 U31 DCDC_SC2602 47B6 U33 LTC3707 50B6 U34 APOLLO_MPC7445_360 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